

10117B,F: -30 to +85°C

DIGITAL 10,000 SERIES ECL

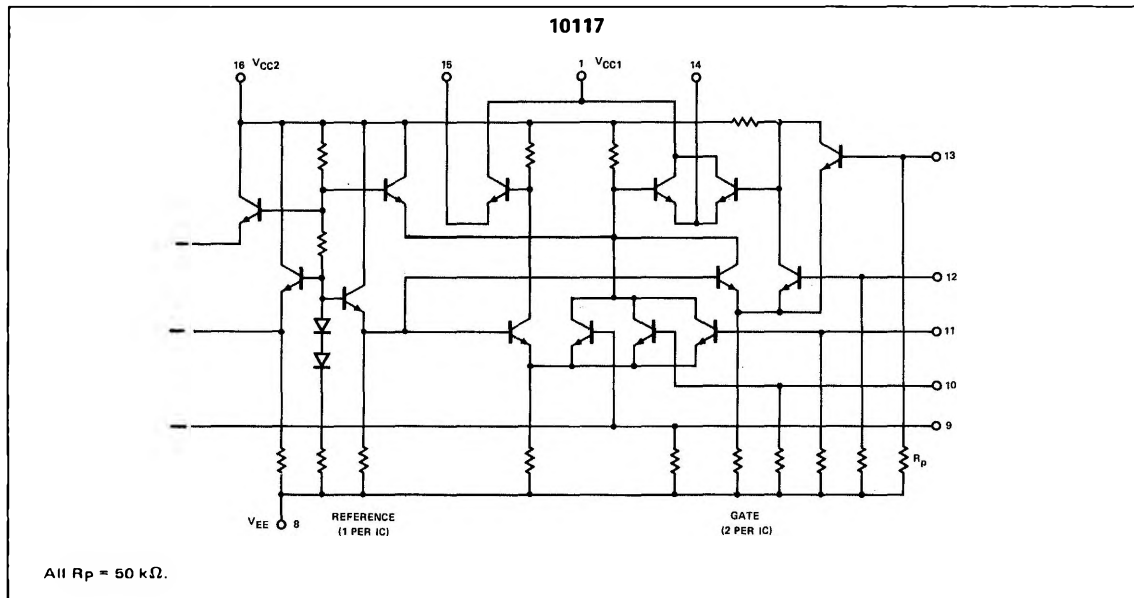
DESCRIPTION

The 10117 package contains two 2 input/3 input OR-AND/OR-AND INVERT complex gates. Pin 9 is common to both gates. This function is particularly useful in data control, multiplexing and distribution. The 10117 is optimized for high performance applications and has an excellent speed power product. All inputs are terminated with a 50 kΩ resistor to V_{EE} which eliminates the need to tie unused inputs low. The high impedance inputs and high output fanout is ideal for a transmission line environment.

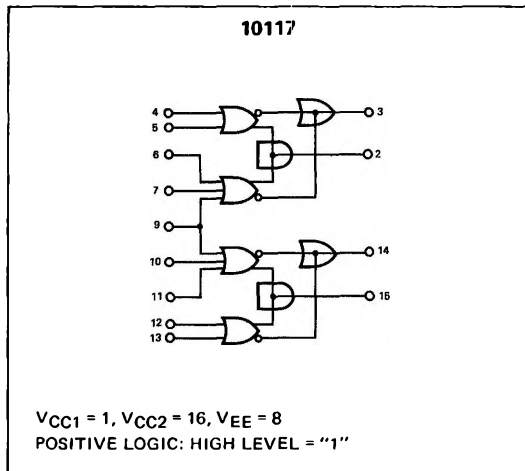
FEATURES

- FAST PROPAGATION DELAY FOR TWO LOGIC LEVELS = 2.3 ns TYP
- POWER DISSIPATION = 100 mW/PACKAGE TYP (NO LOAD)
- VERY HIGH FANOUT CAPABILITY
- CAN DRIVE 50 Ω LINES
- HIGH Z INPUTS - 50 kΩ PULLDOWNS
- HIGH IMMUNITY FROM POWER SUPPLY VARIATIONS: V_{EE} = -5.2 V ±5% RECOMMENDED
- OPEN EMITTERS FOR BUSSING AND LOGIC CAPABILITY
- OUTPUTS MAY BE CROSS COUPLED BACK TO INPUTS TO MAKE A LATCH FUNCTION

CIRCUIT SCHEMATIC



LOGIC DIAGRAM



TEMPERATURE RANGE

- -30 to +85

PACKAGE TYPE

- B: 16 Pin Silicone DIP
- F: 16 Pin CERDIP

