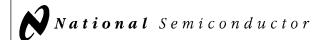
54125,DM54125,DM74125

54125 DM54125 DM74125 Quad TRI-STATE(RM) Buffers



Literature Number: SNOS233A



54125/DM54125/DM74125 Quad TRI-STATE® Buffers

General Description

This device contains four independent gates each of which performs a non-inverting buffer function. The outputs have the TRI-STATE feature. When enabled, the outputs exhibit the low impedance characteristics of a standard TTL output with additional drive capability at the high Logic level to permit the driving of bus lines without external pull-up resistors. When disabled, both the output transistors are turned off presenting a high-impedance state to the bus line. Thus the output will act neither as a significant load nor as a driver.

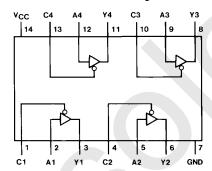
To minimize the possibility that two outputs will attempt to take a common bus to opposite logic levels, the disable time is shorter than the enable time of the outputs.

Features

 Alternate Military/Aerospace device (54125) is available. Contact a National Semiconductor Sales Office/ Distributor for specifications.

Connection Diagram

Dual-In-Line Package



TL/F/6540-1

Order Number 54125DMQB, 54125FMQB, DM54125J, DM54125W or DM74125N See NS Package Number J14A, N14A or W14B

Function Table

Y = A							
Inp	uts	Output					
A	С	Υ					
L	L	L					
Н	L	Н					
Х	Н	Hi-Z					

H = High Logic Level

L = Low Logic Level

X = Either Low or High Logic Level

Hi-Z = TRI-STATE (Outputs are disabled)

TRI-STATE® is a registered trademark of National Semiconductor Corporation

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage 7V
Input Voltage 5.5V
Operating Free Air Temperature Range

Storage Temperature Range -65°C to $+150^{\circ}\text{C}$

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter	DM54125			DM74125			Units
	Turumeter	Min	Nom	Max	Min	Nom	Max	
V _{CC}	Supply Voltage	4.5	5	5.5	4.75	5	5.25	V
V_{IH}	High Level Input Voltage	2			2			V
V _{IL}	Low Level Input Voltage			0.8			0.8	V
loh	High Level Output Current			-2			-5.2	mA
l _{OL}	Low Level Output Current			16			16	mA
T _A	Free Air Operating Temperature	-55		125	0		70	°C

Electrical Characteristics

over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Conditions		Min	Typ (Note 1)	Max	Units
VI	Input Clamp Voltage	$V_{CC} = Min, I_I = -12 \text{ mA}$				-1.5	٧
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH} = Max$ $V_{IL} = Max, V_{IH} = Min$		2.4	3.3		V
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL} = Max$ $V_{IH} = Min, V_{IL} = Max$			0.2	0.4	٧
I _I	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I = 5.5V$				1	mA
l _{IH}	High Level Input Current	$V_{CC} = Max, V_I = 2.4V$				40	μΑ
I _{IL}	Low Level Input Current	$V_{CC} = Max, V_I = 0.4V$	47			-1.6	mA
l _{IZL}	Off-State Input Current with Low Level Input Voltage Applied	$V_{CC} = Max, V_I = 0.4V$				-40	μΑ
l _{OZH}	Off-State Output Current with High Level Output Voltage Applied	$V_{CC} = Max, V_O = 2.4V$ $V_{IH} = Min, V_{IL} = Max$				40	μΑ
l _{OZL}	Off-State Output Current with Low Level Output Voltage Applied	$V_{CC} = Max, V_O = 0.4V$ $V_{IH} = Min, V_{IL} = Max$				-40	μΑ
los	Short Circuit	V _{CC} = Max	DM54	-30		-70	mA
	Output Current	(Note 2)	DM74	-28		-70	, (
Icc	Supply Current	V _{CC} = Max (Note 3)			36	54	mA

Note 1: All typicals are at $V_{CC}=5V$, $T_A=25^{\circ}C$.

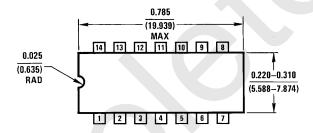
Note 2: Not more than one output should be shorted at a time.

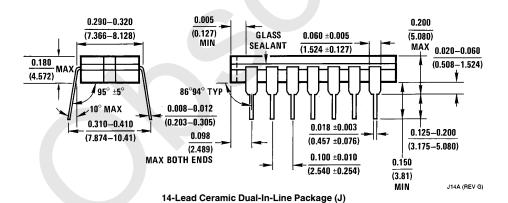
 $\textbf{Note 3: } I_{\text{CC}} \text{ is measured with the output control (C) inputs at 4.5V, the data inputs grounded, and the outputs open.}$

 $\textbf{Switching Characteristics} \text{ at V}_{CC} = 5 \text{V and T}_{A} = 25^{\circ}\text{C (See Section 1 for Test Waveforms and Output Load)}$

	Parameter					
Symbol		C _L = 5 pF		$C_L = 50 pF$		Units
		Min	Max	Min	Max	
t _{PLH}	Propagation Delay Time Low to High Level Output				15	ns
t _{PHL}	Propagation Delay Time High to Low Level Output				18	ns
t _{PZH}	Output Enable Time to High Level Output				18	ns
t _{PZL}	Output Enable Time to Low Level Output				25	ns
t _{PHZ}	Output Disable Time from High Level Output		8			ns
t _{PLZ}	Output Disable Time from Low Level Output		14			ns

Physical Dimensions inches (millimeters)

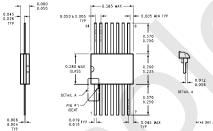




Order Number 54125DMQB or DM54125J NS Package Number J14A

Physical Dimensions inches (millimeters) (Continued) 14 13 12 11 10 9 8 INDEX 0.092 (2.337) DIA 0.030 MAX (0.762) DEPTH 0.135 ± 0.005 (3.429 ± 0.127) 0.300 - 0.320 (7.620 - 8.128) 0.065 (1.651) 0.145 - 0.200 (3.683 - 5.080 $\frac{0.125 - 0.150}{(3.175 - 3.810)}$ 0.075 ±0.015 (1.905 ±0.381) 0.014-0.023 (0.356-0.584) TYP 0.050 ± 0.010 (1.270 - 0.254) TYP 0.325 + 0.04 (8.255 +1.016)

14-Lead Molded Dual-In-Line Package (N) Order Number DM74125N NS Package Number N14A



14-Lead Ceramic Flat Package (W) Order Number 54125FMQB or DM54125W NS Package Number W14B

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