Reversible motor driver
BA6209/BA6209N

The BA6209 and BA6209N are reversible-motor drivers suitable for brush motors. Two logic inputs allow four output modes : forward, reverse, idling, and braking. The motor revolving speed can be set arbitrarily by controlling the voltage applied to the motor with the control pin voltage V_R

●Applications
VCRs and cassette tape recorders

●Features
1) Power transistors can handle a large current (1.6A maximally).
2) Brake is applied when stopping the motor.
3) Built-in function to absorb rush currents generated by reversing and braking.
4) Motor speed controlling pin.

5) Small standby current. ($V_{CC}=12V$, $I_0=5.5mA$ typically)
6) Stable operation during mode changes either from forward to reverse or vice versa.
7) Interface with CMOS devices.

●Block diagram

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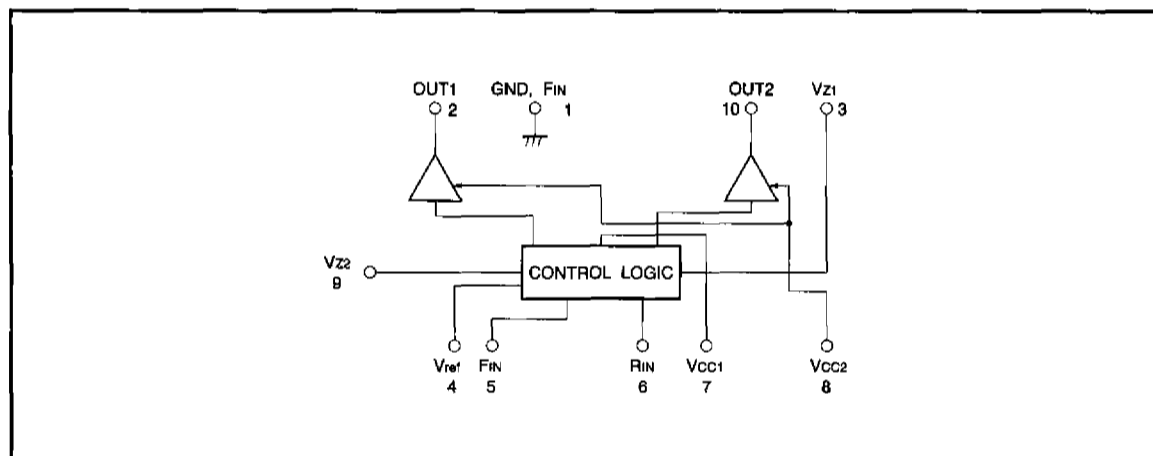
● Absolute maximum ratings (Ta=25℃)

Parameter		Symbol	Limits	Unit
Power supply voltage		Vcc	18	V
Power dissipation	BA6209	Pd	2200*1	mW
	BA6209N		1000*2	
Output current		Io	1.6*3	A
Input voltage		VIN	−0.3～Vcc	V
Operating temperature		Topr	−20～75	℃
Storage temperature		Tstg	−55～125	℃

*1 Reduce power by 22 mW for each degree above 25℃.

*2 Reduce power by 10 mW for each degree above 25℃.

*3 500 μs pulse with a duty ratio of 1%.

● Recommended operating conditions (Ta=25℃)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Operating power supply voltage 1 (Logic section)	Vcc1	6.0	—	18.0	V
Supply voltage 2 (output section)	Vcc2	—	—	18.0	V

● Electrical characteristics (unless otherwise noted, Ta=25℃ and Vcc=12V)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Current consumption	Icc	—	5.5	10	mA	FIN=RIN=GND, RL=∞
Minimum input ON current	IIN	—	10	50	μA	RL=∞
Input threshold voltage	VTH	0.7	1.2	2.0	V	RL=∞
Output leakage current	IOL	—	—	1.0	mA	FIN=RIN=GND, RL=∞
Output voltage	VO	6.6	7.2	—	V	RL=60Ω, ZD=7.4V

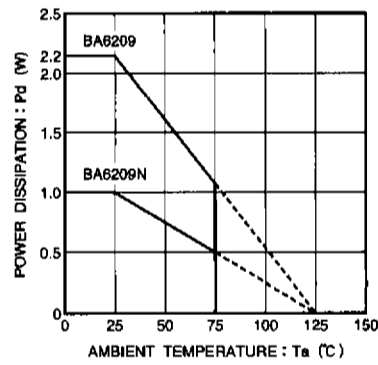● Electrical characteristic curves

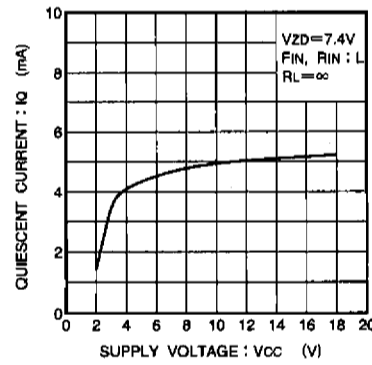Fig.1 Temperature dependence power dissipation curves

Fig.2 Quiescent current vs. power supply voltage
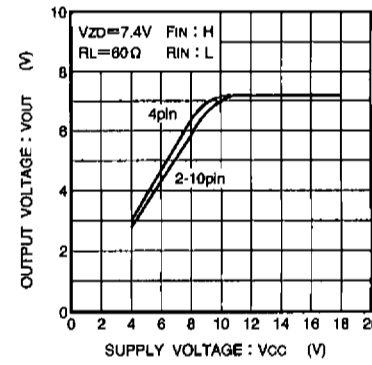

Fig.3 Maximum output voltage vs. power supply voltage (I)
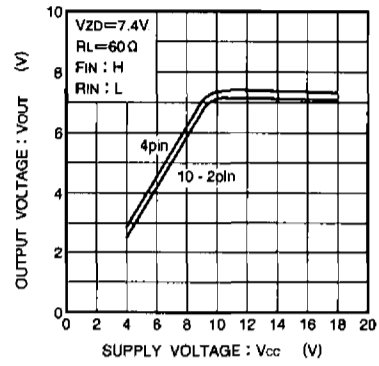
● Electrical characteristic curves

Fig.4   Maximum output voltage vs.
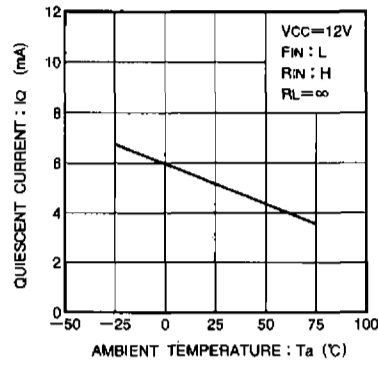   power supply voltage (II)

Fig.5   Quiescent current vs. ambient
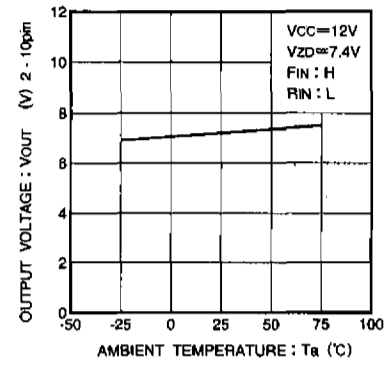   temperature

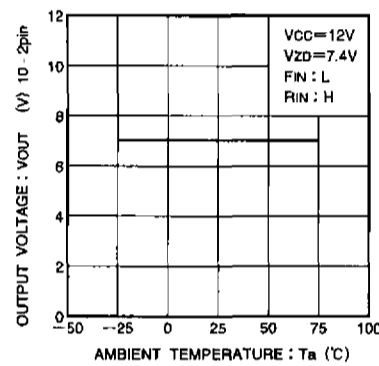Fig.6   Output voltage vs. ambient
        temperature (I)

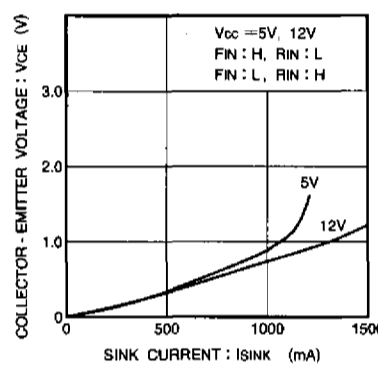Fig.7   Output voltage vs. ambient
        temperature (II)

Fig.8   Output saturated voltage vs.
        sink current (I)
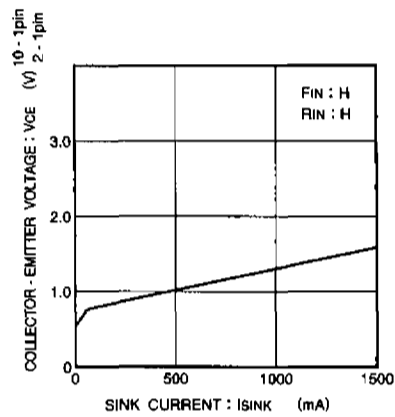
Fig.9   Output saturated voltage vs.
        sink current (II)
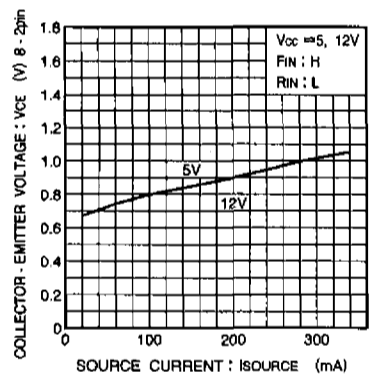
Fig.10 Output saturated voltage vs.
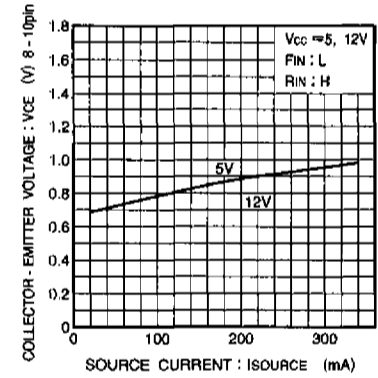   source current (I)

Fig.11 Output saturated voltage vs.
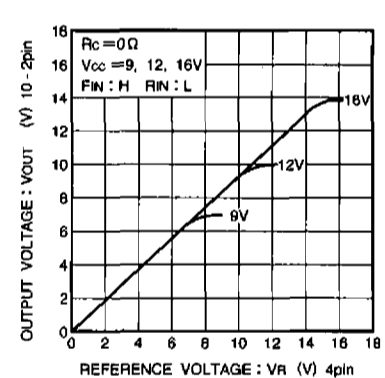   source current (II)

Fig.12 Output voltage vs. reference
        voltage (I)

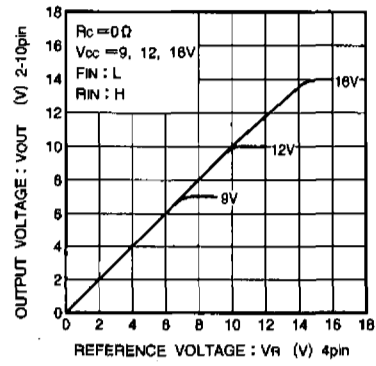**ROHM**

●Electrical characteristic curves

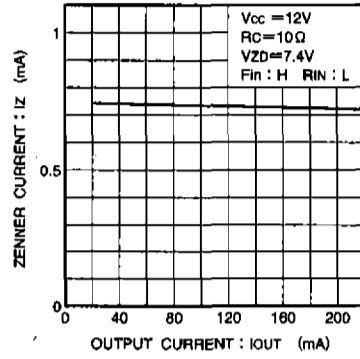Fig.13 Output voltage vs. reference voltage (Ⅱ)
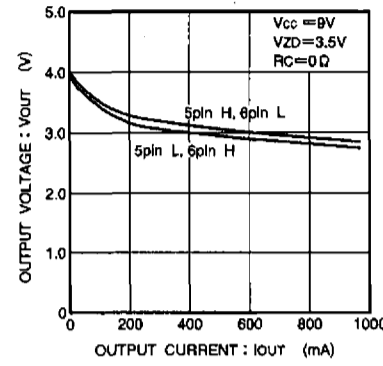

Fig.14 Zener current vs. output current

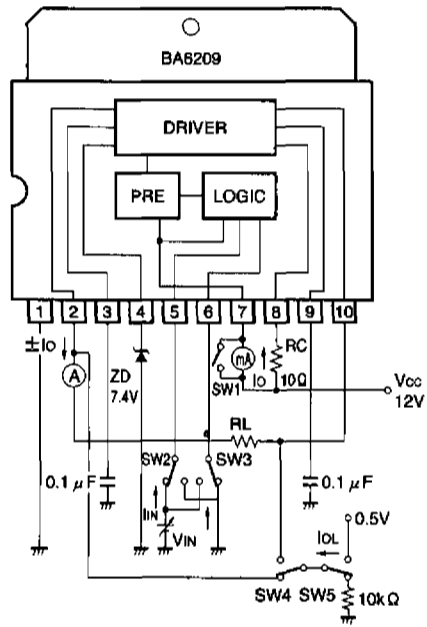Fig.15 Output voltage vs. output current

●Measurement circuit

Fig.16

●Circuit operation

Input/output truth table

Input		Output	
F_{IN}	R_{IN}	OUT1	OUT2
L	L	L	L
H	L	H	L
L	H	L	H
H	H	L	L

Forward/reverse control, forced stop, and rush current absorption are controlled by the combination of F_{IN} and R_{IN} input states.

(1) Forward/reverse control circuit

When F_{IN} is HIGH and R_{IN} is LOW, current flows from OUT1 to OUT2. When F_{IN} is LOW and R_{IN} is HIGH, current flows from OUT2 to OUT1 (refer to the truth table).

(2) Forced stop circuit

By setting R_{IN} and F_{IN} both HIGH or both LOW, power supply to the motor is shut down and a brake is applied by absorbing the motor counter-electromotive force.

(3) Rush current absorption circuit

When a high voltage (caused by such as a motor reversal) is generated on OUT1 and OUT2, an internal comparator detects the high voltage and turns on an internal circuit that absorbs rush currents.

(4) Drive circuit

The forward direction of the motor connected between OUT1 and OUT2 corresponds to the current flow from OUT1 to OUT2, and the reverse direction corresponds to the current flow from OUT2 to OUT1. The output voltage (V_{OUT}) applied to the motor is given by the equation :

V_{OUT} (V) $= V_{ZD} - V_{CE}$ (sat.) $= V_{ZD} - 0.2$ ($I_{OUT} = 100mA$)

where V_{ZD} is the zener voltage of the constant voltage diode (ZD) connected to pin 4.

If V_{REF} is left OPEN, the output voltage (V_{OUT}) is given by the equation :

V_{OUT} (V) $= V_{CC1} - V_{CE}$ (sat.) (PNP) $- 2V_F - V_{CE}$ (sat.)
$= V_{CC1} - 1.8$ ($I_{OUT} = 100mA$)

●Pin description

Pin No.	Pin name	Function
1	GND	GND
2	OUT 1	Motor output pin
3	V_{Z1}	Capacitor connection pin for preventing both output transistors being turned on at the same time
4	V_{ref}	Output HIGH voltage setting pin
5	F_{IN}	Logic input pin
6	R_{IN}	Logic input pin
7	V_{CC1}	Control circuit power supply pin
8	V_{CC2}	Output power supply pin
9	V_{Z2}	Capacitor connection pin for preventing both output transistors being turned on at the same time
10	OUT 2	Motor output pin

● Operation notes

(1) Resistor dividing IC power consumption

To reduce power dissipated in the IC, a resistance (about 3 ~ 10 Ω) must always be connected between Vcc and the power supply pin of the driver circuit. If Vcc2 is connected to Vcc with no resistor, the IC can be damaged by overcurrent when operated at the voltage range close to the maximum operating voltage.

(2) Control signal waveform

The rise and fall times of signals applied to the control pins should be 5ms or less. Longer times can cause erratic operation of the internal logic circuits and may result in damage to the driver circuits.

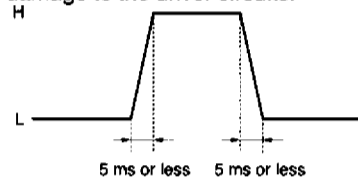5 ms or less   5 ms or less

Fig.17   Control signal

For example, if the supply voltage for the external control circuit comes up after the supply voltage of the IC, the rising edge of the control signal slowly follows the rise of the external supply voltage. This could result in erratic operation or damage to the IC due to excess currents.
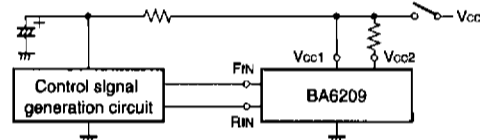

Fig.18

(3) IC ground voltage

To provide separation between the circuit elements within the IC, the GND pin of the IC must always be held at a lower potential than the other pins.

If the potential of the GND pin is allowed to rise above that of other pins (such as the control input pins), separation between the internal circuit elements could break down, resulting in erratic operation or internal damage.

For example, a resistor may be connected between GND (pin 1) and the ground as shown in Fig. 19, when detecting and controlling the motor operating current. In this case, the potential of pin 1 would be above the ground potential by an amount equal to the voltage drop across the resistor. Therefore, dropping the input pin potential to the ground potential would have the effect of applying a negative voltage to the input pin.

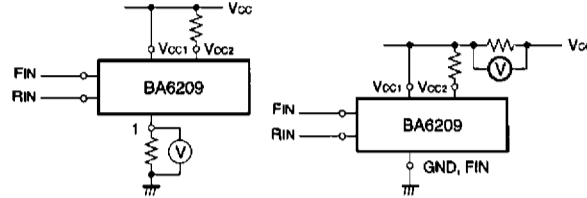This should be avoided by detecting the motor operating current in a way shown in Fig. 20.

Fig.19   Fig.20

(4) Vcc pin capacitor and conductor pattern design

Because a large instantaneous current up to 1 ~ 2A flows in the BA6209 and BA6209N ICs during motor startup and rotational reversing, special care must be taken in designing power supply and conductor patterns. If the conductor lines on the PCB are made too narrow with a high DC resistance, a voltage drop from a few hundred millivolts to a few volts can occur across the foil. This leads to a higher potential at the GND pin than the ground potential, and can result in erratic operation or damage as previously described.

The same is also true for the supply voltage lines. The design must ensure that large current pulses do not result in voltage fluctuations on the supply lines. To avoid these problems, the following precautions should be observed :

1) Make conductor lines as thick as possible, and suppress the occurrence of high voltage by reducing the DC resistance of the conductor runs through which large currents flow.

2) To avoid common impedance, provide adequate physical separation between input and output circuits.

3) The bypass capacitor between the Vcc and GND pins should be placed as close as possible to both pins.
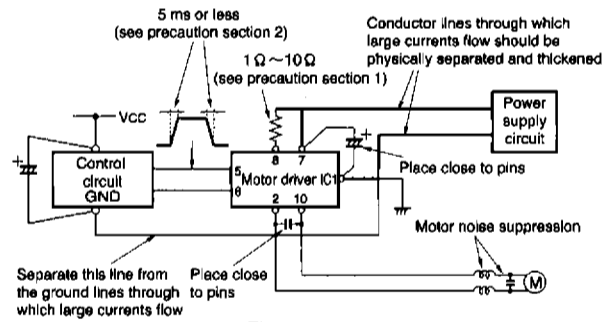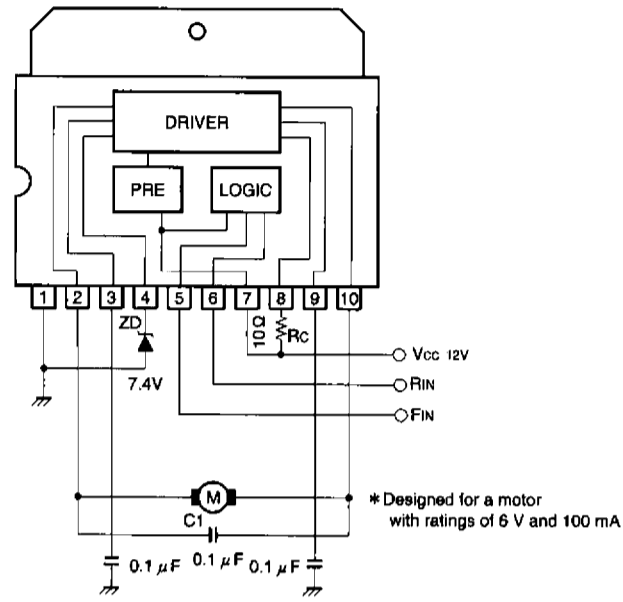

Fig.21

●Application example

Fig.22

●External dimensions (Units: mm)

HSIP10   SIP10

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