DC-DC converter unit

BP51L05/BP51L12

BP51L05 and BP51L12 are DC-DC converter units that use a pulse width modulation (PWM) system. They contain built-in control circuits, switching devices, rectifiers, and coils, and operate if only an I/O smoothing capacitor is connected. With a wide range of input voltage, the ICs are best suited for obtaining a stable local power source from a main power supply with a large voltage variation.

●Applications

Power supplies for copiers, personal computers, word processors, industrial equipment, and maintenance tools

●Features

1) Wide range of input voltage.
2) High power conversion efficiency.
3) Heat sink unnecessary.
4) Compact SIP 9-pin package.

●Absolute maximum ratings

Parameter	Symbol	Limits		Unit
		BP51L05	BP51L12	
Input voltage	V_I	24		V
Output current	I_O	0.1*		A
Operating temperature	Topr	−15~70		℃
Storage temperature	Tstg	−25~85		℃

* Derating required according to ambient temperature

●Electrical characteristics

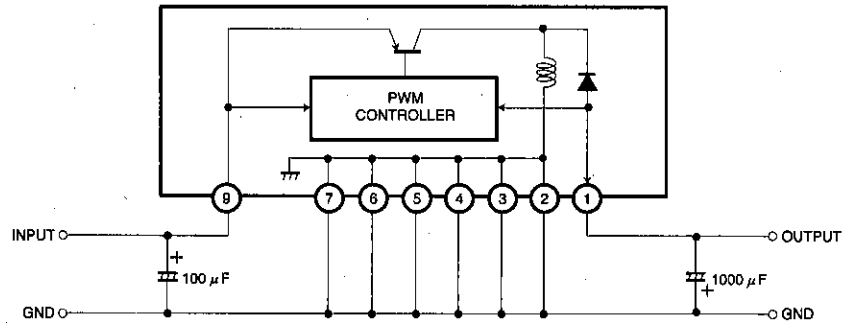
・BP51L05 (Unless otherwise noted, V_I=15V, I_O=50mA, and Ta=25℃)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V_I	8	—	20	V	
Output voltage	V_O	−5.3	−5	−4.7	V	
Output current	I_O	0.01	—	0.1	A	
Line regulation 1	ΔV_{O1}	—	3	30	mV	V_I=15V~20V
Line regulation 2	ΔV_{O2}	—	5	30	mV	V_I=8V~15V
Load regulation 1	ΔV_{O3}	—	3	30	mV	I_O=50mA~100mA
Load regulation 2	ΔV_{O4}	—	0	30	mV	I_O=10mA~50mA
Output ripple voltage	$\upsilon\gamma$	—	13	40	mV_{P-P}	Not including pulsation noise
Power conversion efficiency	η	30	50	—	%	
Switching frequency	fsw	—	45	—	kHz	

・BP51L12 (Unless otherwise noted, V_I=15V, I_O=50mA, and Ta=25℃)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	V_I	8	—	20	V	
Output voltage	V_O	−12.8	−12	−11.2	V	
Output current	I_O	0.01	—	0.1	A	
Line regulation	ΔV_{O1}	—	20	80	mV	V_I=8V~20V
Load regulation	ΔV_{O2}	—	20	80	mV	I_O=10mA~100mA
Output ripple voltage	$\upsilon\gamma$	—	10	80	mV_{P-P}	Not including pulsation noise
Power conversion efficiency	η	40	60	—	%	
Switching frequency	fsw	—	45	—	kHz	

●Block diagram and Measurement circuit



Electrolytic capacitor: TWSS series (Shinei Tsushin Kogyo)

Fig.1

●Pin description

Pin No.	Pin name	Function
1	V_O	Output pin; output smoothing capacitor with a recommended capacitance of 1000 μF is connected between this pin and GND
2~7		Ground pins, which are all connected internally
9	V_I	Input pin; input capacitor with a recommended capacitance of 100 μF is connected between this pin and GND

●Operation notes

(1) Reduce output current according to an increase in ambient temperature. Use the IC within the derating curve range.

(2) Sudden power increase at the input pin (pin 9) results in increased rush current, and may cause damage to the hybrid IC and overshooting of output voltage. Check for this problem, which is dependent on the rise time of input power supply and load conditions, in the actual application. As a guide, input power supply of 10ms or greater is required against rush current, and 100ms or greater against overshooting. Suppress the peak value of rush current to 2A or less.

(3) Pins 2 to 7 are ground pins that are connected to each other internally. Not all pins have to be used.

(4) The IC contains no circuit to protect the output current. If short circuit is feared, use the ICP or other protection measures.

●Electrical characteristic curves

・BP51L05

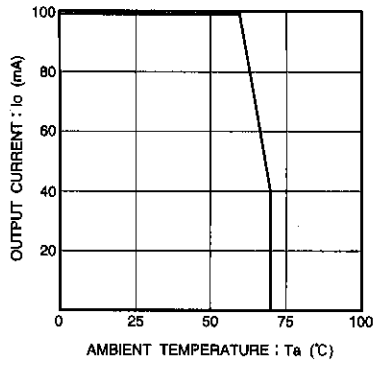


Fig.2 Derating curve

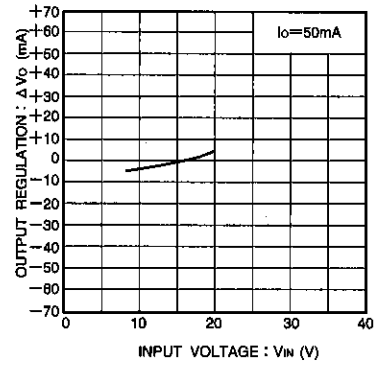


Fig.3 Output voltage variation vs. input voltage

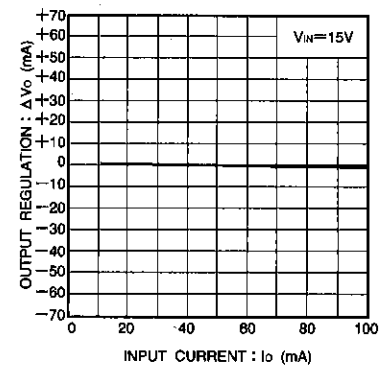


Fig.4 Output voltage variation vs. input current

・BP51L12

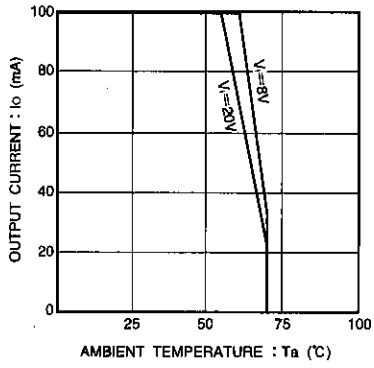


Fig.5 Derating curve

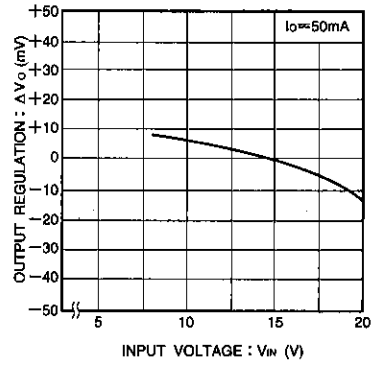


Fig.6 Output voltage variation vs. input voltage

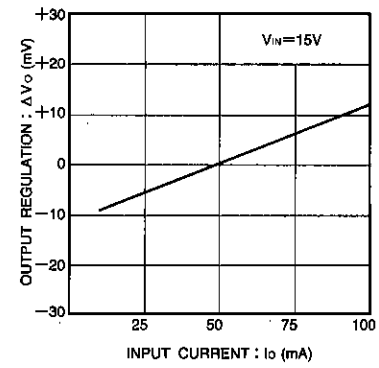
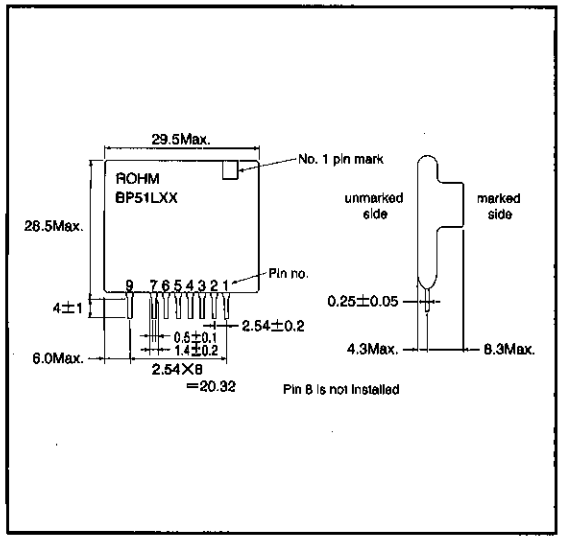


Fig.7 Output voltage variation vs. input current

●External dimensions (Units: mm)



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