

DM7598/DM8598 tri-state 256-bit read only memory

general description

The DM8598 is identical to the SN7488 except that the enable input on the SN7488 simply places all outputs in the logical "1" state, whereas the enable input on the DM8598 places the outputs in a high impedance state. This high impedance state allows many outputs to be connected in parallel for expansion to greater numbers of words and/or connection to a common bus line. Features include:

- Organized as 32 8-bit words

- Party line capability
- On-chip decoding
- Pin compatible with SN7488
- Typical access time — 30 ns
- Total power dissipation — 350 mW
- TTL and DTL compatible.
- Strobe input
- Input clamp diodes.

connection diagram

