# **DP8483**

DP8483 TTL to 100k ECL Level Translator with Latch



Literature Number: SNOSBO1A



# DP8483 TTL to 100k ECL Level Translator with Latch

TL/F/5864-1

### **General Description**

This circuit translates TTL input levels to ECL output levels and provides a fall-through latch. The outputs are gated with CS providing for wire ORing of outputs. The strobe and chip select inputs operate at ECL levels.

### **Features**

- 16-pin DIP or S.O.
- ECL control inputs
- CS provided for wire ORing of output bus
- 100k ECL I/O compatible
- 3.0 ns typical propagation delay

### **Logic and Connection Diagram**

# Dual-In-Line Package VEE 1 16 VCC QQ 2 Q G D 15 DO QQ G D 14 D1 QQ G D 12 D3 QQ G D 11 D4 QQ G D 12 D3 QQ G D 11 D4 QQ G D 11 D4 QQ G D 11 D4

**Top View** 

### **Truth Table**

D	lœ	STR	CS
Н	L	L	Н
L	Н	L	Н
X	Q	Н	Н
X	L	X	L

H=high level (most positive)

- L=low level (most negative)
- X = don't care

Order Number DP8483J, DP8483M or DP8483N See NS Package Number J16A, M16B or N16A

### **Absolute Maximum Ratings** (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

 $\begin{array}{ccc} V_{EE} \ Supply \ Voltage & -8V \\ V_{CC} \ Supply \ Voltage & 7V \\ Input \ Voltage \ (ECL) & GND \ to \ V_{EE} \\ Input \ Voltage \ (TTL) & -1V \ to \ 5.5V \\ Output \ Current & 50 \ mA \\ \end{array}$ 

Maximum Power Dissipation\* at 25°C

\*Derate molded package 11.8 mW/°C above 25°C.

# Recommended Operating Conditions

 $\begin{array}{lll} V_{EE} \ Supply \ Voltage & -4.5V + 7\% \\ V_{CC} \ Supply \ Voltage & 5.0V \pm 10\% \\ T_A, \ Ambient \ Temperature & 0°C \ to 85°C \end{array}$ 

### Electrical Characteristics (TTL Logic) (Notes 2 and 3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V <sub>IL</sub>	Input Low Voltage				0.8	V
V <sub>IH</sub>	Input High Voltage		2.0			V
I <sub>IL</sub>	Input Low Current	V <sub>IN</sub> = 0.5V		-25	-200	μΑ
I <sub>IH</sub>	Input High Current	V <sub>IN</sub> =2.5V		1.0	40	μΑ
V <sub>CLAMP</sub>	Input Clamp Voltage	I <sub>IN</sub> = -12 mA		-0.9	-1.2	V
Icc	Supply Current	V <sub>CC</sub> =5.5V		10	20	mA

### Electrical Characteristics (ECL Logic) (Notes 2 and 3)

Symbol	Parameter	Conditions	Min	Тур	Max	Units
V <sub>IL</sub>	Input Low Voltage	V <sub>EE</sub> = -4.5V	-1810		<b>-1475</b>	mV
V <sub>IH</sub>	Input High Voltage	V <sub>EE</sub> = -4.5V	-1165		-880	mV
I <sub>IL</sub>	Input Low Current	V <sub>IN</sub> = -1.8V		45	150	μΑ
I <sub>IH</sub>	Input High Current	$V_{IN} = -0.8V$		75	200	μΑ
V <sub>OL</sub>	Output Low Voltage	V <sub>EE</sub> = -4.5V	-1810	<b>-1705</b>	<b>-1620</b>	mV
V <sub>OH</sub>	Output High Voltage	V <sub>EE</sub> = -4.5V	-1025	-955	-880	mV
V <sub>OLC</sub>	Output Low Voltage	V <sub>EE</sub> = -4.5V			-1610	mV
V <sub>OHC</sub>	Output High Voltage	V <sub>EE</sub> = -4.5V	-1035			mV
I <sub>EE</sub>	Supply Current	V <sub>EE</sub> = -4.8V		-65	-85	mA

### Switching Characteristics (Notes 2 and 4)

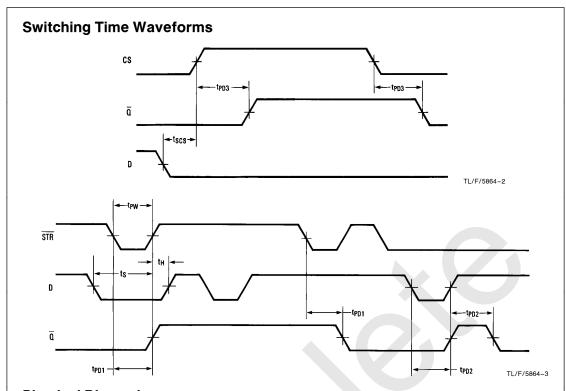
Symbol	Parameter	Conditions	Min	Тур	Max	Units
t <sub>PD1</sub>	Strobe To Output Delay		1.5	3.0	6.0	ns
t <sub>PD2</sub>	Data To Output Delay		2.5	4.5	7.5	ns
t <sub>S</sub>	Data Set-Up Time to Strobe		5.0	2.0		ns
t <sub>H</sub>	Data Hold Time	(Note 4)	1.0	0		ns
t <sub>PW</sub>	Strobe Pulse Width		5.0	3.0		ns
t <sub>PD3</sub>	Chip Select to Output Delay		1.0	2.5	4.0	ns
t <sub>SCS</sub>	Data Set-Up Time to Chip Select		5.5	3.0		ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. They are not meant to imply that the device should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

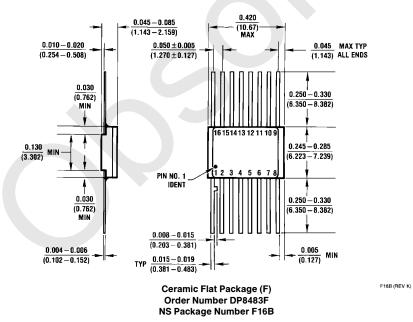
Note 2: Unless otherwise specified, min/max limits apply across the 0°C to 85°C ambient temperature range in still air and across the specified supply variations. All typical values are for 25°C and nominal supply.

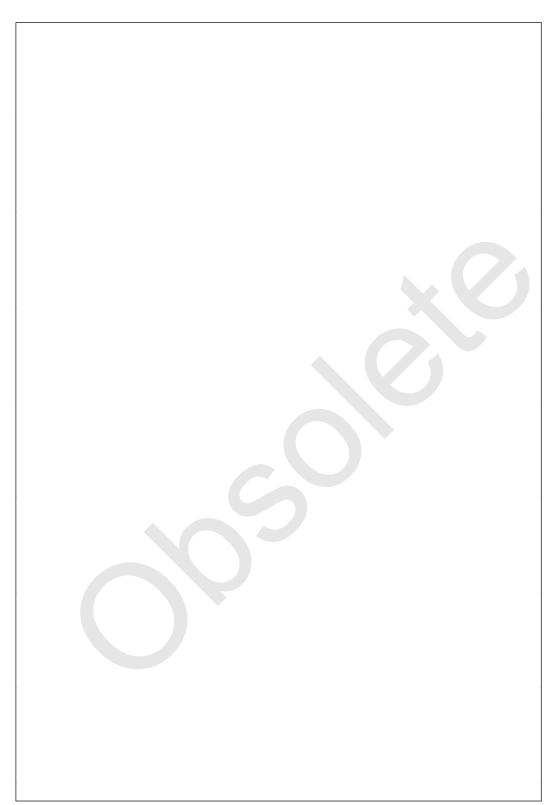
Note 3: All currents into device pins are shown as positive; all currents out of device pins are shown as negative. All voltages are referenced to ground, unless otherwise specified.

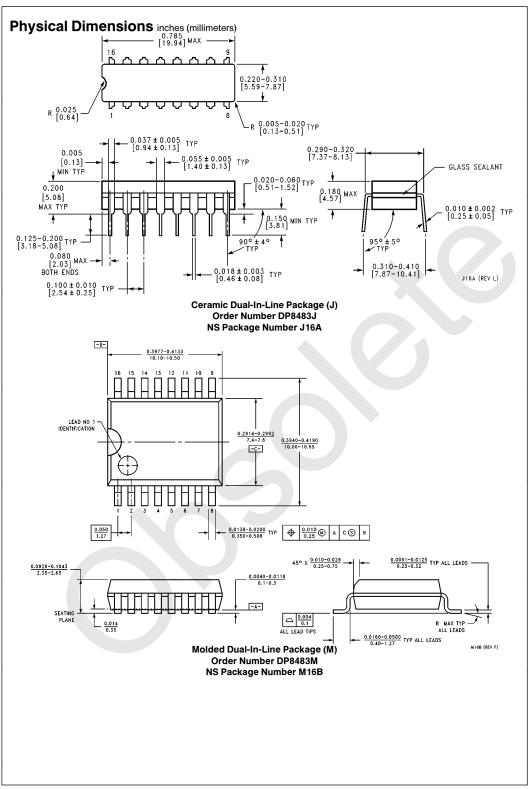
Note 4: Unless otherwise specified, all AC measurements are referenced from the 1.5V level of the TTL input and to/from the 50% point of the ECL signal and a  $50\Omega$  resistor to -2V is the load. ECL input rise and fall times are 0.7 ns  $\pm$ 0.1 ns from 20% to 80%. TTL input characteristics is 0V to 3V with  $t_r$ = $t_f$  $\le$  3 ns measured from 10% to 90%.



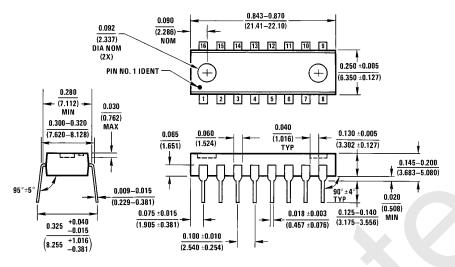
## Physical Dimensions inches (millimeters)







### Physical Dimensions inches (millimeters) (Continued)



Molded Dual-In-Line Package (N) Order Number DP8483N NS Package Number N16A N16A (REV E)

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