DS75123

National Semiconductor

DS75123 Dual Line Driver

General Description

Connection Diagram

The DS75123 is a monolithic dual line driver designed specifically to meet the I/O interface specifications for IBM System 360. It is compatible with standard TTL logic and supply voltage levels.

The low-impedance emitter-follower outputs of the DS75123 enable driving terminated low impedance lines. In addition the outputs are uncommitted allowing two or more drivers to drive the same line.

Output short-circuit protection is incorporated to turn off the output when the output voltage drops below approximately 1.5V.

Dual-In-Line Package

Features

- Meet IBM System 360 I/O interface specifications for digital data transmission over 50Ω to 500Ω coaxial cable, strip line, or terminated pair transmission lines
- TTL compatible with single 5.0V supply
- 3.11V output at I_{OH} = -59.3 mA
- Open emitter-follower output structure for party-line operation
- Short circuit protection
- AND-OR logic configuration ...
- Plug-in replacement for the SN75123 and the 8T23

Typical Performance Characteristics



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Absolute Maximum Ratings (Note 1)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage, V _{CC}	7.0V
Input Voltage	5.5V
Output Voltage	7.0V
Maximum Power Dissipation* at 25°C Molded Package	1280 mW
Operating Free-Air Temperature Range	0°C to + 75°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (Soldering, 4 seconds)	260°C
*Derate molded package 10.2 mW/*C above 25*C.	

Operating Conditions

	Min	Max	Units	
Supply Voltage, V _{CC}	4.75	5.25	v	
High Level Output Current,		- 100	mA	
ЮН				
Temperature, T _A	0	+ 75	°C	

Electrical Characteristics (Notes 2 and 3)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
VIH	High Level Input Voltage			2.0			V
VIL	Low Level Input Voltage					0.8	V
VI	Input Clamp Voltage	$V_{\rm CC} = 5.0V, I_{\rm I} = -12 {\rm mA}$				-1.5	V
ų	Input Current at Max Input Voltage	$V_{CC} = 5.25V, V_{IN} = 5.5V$				1	mA
VOH	High Level Output Voltage	$V_{\rm CC} = 5.0V, V_{\rm H} = 2.0V,$	T _A = 25°C	3.11			V
		$I_{OH} = -59.3 \text{ mA}, (Note 4)$	$T_A = 0^{\circ}C \text{ to } + 75^{\circ}C$	2.9			V
ЮН	High Level Output Current	$V_{CC} = 5.0V, V_{IH} = 4.5V, T_A = 25^{\circ}C,$ $V_{OH} = 2.0V, (Note 4)$		-100		- 250	mA
VOL	Low Level Output Voltage	$V_{IL} = 0.8V, I_{OL} = -240 \ \mu A$, (Note 4)				0.15	v
IO(OFF)	Off State Output Current	$V_{CC} = 0, V_{O} = 3.0V$				40	μA
lін	High Level Input Current	V _I = 4.5V				40	μΑ
	Low Level Input Current	$V_{I} = 0.4V$	2	-0.1		- 1.6	mA
los	Short Circuit Output Current	$V_{CC} = 5.0V, T_A = 25^{\circ}C$				-30	mA
Іссн	Supply Current, Outputs High	V _{CC} = 5.25V, All Inputs at 2.0V, Outputs Open				28	mA
ICCL	Supply Current, Outputs Low	$V_{CC} = 5.25V$, All Inputs at 0.8V, Outputs Open				60	mA

Switching Characteristics $V_{CC} = 5.0V$, $T_A = 25^{\circ}C$

Symbol	Parameter	Conditions		Min	Тур	Max	Units
t _{PLH}	Propagation Delay Time, Low-	$R_L = 50\Omega$, (See AC Test Circuit	$C_L = 15 pF$		12	20	ns
1	to-High Level Output	and Switching Time Waveforms	C _L = 100 pF		20	35	ns
t _{PHL}	Propagation Delay Time, High-	$R_L = 50\Omega$, (See AC Test Circuit	C _L = 15 pF		12	20	ns
	to-Low Level Output	and Switching Time Waveforms	$C_L = 100 pF$		15	25	ns

Note 1: "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. Except for "Operating Temperature Range" they are not meant to imply that the devices should be operated at these limits. The table of "Electrical Characteristics" provides conditions for actual device operation.

Note 2: All currents into device pins are shown as positive, currents out of device pins shown as negative, all voltage values are referenced with respect to network ground terminal, unless otherwise noted. All values shown as max or min on absolute value basis.

Note 3: Min/max limits apply across the guaranteed operating temperature range of 0°C to + 75°C for DS75123, unless otherwise specified. Typicals are for V_{CC} = 5.0V, T_A = 25°C. Positive current is defined as current into the referenced pin.

Note 4: The output voltage and current limits are guaranteed for any appropriate combination of high and low inputs specified by the truth table for the desired output.

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