

# ICM7213

# One Second/One Minute Timebase Generator

December 1993

#### **Features**

- Guaranteed 2V Operation
- Very Low Current Consumption: Typ. 100μA at 3V
- · All Outputs TTL compatible
- On Chip Oscillator Feedback Resistor
- Oscillator Requires Only 3 External components: Fixed Capacitor, Trim Capacitor, and A Quartz Crystal
- Output Inhibit Function
- 4 Simultaneous Outputs: One Pulse/Sec, One Pulse/ Min, 16Hz and Composite 1024 + 16 + 2Hz Outputs
- Test Speed-Up Provides Other Frequency Outputs

### Description

The ICM7213 is a fully integrated micropower oscillator and frequency divider with four buffered outputs suitable for interfacing with most logic families. The power supply may be either a two battery stack (Ni-cad, alkaline, etc.) or a regular power supply greater than 2V. Depending upon the state of the WIDTH, INHIBIT, and TEST inputs, using a 4.194304MHz crystal will produce a variety of output frequencies including 2048Hz, 1024Hz, 34.133Hz, 16Hz, 1Hz, and  $^1/_{60}$ Hz (plus composites).

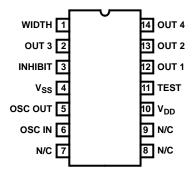
The ICM7213 utilizes a very high speed low power metal gate CMOS technology which uses 6.4V zeners between the drains and sources of each transistor and also across the supply terminals. Consequently, the ICM7213 is limited to a 6V maximum  $V_{\text{SUPPLY}}$ , although a simple dropping network can be used to extend the  $V_{\text{SUPPLY}}$  range well above 6V (See Figure 9).

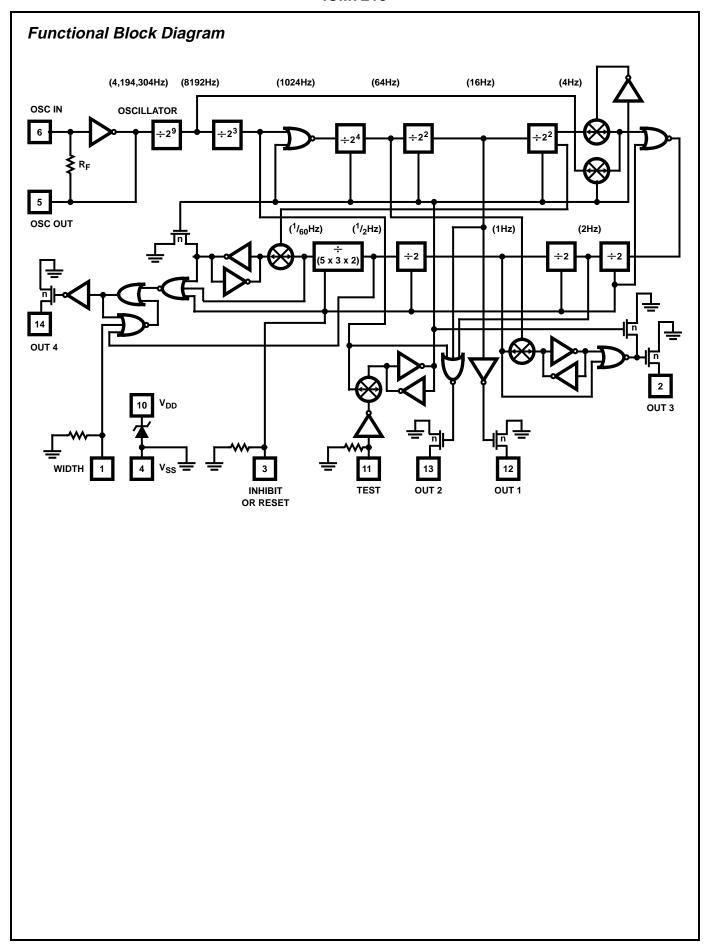
### Ordering Information

PART NUMBER	TEMPERATURE RANGE	PACKAGE
ICM7213IPD	-25°C to +85°C	14 Lead Plastic DIP

#### **Pinout**

ICM7213 (PDIP) TOP VIEW





# Specifications ICM7213

#### **Absolute Maximum Ratings**

#### **Thermal Information**

Supply Voltage (V <sub>DD</sub> - V <sub>SS</sub> )	VC
Output Current (Any output)	nΑ
All Input and Oscillator Voltages V <sub>SS</sub> - 0.3V to V <sub>DD</sub> + 0.3	3V
All Output VoltagesV <sub>SS</sub> to 6.0	VC
Operating Temperature Range25°C to +85°	$^{\circ}$ C
Storage Temperature Range55°C to +150°	οС
Lead Temperature (Soldering, 10s) +300 <sup>c</sup>	$^{\circ}$ C

Thermal Resistance	$\theta_{JA}$
Plastic Package	145°C/W
Junction Temperature	+150°C

CAUTION: Stresses above those listed in "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress only rating and operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied.

## $\textbf{Electrical Specifications} \qquad \textit{V}_{\text{DD}} - \textit{V}_{\text{SS}} = 3.0 \, \textit{V}, \, \textit{f}_{\text{OSC}} = 4.194304 \, \textit{MHz}, \, \text{Test Circuit, T}_{\text{A}} = 25 \, ^{\circ}\text{C Unless Otherwise Specified}$

PARAMETER	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Supply Current, I <sub>DD</sub>		-	100	140	μΑ
Guaranteed Operating Supply Voltage Range (V <sub>DD</sub> - V <sub>SS</sub> ), V <sub>SUPPLY</sub>	-20°C < T <sub>A</sub> < +85°C	2	-	4	V
Output Leakage Current, I <sub>OLK</sub>	Any output, V <sub>OUT</sub> = 6V	-	-	10	μΑ
Output Sat. Resistance, R <sub>OUT</sub>	Any output, I <sub>OLK</sub> = 2.5mA	-	120	200	Ω
Inhibit Input Current, I <sub>I</sub>	Inhibit terminal connected to V <sub>DD</sub>	-	10	40	μΑ
Test Point Input Current, I <sub>TP</sub>	Test point terminal connected to V <sub>DD</sub>	-	10	40	μΑ
Width Input Current, I <sub>W</sub>	Width terminal connected to V <sub>DD</sub>	-	10	40	μΑ
Oscillator Transconductance, g <sub>M</sub>	$V_{DD} = 2V$	100	-	-	μS
Oscillator Frequency Range (Note 1), f <sub>OSC</sub>		1	-	10	MHz
Oscillator Stability, f <sub>STAB</sub>	2V < V <sub>DD</sub> < 4V	-	1.0	-	ppm
Oscillator Start Time, t <sub>S</sub>		-	0.1	-	S
	V <sub>DD</sub> = 2.0V	-	0.2	-	s

#### NOTE:

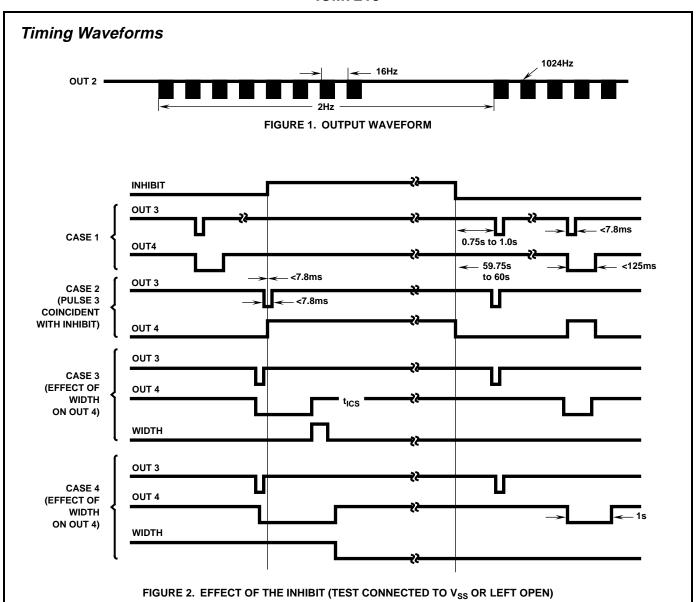
#### **Output Definitions**

INI	(NOTE 1) PUT STAT		PIN 12	PIN 12 PIN 13		PIN 14	
TEST	INHIBIT	WIDTH	OUT 1	OUT 2	PIN 2 OUT 3	OUT 4	
L	L	L	16Hz ÷ 2 <sup>18</sup>	$1024 + 16 + 2$ Hz (÷ $2^{12} \div 2^{18} \div 2^{21}$ ) composite	1Hz, 7.8ms ÷ 2 <sup>22</sup>	1/60Hz, 1s ÷ (2 <sup>24</sup> x 3 x 5)	
L	L	Н	16Hz ÷ 2 <sup>18</sup>	$1024 + 16 + 2$ Hz (÷ $2^{12} \div 2^{18} \div 2^{21}$ ) composite	1Hz, 7.8ms ÷ 2 <sup>22</sup>	<sup>1</sup> / <sub>60</sub> Hz, 125ms	
L	Н	L	16Hz ÷ 2 <sup>18</sup>	1024 + 16Hz (÷ 2 <sup>12</sup> ÷ 2 <sup>18</sup> ) composite	OFF	OFF	
L	Н	Н	16Hz ÷ 2 <sup>18</sup>	$\overline{1024 + 16}$ Hz (÷ 2 <sup>12</sup> ÷ 2 <sup>18</sup> ) composite	OFF	See Waveforms	
Н	L	L	ON	$\overline{4096 + 1024}$ Hz (÷ 2 <sup>10</sup> ÷ 2 <sup>12</sup> ) composite	2048Hz ÷ 2 <sup>11</sup>	34.133Hz, 50% D.C. ÷ (2 <sup>13</sup> x 5 x 3)	
Н	L	Н	ON	$4096 + 1024$ Hz (÷ $2^{10}$ ÷ $2^{12}$ ) composite	2048Hz ÷ 2 <sup>11</sup>	34.133Hz, 50% D.C. ÷ (2 <sup>13</sup> x 5 x 3)	
Н	Н	L	ON	1024Hz ÷ 2 <sup>12</sup>	ON	OFF	
Н	Н	Н	ON	1024Hz ÷ 2 <sup>12</sup>	ON	OFF	

#### NOTE:

<sup>1.</sup> The ICM7213 uses dynamic dividers for high frequency division. As with any dynamic system, information is stored on very small nodal capacitances instead of latches (static system), therefore there is a lower frequency of operation. Dynamic dividers are used to improve the high frequency performance while at the same time significantly decreasing power consumption. At low V<sub>SUPPLY</sub>, operation at less than 1MHz is possible.

<sup>1.</sup> When TEST and RESET are connected to ground, or left open, all outputs except for OUT 3 and OUT 4 have a 50% duty cycle.



## **Typical Performance Curves**

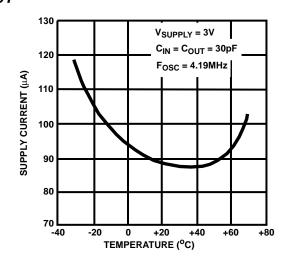


FIGURE 3. SUPPLY CURRENT AS A FUNCTION OF TEMPERATURE

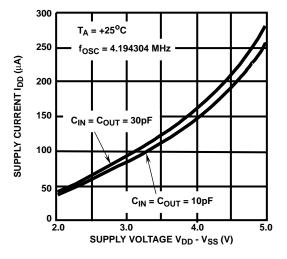


FIGURE 4. SUPPLY CURRENT AS A FUNCTION OF SUPPLY VOLTAGE

# Typical Performance Curves

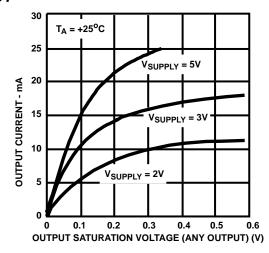


FIGURE 5. OUTPUT CURRENT AS A FUNCTION OF OUTPUT SATURATION VOLTAGE

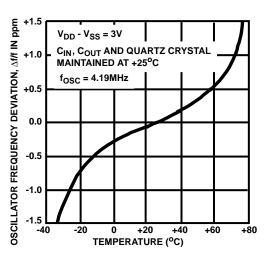


FIGURE 6. OSCILLATOR STABILITY AS A FUNCTION OF DEVICE TEMPERATURE

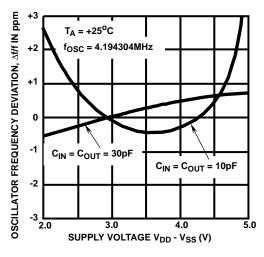


FIGURE 7. OSCILLATOR STABILITY AS A FUNCTION OF SUPPLY VOLTAGE

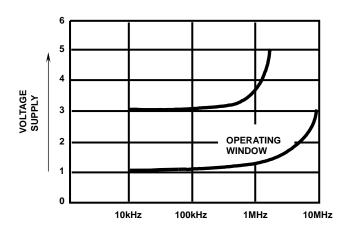
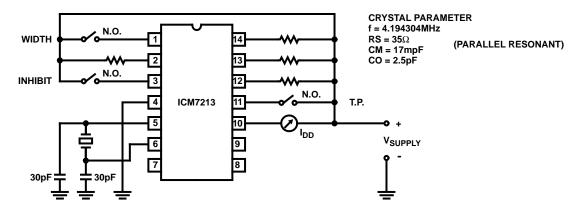


FIGURE 8. WINDOW OF CORRECT OPERATION

#### **Test Circuit**



## **Detailed Description**

#### **Supply Voltage Considerations**

The ICM7213 may be used to provide various precision outputs with frequencies from 2048Hz to  $^{1}/_{60}$ Hz using a 4.194304MHz quartz oscillator, and other output frequencies may be obtained using other quartz crystal frequencies. Since the ICM7213 uses dynamic high frequency dividers for the initial frequency division there are limitations on the  $V_{\rm SUPPLY}$  range depending on the oscillator frequency. If, for example, a low frequency quartz crystal is selected, the  $V_{\rm SUPPLY}$  should be selected in the center of the operating window, or approximately 1.7V.

The  $V_{\text{SUPPLY}}$  to the ICM7213 may be derived from a high voltage supply by using a simple resistor divider (if power is of no concern), by using a series resistor for minimum current consumption, or by means of a regulator.

#### **Outputs**

Pull up resistors will generally be required to interface with other logic families. These resistors must be connected between the various outputs and the positive power supply.

#### **Oscillator Considerations**

The oscillator consists of a CMOS inverter and a feedback resistor whose value is dependent on the voltage at the oscillator input and output terminals and the  $V_{SUPPLY}$ . Oscillator stabilities of approximately 0.1ppm per 0.1V variation are achievable with a nominal  $V_{SUPPLY}$  of 5V and a single voltage dropping resistor. The crystal specifications are shown in the Test Circuit.

It is recommended that the crystal load capacitance (CL) be no greater than 22pF for a crystal having a series resistance equal to or less than 75 $\Omega$ , otherwise the output amplitude of the oscillator may be too low to drive the divider reliably.

It a very high quality oscillator is desired, it is recommended that a quartz crystal be used having a tight tuning tolerance  $\pm 10 \text{ppm}$ , a low series resistance (less than  $25\Omega)$ , a low motional capacitance of 5mpF and a load capacitance of 20pF. The fixed capacitor  $C_{\text{IN}}$  should be 30pF and the oscillator tuning capacitor should range between approximately 16pF and 60pF.

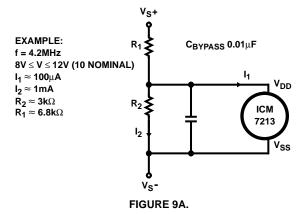
Use of a high quality crystal will result in typical stabilities of 0.05ppm per 0.1V change of  $V_{\mbox{\scriptsize SUPPLY}}$ 

#### **Control Inputs**

The TEST input inhibits the  $2^{18}$  output and applies the  $2^9$  output to the  $2^{21}$  divider, thereby permitting a speedup of the testing of the  $\div$  60 section by a factor of 2048 times. This also results in alternative output frequencies (see table).

The WIDTH input may be used to change the pulse width of OUT 4 from 125ms to 1s, or to change the state of OUT 4 from ON to OFF during INHIBIT.

See Figures 1 and 2 for output waveforms and effect of control inputs.



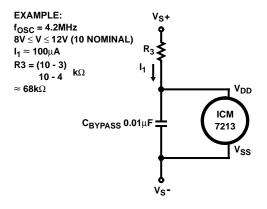


FIGURE 9B.

FIGURE 9. BIASING SCHEMES WITH HIGH VOLTAGE SUPPLIES