## To request the full datasheet, please contact your local IDT Sales Representative or call 1-831-754-4555

## Introduction

As part of the IDT IP Co-Processor productfamily, IDT delivers high performance, feature-rich, easy-to-use, Network Search Engine (NSE) products. Using CAM (Content Addressable Memory) technology, IDT NSE products accelerate searchfunctionsfor Access Control Lists (ACL), FlowCaching, andforwardingto improve performance innextgeneration networkingequipment.

## Device Description

The NSE2PortXSC interface is intended to work with any AMCCNPU with an external search co-processor interface(XSC). It will operate with multiple NPUs such as thenP3400 andnP7250, or can be used to satisfy themultipleXSC requirements of thenP7510. The75K52213iscompliant with the XSC specification and the device may beoptionally expandedup to eight devices deep providing 1 million entries of 32 -bit width.

## Features

- 64K x 72 (4.5M) Data and Mask cells
- Full Ternary Content Addressable Memory
- Advanced DatabaseManagement
- SelectableDatabases
- ProgrammableWidthperDatabase
- Lookup widths from 32 to 512 bits
- Only the selected Database is powered
- Maintenance Instructions
- Aging
- MultiHitInvalidate
- Learn per Database
- Support for Multiple Contexts
- Pool of Global Mask Registers
- Control and Management

Figure 1.0 AMCC NPU Interface

- 32-bitHost control portfeaturing glueless PPC interface
- Capable of In-Band Control and Management
- Associated Data SRAM is supported through a glueless ZBT® interface
- Lowest Power per Application
- Synchronous PipelineOperation
- Boundary Scan JTAG Interface
- 1.2V Core Supply
- 3.3V I/O Supply for XSC I/O and ZBT® SRAM


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