



KA319 Dual Comparator

Features

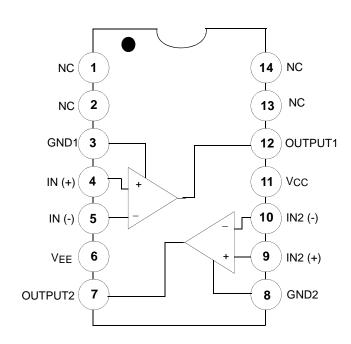
- Operates From a Single 5V Supply
- Typically 80ns Response Time at ±15V
- Open Collector Outputs : up to +35V
- High Output Drive Current : 25mA
- Inputs and Outputs can be Isolated From System Ground
- Minimum Fan-out of 2 (Each Side)
- Two Independent Comparators

Description

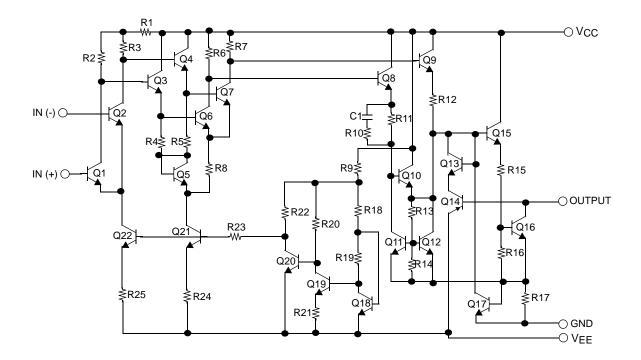
The KA319 is a dual high speed voltage comparator designed to operate from a single +5V supply up to $\pm 15V$ dual supplies. Open collector of the output stage makes the KA319 compatible with RTL, DTL and TTL as well as capable of driving lamps and relays at currents up to 25mA. Typical response time of 80ns with $\pm 15V$ power supplies makes the KA319 ideal for application in fast A/D converts, level shiftiers, oscillators, and multivibrators.



Internal Block Diagram



Schematic Diagram



Absolute Maximum Ratings

Parameter	Symbol	Value	Unit	
Supply Voltage	Vcc	36	V	
Output to Negative Supply Voltage	Vo - Vee	36	V	
Ground to Negative Supply Voltage	VEE	25	V	
Ground to Positive Supply Voltage	Vcc	18	V	
Differential Input Voltage	VI(DIFF)	5	V	
Input Voltage	VI	±15	V	
Output Short Circuit Duration	-	10	sec	
Power Dissipation	PD	500	mW	
Thermal Resistance Junction-Ambient Max.	Rθja	250	°C/W	
Operating Temperature Range KA319	TOPR	0 ~ +70	°C	
Storage Temperature Range	TSTG	-65 ~ +150	°C	

Electrical Characteristics

(VCC = +15V, VEE = -15V, TA = 25°C, unless otherwise specified)

Devementer	0	Conditions		KA319			11
Parameter	Symbol			Min.	Тур.	Max.	Unit
Input Offect Voltage (Note1)	Vio	Rs ≤ 5kΩ		-	2.0	8.0	mV
Input Offset Voltage (Note1)	VIO	$L2 \ge 2K25$	Note3	-	-	10	
Input Offset Current (Note1)	lio			-	10	200	nA
			Note3	-	-	300	ΠA
Input Bias Current	IBIAS		<u>.</u>	-	150	1000	nA
			Note3	-	-	1200	
Voltage Gain	Gv	-		8	40	-	V/mV
Response Time (Note2)	TRES	$V_{CC} = \pm 15V$		-	80	-	ns
Saturation Voltage	VSAT	VCC=15V, VEE = -15V , VI \leq -5mV, IO = 25mA		-	0.6	1.5	
		$\label{eq:VCC} \begin{array}{l} V_{CC} = 4.5 V, V_{EE} = 0 V \\ V_{I} \leq -10 mV, \ I_{O} \leq 3.2 mA \end{array}$	Note3	-	0.3	0.4	V
Output Leakage Current	lO(LKG)	M > Em M M O(D) = 2E M		-	-	-	μΑ
		$VI \ge 5mV, VO(P) = 35V$	Note3	-	-	-	
		$V_I \geq 10mV, \ V_O(P) = 35V$	·	-	0.2	10	
Input Voltage Range	VI(R)	Note3	$V_{CC} = \pm 15V$	-	±13	-	V
			VCC = 5V, VEE = 0V	1	-	3	
Differential Input Voltage	VI(DIFF)	-	Note3	-	-	±5	V
Positive Supply Current	ICC1	$V_{CC} = 5V, V_{EE} = 0V$		-	3.6	-	mA
Positive Supply Current	ICC2	$V_{CC} = \pm 15V$		-	7.5	12.5	mA
Negative Supply Current	IEE	$V_{CC} = \pm 15V$		-	3	5	mA

Notes :

1. The offset voltage and offset currents given are the maximum values required to drive the output within a volt of either supply with a 1mA load. Thus, these parameters define an error band and take into account the worst case effects of voltage gain and input impedance.

2. The response time specified is for a 100mV input step with 5mV overdrive.

3. KA319 : $0 \le T_A \le +70^{\circ}C$

Typical Performance Characteristics

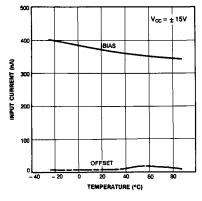


Figure 1. Input Current

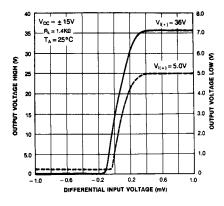


Figure 3. Transfer Function

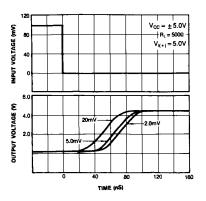


Figure 5. Response Time Various Input Overdriver

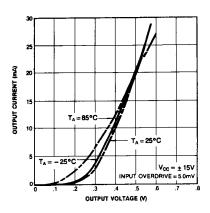


Figure 2. Output Saturation Voltage

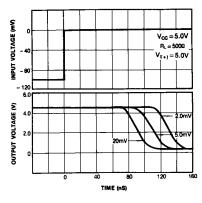


Figure 4. Response Time for Various Input Overdriver

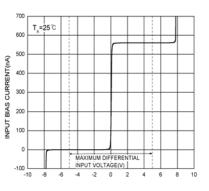


Figure 6. Input Characteristics

Typical Performance Characteristics (Continued)

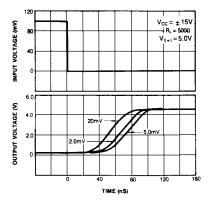


Figure 7. Response Time for Various Input Over driver

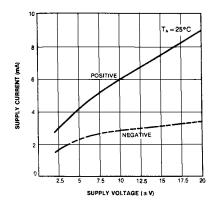


Figure 9. Supply Current

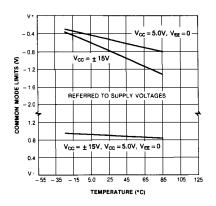


Figure 11. Common Mode Limits

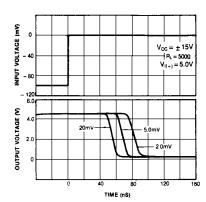


Figure 8. Response Time for Various Input Over driver

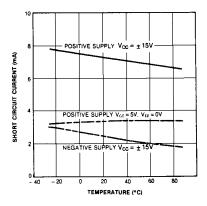


Figure 10. Supply Current

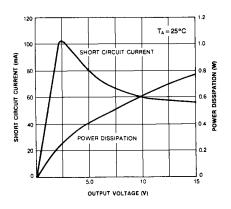
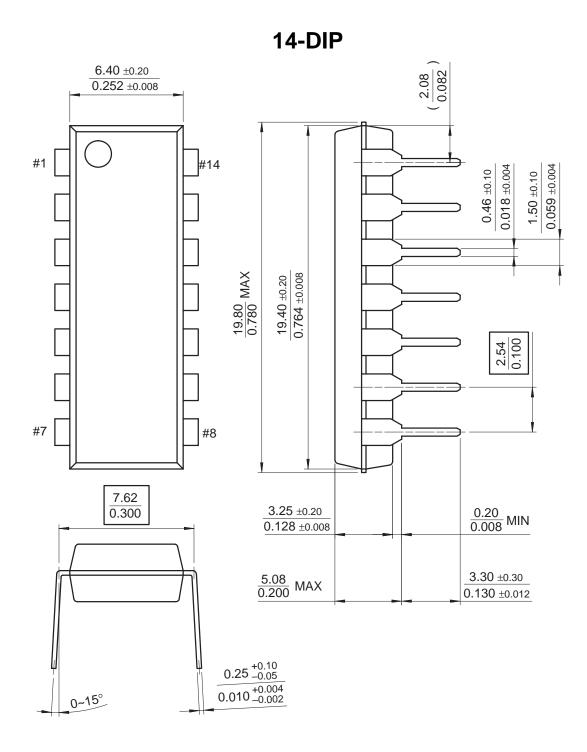


Figure 12. Output Limiting Characteristics

Mechanical Dimensions

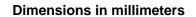
Package

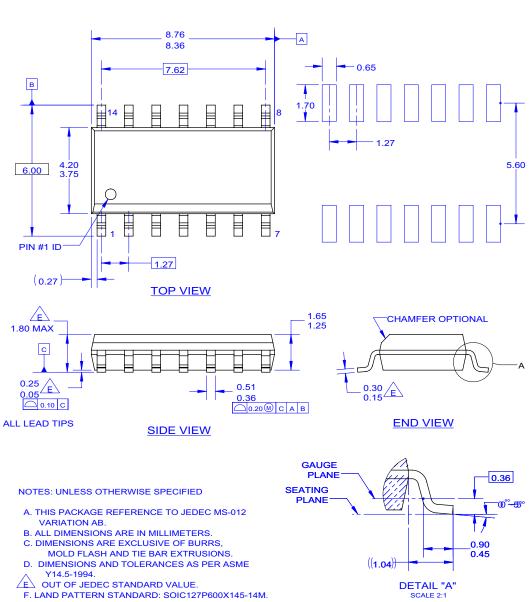
Dimensions in millimeters



Mechanical Dimensions (Continued)

Package





14-SOP

- G. FILE NAME: MKT-M14C REV2

Ordering Information

Product Number	Package	Operating Temperature
KA319	14-DIP	0 ~ +70°C
KA319D	14-SOP	0~+70 C

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
- 2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

www.fairchildsemi.com