## TELEPHONE SPEECH CIRCUIT WITH MULTIFREQUENCY TONE GENERATOR INTERFACE

PRESENTS THE PROPER DC PATH FOR THE LINE CURRENT

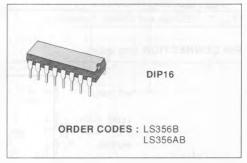
SGS-THOMSON MICROELECTRONICS

- HANDLES THE VOICE SIGNAL. PERFOR-MING THE 2/4 WIRES INTERFACE AND CHANGING THE GAIN ON BOTH SENDING AND RECEIVING AMPLIFIERS TO COMPEN-SATE FOR LINE ATTENUATION BY SENSING EITHER THE LINE CURRENT OR THE LINE VOLTAGE IN ADDITION. THE LS356 CAN AL-SO WORK IN FIXED GAIN MODE
- ACTS AS LINEAR INTERFACE FOR MF, SUP-PLYING A STABILIZED VOLTAGE TO THE DI-GITAL CHIP AND DELIVERING TO THE LINE THE MF TONES GENERATED BY THE M761

#### DESCRIPTION

**BLOCK DIAGRAM** 

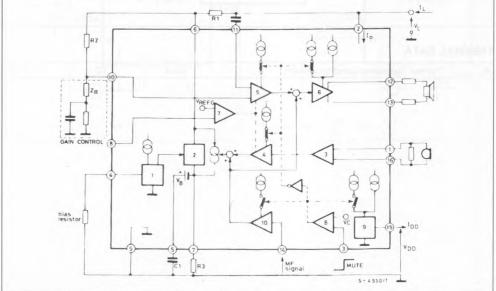
The LS356 is a monolithic circuit in 16-lead dual inline plastic package to replace the hybrid circuit in telephone set. It works with the same type of transducers for both transmitter and receiver (typical dy-



namic capsules, but the device can also work with piezoceramic ones). Many of its electrical characteristics can be controlled by means of external components to meet different specifications.

In addition to the speech operation, the LS356 acts as an interface for the MF tone signal (particularly for M761 C/MOS frequency synthesizer).





#### ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
VL	Line Voltage (3 ms pulse duration)	22	V
١L	Forward Line Current	150	mA
۱ <sub>L</sub>	Reverse Line Current	- 150	mA
Ptot	Total Power Dissipation at Tamb = 70 °C	1	W
Top	Operating Temperature	- 45 to 70	°C
T <sub>stg</sub> , T <sub>j</sub>	Storage and Junction Temperature	- 65 to 150	°C

## **PIN CONNECTION** (top view)

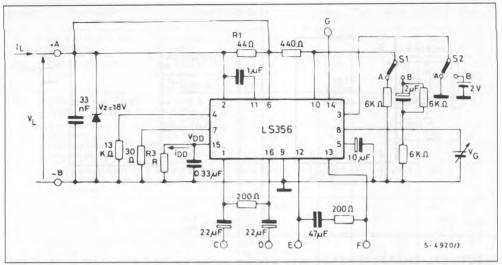
MIC. INPUT	<b>U</b> <sup>1</sup>	16 MIC.INPUT
+ LINE	[] Z	15 VDD
MUTING	3	14 MEINPUT
BIAS ADJ.	14	13 RECEIVER OUTPUT
SHUNT REG. BY PASS	5	12 RECEIVER OUTPUT
D.C.REGULATO	R [6	11 INPUT+(RECAMP)
LINE CURREN SENSING	T <b>1</b> 7	10 INPUT-(REC AMP)
GAIN CONTRO	DL 8	9 -LINE

## THERMAL DATA

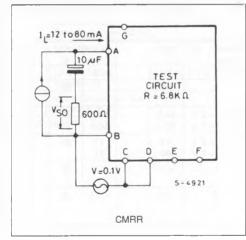
				T	
R <sub>thj-amb</sub>	Thermal Resistance Junction-ambient	Max	80	°C/W	



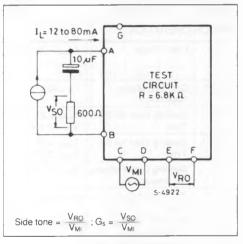
## **TEST CIRCUITS**

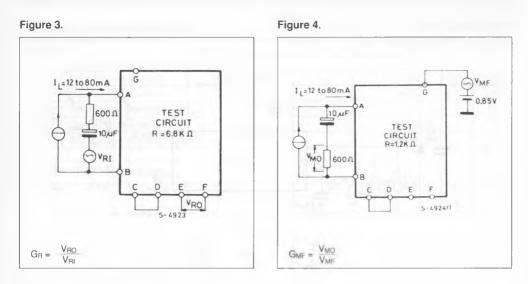












**ELECTRICAL CHARACTERISTICS** (refer to the test circuits, V<sub>G</sub>= 1 to 2 V, I<sub>L</sub> = 12 to 80 mA. S1 and S2 in (a), T<sub>amb</sub> = -25 to +50 °C, f = 200 to 3400 Hz, unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit	Fig.	
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#### SPEECH OPERATION

VL	Line Voltage	T <sub>amb</sub> = 25 °C	I <sub>L</sub> = 12 mA I <sub>L</sub> = 20 mA I <sub>L</sub> = 80 mA	3.65		4.5 5 10	v	-
CMR	Common Mode Rejection	f = 1 KHz		50			dB	1
Gs	Sending Gain for B Type	T <sub>amb</sub> = 25 °C V <sub>MI</sub> = 2 mV	$f = 1 \text{ KHz}$ $V_G = 2 \text{ V}$ $V_G = 1 \text{ V}$	44.5 48.5		46.5 50.5	dB	2
Gs	Sending Gain for AB Type	$T_{amb} = 25 \ ^{\circ}C$ $V_{MI} = 2 \ mV$	$      f = 1 \text{ KHz}  V_G = 2 \text{ V} \\ V_G = 1 \text{ V} $	44 48		47 51	dB	2
	Sending Gain Flatness (vs. frequency)	$V_{MI} = 2 \text{ mV}$	f <sub>ref</sub> = 1 KHz	- 1		+ 1	dB	2
	(*) Sending Gain Flatness for B Type (vs. current)	$V_{G} = 2 V$	$I_{ref} = 50 \text{ mA}$	- 0.5		+ 0.5	dB	2
	(*) Sending Gain Flatness for AB Type (vs. current)	$V_G = 2 V$	I <sub>ref</sub> = 50 mA	- 1		+ 1	dB	2
	Sending Distortion for B Type	f = 1 KHz I <sub>L</sub> = 16 mA	V <sub>so</sub> = 775 mV V <sub>so</sub> = 900 mV			2 10	%	2
	Sending Distortion for AB Type	f = 1 KHz I <sub>L</sub> = 16 mA	V <sub>so</sub> = 775 mV V <sub>so</sub> = 900 mV			3 10	%	2
	Sending Noise	$V_{M1} = 0 V$	$V_{G} = 1 V$		- 71		dBmp	2
	Microphone Input Impedance (pin 1-16)	$V_{M1} = 2 \text{ mV}$		40			KΩ	-

\* Fixed gain mode.



## ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter Test Conditions				Typ.	Max.	Unit	Fig.
	Sending Gain in MF Operation	V <sub>MI</sub> = 2 mV S2 in (b)		- 30			dB	2
G <sub>R</sub> Receiving Gain for B Type		V <sub>RI</sub> = 0.3 V f = 1 KHz T <sub>amb</sub> = 25 °C	$V_G = 2 V$ $V_G = 1 V$	- 5		- 3 + 1.5	dB	3
G <sub>R</sub>	Receiving Gain for AB Type	$V_{RI} = 0.3 V$ f = 1 KHz $T_{amb} = 25 ° C$	$V_G = 2 V$ $V_G = 1 V$	- 5.5		- 2.5 + 2.0	dB	3
	Receiving Gain Flatness (vs. frequency)	V <sub>R1</sub> = 0.3 V	f <sub>ref</sub> = 1 KHz	- 1		+ 1	dB	3
	(*) Receiving Gain Flatness for B Type (vs. current)	V <sub>G</sub> = 2 V	I <sub>ret</sub> = 50 mA	- 0.5		+ 0.5	dB	3
	Receiving Gain Flatness for AB Type (vs. current)	V <sub>G</sub> = 2 V	$I_{ref} = 50 \text{ mA}$	- 1		+ 1	dB	3
	Receving Distortion for B Type	f = 1 KHz	V <sub>RO</sub> = 400 mV V <sub>RO</sub> = 450 mV			2 5	%	3
	Receiving Distortion for AB Type	f = 1 KHz	V <sub>RO</sub> = 400 mA V <sub>RO</sub> = 450 mA			3 5	%	3
	Receiving Noise	$V_{RI} = 0 V$			100		μV	3
	Receiver Output Impedance (pin 12-13)	$V_{RO} = 50 \text{ mV}$			30		Ω	-
	Sidetone	f = 1 KHz S1 in (b)	$T_{amb} = 25 \ ^{\circ}C$			36	dB	2
Z <sub>ML</sub>	Line Matching Impedance	V <sub>RI</sub> = 0.3 V	f = 1KHz	500	600	700	Ω	3
I <sub>8</sub>	Input Current for Gain Control (pin 8)					- 10	μA	-

\* Fixed gain mode.



## ELECTRICAL CHARACTERISTICS (continued)

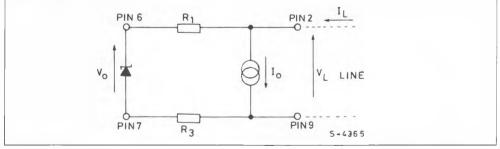
ymbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit	Fig.
IULTIFF	REQUENCY SYNTHES	IZER INTERFACE					
V <sub>DD</sub>	MF Supply Voltage (standby and operation)	S2 in (b)	2.4	2.5	2.7	V	1
I <sub>DD</sub>	MF Supply Current Standby Operation	S2 in (b)	0.5 2			mA	-
	MF Amplifier Gain	$f_{MFin} = 1 \text{ KHz}$ $V_{MFin} = 80 \text{ mV}$	15		17	dB	4
Vi	DC Input Voltage Level (pin 14)	$V_{MF in} = 80 \text{ mV}$		0.3 V <sub>DD</sub>		V	-
R	Input Impedance (pin14)	$V_{MF in} = 80 \text{ mV}$	60			KΩ	-
d	Distortion for B Type	V <sub>MF in</sub> = 110 mV			2	%	4
d	Distortion for AB Type	V <sub>MF in</sub> = 110 mV			4	%	4
	Starting Delay Time				5	ms	-
Muting Threshold Voltage (pin 3)		Speech Operation			1	V	-
		MF Operation	1.6			V	-
	Muting Standby Current (pin 3)				- 10	μA	-
	Muting Operating Current (pin 3)	S2 in (b)			+ 10	μA	-

## CIRCUIT DESCRIPTION

#### **1. DC CHARACTERISTIC**

The fig. 5 shows the DC equivalent circuit of the LS356.

Figure 5 : Equivalent DC Load to the Line.



A fixed amount  $I_{\text{o}}$  of the total available current  $I_{\text{L}}$  is drained for the proper operation of the circuit.

The value of  $I_o$  can be programmed externally by changing the value of the bias resistor connected to pin 4 (see block diagram).

The minimum value of  $I_0$  is 7.5 mA.

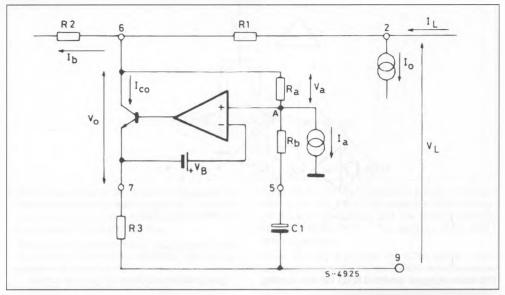
The voltage  $V_o \approx 3.8$  V of the shunt regulator is independent of the line current.

The shunt regulator (2) is controlled by a temperature compensated voltage reference (1) (see the block diagram).



Fig. 6 shows a more detailed circuit configuration of the shunt regulator.

Figure 6 : Circuit Configuration of the Shunt Regulator.



The difference  $I_{L} - I_{0}$  flows through the shunt regulator being lb negligible.

la is an internal constant current generator ; hence  $V_0 = V_B + I_a \cdot R_a = 3.8 \text{ V}.$ 

The VL, IL characteristic of the device is therefore similar to a pure resistance in series to a battery.

It is important to note that the DC voltage at pin 5 is proportional to the line current  $(V_5 = V_7 + V_B =$  $(I_L - I_0) R3 + V_B).$ 

#### 2. TWO TO FOUR WIRES CONVERSION

The LS356 performs the two wires (line) to four wires (microphone, earphone) conversion by means of a Wheatstone bridge configuration so obtaining the proper decoupling between sending and receiving signals (see fig. 7).

For a perfect balancing of the bridge -

$$\frac{1}{2_B} = \frac{R_1}{R_2}$$

The AC signal from the microphone is sent to one diagonal of the bridge (pin 6 and 9). A small percentage of the signal power is lost on ZB (being  $Z_B >> Z_L$ ); the main part is sent to the line via R1.

In receiving mode, the AC signal coming from the line is sensed across the second diagonal of the bridge (pin 11 and 10). After amplification it is applied to the receiving capsule.

The impedance Z<sub>M</sub> is simulated by the shunt regulator that is also intended to work as a transconductance amplifier for the transmission signal.

AV6-9 The impedance Z<sub>M</sub> is defined as Al6-9

From fig. 6 considering C1 as a short circuit for AC signal, any variation  $\Delta V_6$  generates a variation :

$$\Delta V_7 = \Delta V_A = \Delta V_6 \cdot \frac{H_b}{B_a + B_b}$$

The corresponding current change is

$$\Delta I = \frac{\Delta V}{B3}$$

Therefore

$$Z_{M} = \frac{\Delta V_{B}}{\Delta I} = R3 \left(1 + \frac{R_{a}}{R_{b}}\right)$$

The total impedance across the line connections (pin 11 and 9) is given by

$$Z_{ML} = R1 + Z_M // (R2 + Z_B)$$

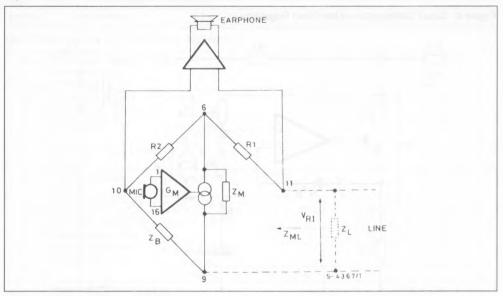
By choosing  $Z_M >> R1$  and  $Z_B >> Z_M$ 

$$Z_{ML} \equiv Z_M = R3 \left(1 + \frac{R_a}{R_b}\right)$$

The received signal amplitude across pin 11 and 10 can be changed using different values of R1 (of course the relationship  $Z_L/Z_B = R1/R2$  must be always valid).



Figure 7 : Two to Four Wires Conversion.



The received signal is related to R1 value according to the approximated relationship :

$$V_{\rm R} = 2 V_{\rm RI} \frac{R1}{R1 + 7M}$$

Note that by changing the value of R1, the transmission signal current is not changed, being the microphone amplifier a transconductance amplifier.

#### 3. AUTOMATIC GAIN CONTROL

The LS356 automatically adjusts the gain of the sending and receiving amplifiers to compensate for line attenuation.

This function is performed by the circuit of fig. 8.

The differential stage is progressively unbalanced by changing  $V_G$  in the range 1 to 2 V ( $V_{REFG}$  is an internal reference voltage, temperature compensated).

It changes the current  $I_G$ , and this current is used as a control quantity for the variable gain stages (amplifier (4) and (5) in the block diagram). The voltage  $V_G$  can be taken :

- a) from the LS356 itself (both in variable and in fixed mode) and
- b) from a resistive divider, directly at the end of the line.
- a) In the first case, connecting  $V_G$  (pin 8) to the regulator bypass (pin 5) it is possible to obtain a

gain charcteristic depending on the current. In fact (see fig. 6) :

$$V_5 = V_B + V_7 \cong V_B + (I_L - I_o)' R3$$

The starting point of the automatic level control is obtained at  $I_L = 25$  mA when the drain current  $I_0 = 7.5$  mA.

Minimum gain is reached for a line current of about 52 mA for the same drain current  $I_0 = 7.5$  mA. When  $I_0$  is increased by means of the external resistor connected to pin 4, the two above mentioned values of the line current for the starting point and for the minimum gain increase accordingly.

It is also possible to change the starting point without changing I<sub>0</sub> by connecting pin 8 to the centre of a resistive divider placed between pin 5 and ground (the total resistance seen by pin 5 must be at least 100 KΩ). In this case, the AGC range increases too; for example using a division 1 : 1 (50 K/50 K) the AGC starting point shifts to about I<sub>L</sub> = 40 mA, and the minimum gain is obtained at I<sub>L</sub> = 95 mA. In addition to this operation mode, the V<sub>G</sub> voltage can be maintained constant thus fixing the gain values (Rx, Tx) independently of the line conditions.

For this purpose the  $V_{DD}$  voltage, available for supplying the MF generator, can be used.



#### Figure 8.

 $V_{GO}$  $V_{GO}$  $V_{REF}G$  $J_{IG}$ S-4696

b) When gains have to be related to the voltage at the line terminals of the telephone set, it is necessary to obtain  $V_G$  from a resistive divider directly connected to the end of the line.

This type of operation meets for instance the requirements of the French standard. (see the application circuit of fig. 12).

### 4. TRANSDUCERS INTERFACING

The microphone amplifier (3) has a differential input stage with high impedance ( $\approx 40 \text{ K}\Omega$ ) so allowing a good matching to the microphone by means of external resistor without affecting the sending gain. The receiving output stage (6) is particularly intended to drive dynamic capsules. (Low output impedance, 100  $\Omega$  max; high current capability, 3 mAp).

When a piezoceramic capsule is used, it is useful to increase the receiving gain by increasing R1 value (see the relationship for  $V_R$ ).

With very low impedance transducer, DC decoupling by an external capacitor must be provided to prevent a large DC current flow across the transducer itself due to the receiving output stage offset.

#### 5. MULTIFREQUENCY INTERFACING

The LS356 acts as a linear interface for the Multifrequency synthesizer M761 according to a logical signal (mute function) present on pin 3. When no key of the keyboard is pressed the mute state is low and the LS356 feeds the M761 through pin 15 with low voltage and low current (standby operation of the M761). The oscillator of the M761 is not operating.

When one key is pressed, the M761 sends a "high state" mute condition to the LS356. A voltage comparator (8) of LS356 drives internal electronic switches : the voltage and the current delivered by the voltage supply (9) are increased to allow the operation of the oscillator.

This extra current is diverted by the receiving and sending section of the LS356 and during this operation the receiving output stage is partially inhibited and the input stages of sending and receiving amplifiers are switched OFF.

A controlled amount of the signalling is allowed to reach the earphone to give a feedback to the subscriber ; the MF amplifier (10) delivers the dial tones to the sending paths.

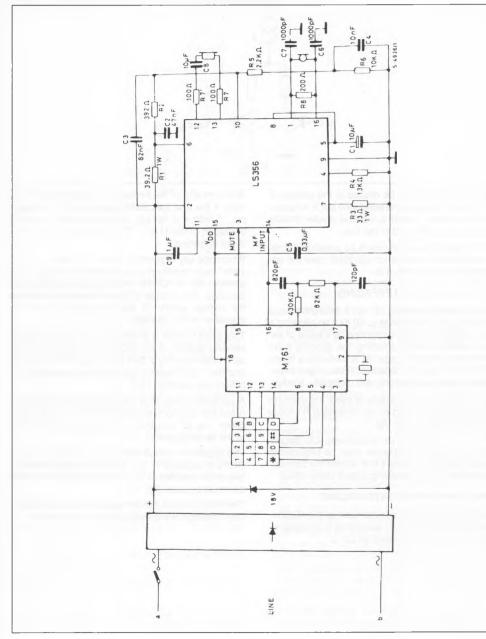
The application circuit shown in fig. 9 fulfils the EU-ROPE II standard (-6, -8 dBm). If the EUROPE I levels are required (-9, -11 dBm) an external divider must be used (fig. 10).

The mute function can be used also when a temporary inhibition of the output signal is requested.



## **APPLICATION INFORMATION**





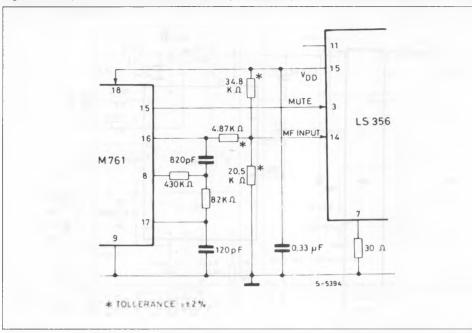
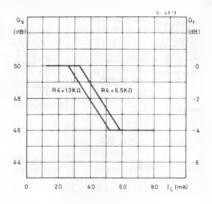
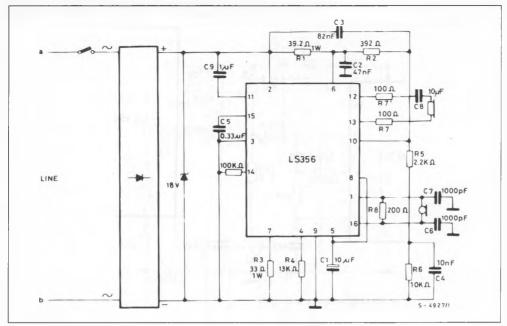


Figure 10 : Application Circuit with Multifrequency (EUROPE I).

Figure 11 : Sending and Receiving Gain vs. Line Current (application circuit of fig. 13).









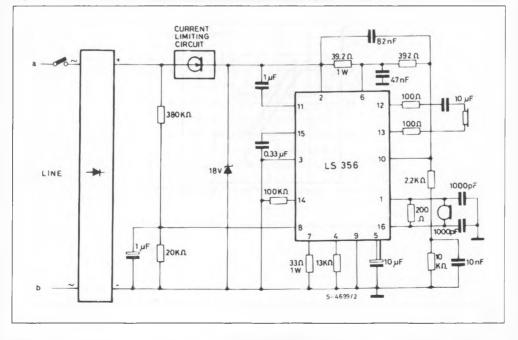
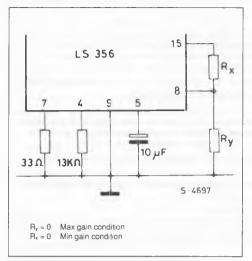
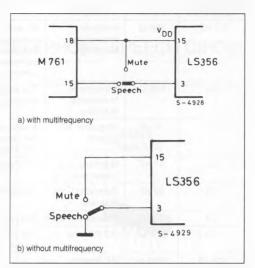




Figure 14 : Application Circuit with Fixed Gain Operation.



In addition to the above mentioned applications, different values for the external components can be used in order to satisfy different requirements. Figure 15 : External Mute Function.



The following table (refer to the application circuit of fig. 9) can help the designers.

Component	Value	Purpose	Note
R1	39.2 Ω	Bridge	R1 controls the receiving gain. When high current values are allowed, R1 must be able to dissipate up to 1 W.
R2	392 Ω	Resistors	The Ratio R2/R1 fixes the amount of signal delivered to the line. R1 helps in fixing the DC characteristic (see R3 note).
R3	33 Ω	Line Current Sensing. Fixing DC Characteristic	The relationships involving R3 are : $- Z_{ML} = (20 \text{ R3}//ZB) + \text{R1}$ $- \overline{G}_s = K \cdot \frac{Z_L//Z_{ML}}{R3}$ $- V_L = (I_L - I_O) (R3 + R1) + V_O; V_O = 3.8 \text{ V}.$ Without any problem it is possible to have a $Z_{ML}$ ranging from 600 up to 900 $\Omega$ . As far as the power dissipation is concerned, see R1 note.
R4	13 ΚΩ	Bias Resistor	The suggested value assures the minimum operating current. It is possible to increase the supply current by decreasing R4 (they are inversely proportional), in order to achieve the shifting of the AGC starting point.
R5	2.2 ΚΩ	Balance Network	It it possible to change R5 and R6 values in order to improve the matching to different lines ; in any case :
R6	10 KΩ		$\frac{Z_B}{Z_L} = \frac{R}{R} \frac{2}{1}$ $Z_B = R5 + \frac{R6}{X_{C4}}$

#### **APPLICATION INFORMATION**

Component	Value	Purpose	Note
R7-R7' 100 Ω		Receiver Impedance Matching	R7 and R7' must be equal : the suggested value is good for matching to dynamic capsule ; there is no problem in increasing and decreasing (down to 0 $\Omega$ ) this value. A DC decoupling must be inserted when low resistance levels are used to stop the current due to the receiver output offset voltage (max 200 mV).
R8	R8 200 Ω Microphone Impedance Matching		The suggested value is typical for a dynamic microphone, but it is possible to choose R8 in a wide range.
C1	C1 10 µF Regulator AC Bypass		A value greater than 10 $\mu F$ gives a system start time to high for low current line during MF operation ; a lower value gives an alteration of the AC line impedance at low frequency.
C2	47 nF	Matching to a Capacitive Line	C2 changes with the characteristics of the transmission line.
C3	82 nF	Receiving Gain Flatness	C3 depends on balancing and line impedance versus frequency.
C4	10 nF	Balance Network	See note for R5, R6.
C5	0.33 μF	DC Filtering	The C5 range is from 0.1 $\mu F$ to 0.47 $\mu F$ . The lowest value is ripple limited, the higher value is starting up time limited.
C6-C7	1000 pF	RF Bypass	
C8	10 µF	Receiving Output DC Decoupling	See note for R7, R7'.
C9	1 μF	Receiving Input DC Decoupling	

## **APPLICATION INFORMATION** (continued)

