19-0204; Rev 2; 7/96

EVALUATION KIT MANUAL FOLLOWS DATA SHEET

MAXIM

5V/3.3V/3V/Adjustable-Output, Step-Up/Step-Down DC-DC Converters

General Description

The MAX877L/MAX878L/MAX879L are pulse-skipping, step-up/step-down DC-DC converters that provide a regulated output from inputs both above and below the output. They require only three external components—an inductor (typically 22µH) and two filter capacitors.

The MAX877L delivers a regulated 5V output from 2.5V to 4.5V inputs. The MAX878L generates pin-selectable voltages of 3.0V or 3.3V from 1.5V to 4.5V inputs. The MAX879L output can be adjusted from 2.5V to 6V via an external resistor divider from 2.5V to 4.5V inputs.

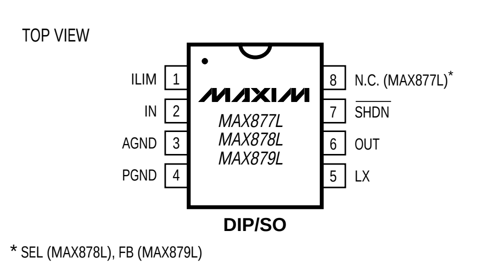
A unique high-power, internal, synchronous rectifier design (Active Rectifier™) enables the devices to regulate in a switched linear mode if the input voltage is higher than the desired output voltage. When the input voltage falls below the output voltage, the MAX877L/MAX878L/MAX879L will smoothly switch into a pulse-skipping boost mode and step up from input voltages as low as 1V. In shutdown, the active rectifier disconnects the output from the source. This stops the current drain from input to output associated with conventional step-up converters.

High-frequency operation (up to 300kHz) allows the use of small surface-mount inductors. Supply current is 195µA under no load, and only 20µA in shutdown mode. For 1-cell (1V) step-up converters with similar performance and the same pinout, refer to the MAX777L/MAX778L/MAX779L data sheet.

Applications

Two or Three NiCd Cells to 3V/3.3V Conversion
Three Alkaline Cells to 5V Conversion
One Lithium Cell to 3V/3.3V Conversion
Pagers
Palmtop and Notebook Computers
Battery-Powered and Hand-Held Instruments

Pin Configuration



Active Rectifier is a trademark of Maxim Integrated Products.

Features

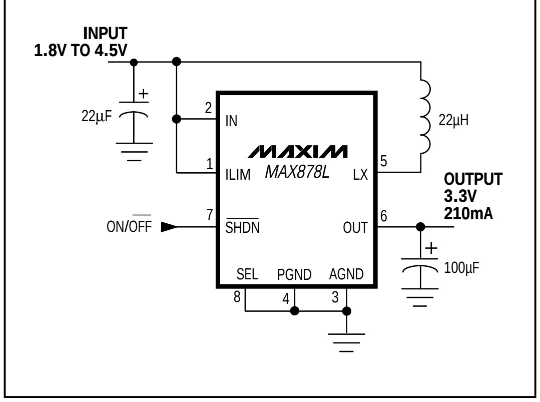
- ♦ **Regulates from Inputs Above & Below the Output**
- ♦ **1V to 4.5V Supply-Voltage Range**
- ♦ **Internal 1A Active Rectifier with Input-to-Output Disconnect in Shutdown**
- ♦ **Up to 210mA Load Currents, Guaranteed**
- ♦ **85% Efficiency**
- ♦ **Only Three External Components**
- ♦ **Adjustable Current Limit**
- ♦ **195µA Quiescent Supply Current**
- ♦ **20µA Shutdown Supply Current**
- ♦ **3V/3.3V/5V and Adjustable Output Voltage Versions**
- ♦ **Available in 8-Pin DIP and SO Packages**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX877LCPA	0°C to +70°C	8 Plastic DIP
MAX877LCSA	0°C to +70°C	8 SO
MAX877LC/D	0°C to +70°C	Dice*
MAX877LEPA	-40°C to +85°C	8 Plastic DIP
MAX877LESA	-40°C to +85°C	8 SO
MAX877LMJA	-55°C to +125°C	8 CERDIP

Ordering Information continued at end of data sheet.
** Contact factory for dice specifications.*

Typical Operating Circuit



ABSOLUTE MAXIMUM RATINGS

Supply Voltage (IN to PGND)......................................0V to 4.5V
Output Short-Circuit Duration to PGND, AGND (Note 1)....30sec
Voltage Applied to:
  LX (switch off) ....-0.3V to 4.5V
    (switch on) ....30sec short to IN or OUT
  OUT, $\overline{SHDN}$..-0.3V to +7V
  FB ....-0.3V to (OUT + 0.3V)
AGND to PGND...-0.3V, +0.3V
Reverse Battery Current...900mA

Continuous Power Dissipation (T_A = +70°C)
  Plastic DIP (derate 9.09mW/°C above +70°C) 727mW
  SO (derate 5.88mW/°C above +70°C).........................471mW
  CERDIP (derate 8.00mW/°C above +70°C).................640mW
Operating Temperature Ranges:
  MAX87_LC_A...0°C to +70°C
  MAX87_LE_A..-40°C to +85°C
  MAX87_LMJA ....-55°C to +125°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10sec) ....+300°C

Note 1: The output may be shorted to ground continuously if the package power dissipation is not exceeded.

Stresses beyond those listed under “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{IN} = 2.7V, I_{LOAD} = 0mA, LX = 22µH, C_{OUT} = 100µF, $\overline{SHDN}$ and ILIM connected to IN, AGND connected to PGND, T_A = T_{MIN} to T_{MAX}, typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Minimum Start-Up Voltage (Notes 2, 6)	I_{LOAD} = 0mA, T_A = +25°C		1		V
	MAX877L/MAX879L (V_{OUT} = 5V), 0mA < I_{LOAD} < 180mA, T_A = +25°C			2.5	
	MAX878L/MAX879L (V_{OUT} = 3.3V), 0mA < I_{LOAD} < 120mA, T_A = +25°C			1.5	
Maximum Operating Voltage	(Notes 2, 3)	4.5			V
Output Voltage (MAX879L set to 5V) (Note 3)	MAX877LC/MAX879LC: 0mA ≤ I_{LOAD} ≤ 240mA, 2.7V ≤ V_{IN} ≤ 4.5V; MAX877LE/MAX879LE: 0mA ≤ I_{LOAD} ≤ 220mA, 2.7V ≤ V_{IN} ≤ 4.5V; MAX877LM/MAX879LM: 0mA ≤ I_{LOAD} ≤ 180mA, 2.7V ≤ V_{IN} ≤ 4.5V	4.80	5.00	5.20	V
Output Voltage (MAX879L set to 3.3V) (Note 3)	SEL = 0V: MAX878LC/MAX879LC: 0mA ≤ I_{LOAD} ≤ 210mA, 1.8V ≤ V_{IN} ≤ 4.5V; MAX878LE/MAX879LE: 0mA ≤ I_{LOAD} ≤ 200mA, 1.8V ≤ V_{IN} ≤ 4.5V; MAX878LM/MAX879LM: 0mA ≤ I_{LOAD} ≤ 180mA, 1.8V ≤ V_{IN} ≤ 4.5V	3.17	3.30	3.43	V
	SEL = Open: MAX878LC: 0mA ≤ I_{LOAD} ≤ 210mA, 1.8V ≤ V_{IN} ≤ 4.5V; MAX878LE: 0mA ≤ I_{LOAD} ≤ 200mA, 1.8V ≤ V_{IN} ≤ 4.5V; MAX878LM: 0mA ≤ I_{LOAD} ≤ 180mA, 1.8V ≤ V_{IN} ≤ 4.5V	2.88	3.00	3.12	
Output Voltage Range	MAX879L, I_{LOAD} = 0mA (Note 4)	2.5		6.0	V
Efficiency	MAX877L/MAX879L (V_{OUT} = 5V), I_{LOAD} = 100mA, V_{IN} = 4V		85		%
	MAX878L/MAX879L (V_{OUT} = 3.3V), I_{LOAD} = 100mA, V_{IN} = 2.5V		82		
No-Load Supply Current	I_{LOAD} = 0mA (switch off)		195	310	µA
Shutdown Supply Current	$\overline{SHDN}$ = 0V, MAX87_LC, MAX87_LE		20	30	µA
	$\overline{SHDN}$ = 0V, MAX87_LM		20	35	
$\overline{SHDN}$ Bias Current	0V < $\overline{SHDN}$ < V_{IN}		15	100	nA
	V_{IN} < $\overline{SHDN}$ < 5V		12	40	µA

ELECTRICAL CHARACTERISTICS (continued)

(V_{IN} = 2.7V, I_{LOAD} = 0mA, LX = 22µH, C_{OUT} = 100µF, $\overline{SHDN}$ and ILIM connected to IN, AGND connected to PGND, T_A = T_{MIN} to T_{MAX}, typical values are at T_A = +25°C, unless otherwise noted.)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
$\overline{SHDN}$ Threshold	V_{IN} = 1V to 4.5V		V_{IN}/2 +0.25		V
	V_{IN} = 2.7V	1.3		1.7	
$\overline{SHDN}$ Enable Delay			150		µs
Current Limit			1.0		A
Current-Limit Temperature Coefficient			-0.3		%/°C
Switch Saturation Voltage	I_{SW} = 400mA		0.275		V
	I_{SW} = 600mA		0.33		
	I_{SW} = 1000mA		0.50		
Maximum Switch On Time	V_{IN} = 2.5V		4.0		µs
	V_{IN} = 1.8V		5.9		
	V_{IN} = 1V		12.6		
Minimum Switch Off Time	MAX877L/MAX879L		1.3		µs
	MAX878L		2.3		
Rectifier Forward Voltage Drop	I_{SW} = 400mA		0.21		V
	I_{SW} = 600mA		0.31		
	I_{SW} = 1000mA		0.50		
Error-Comparator Trip Point (V_{REF})	MAX879L, V_{IN} = 1.8V to 4.5V (Note 5)	197.5	202.5	207.5	mV
FB Pin Bias Current	MAX879L		10	40	nA
Switch Off Leakage Current			0.1		µA
Rectifier Off Leakage Current			0.1		µA

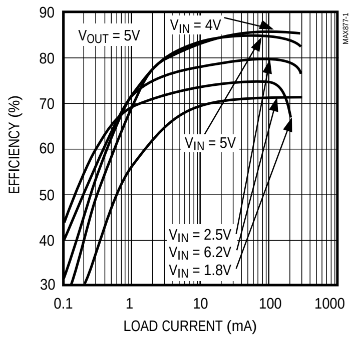
Note 2: Output in regulation, V_{OUT} = V_{OUT} (nominal) ±4%.
Note 3: At high V_{IN} to V_{OUT} differentials, the maximum load current is limited by the maximum allowable power dissipation in the package (see *Absolute Maximum Ratings* and Maximum Output Current graphs in the *Typical Operating Characteristics*).
Note 4: Minimum value is production tested. Maximum value is guaranteed by design and is not production tested.
Note 5: V_{OUT} is set to a target value of 5V by 0.1% external feedback resistors. V_{OUT} is measured to be within 5V ±2.5% to guarantee error-comparator trip point.
Note 6: Startup guaranteed under these load conditions.

5V/3.3V/3V/Adjustable-Output, Step-Up/Step-Down DC-DC Converters

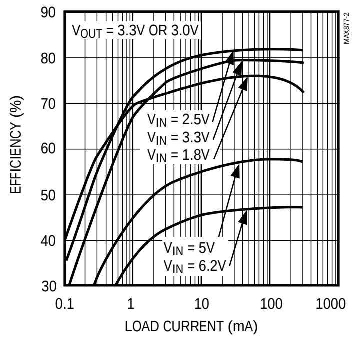
Typical Operating Characteristics

(Circuit of Figure 4, T_A = +25°C, unless otherwise noted.)

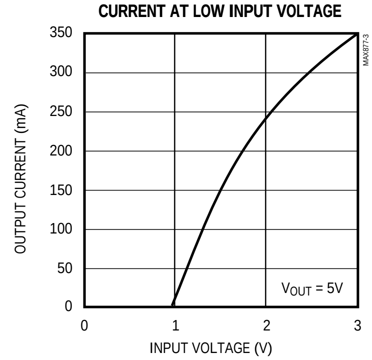
MAX877L/MAX879L EFFICIENCY vs. LOAD CURRENT



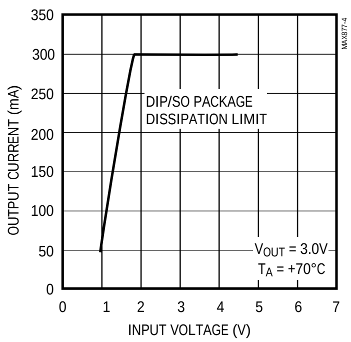
MAX878L EFFICIENCY vs. LOAD CURRENT



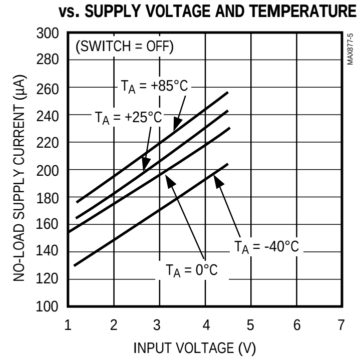
MAX877L/MAX879L MAXIMUM OUTPUT CURRENT AT LOW INPUT VOLTAGE



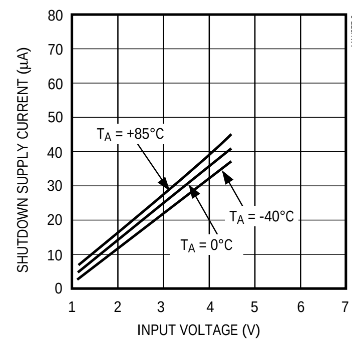
MAX878L MAXIMUM OUTPUT CURRENT vs. INPUT VOLTAGE



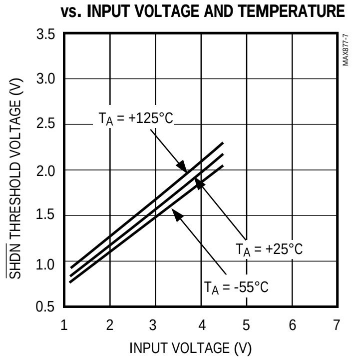
NO-LOAD SUPPLY CURRENT vs. SUPPLY VOLTAGE AND TEMPERATURE



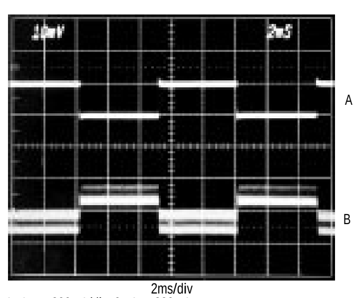
SHUTDOWN SUPPLY CURRENT vs. INPUT VOLTAGE AND TEMPERATURE



$\overline{\text{SHDN}}$ THRESHOLD VOLTAGE vs. INPUT VOLTAGE AND TEMPERATURE

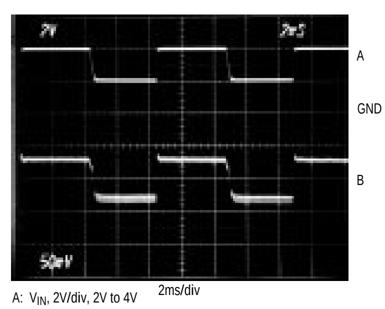


LOAD-TRANSIENT RESPONSE



A: I_{OUT}, 200mA/div, 0mA to 200mA
B: V_{OUT}, 50mV/div, AC COUPLED
MAX878L, V_{OUT} = 3.3V, V_{IN} = 2.5V

LINE-TRANSIENT RESPONSE

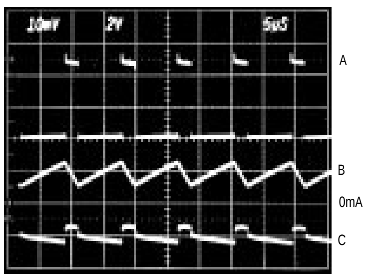


A: V_{IN}, 2V/div, 2V to 4V
B: V_{OUT}, 50mV/div, AC COUPLED
I_{OUT} = 240mA
MAX878L, V_{OUT} = 3.3V, $C_{INBYPASS}$ = 47µF

Typical Operating Characteristics (continued)

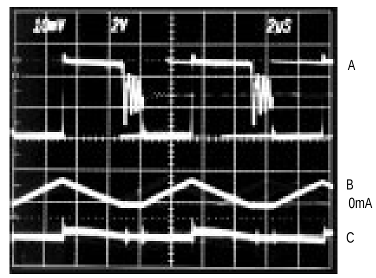
(Circuit of Figure 4, T_A = +25°C, unless otherwise noted.)

SWITCHING WAVEFORMS— CONTINUOUS CONDUCTION



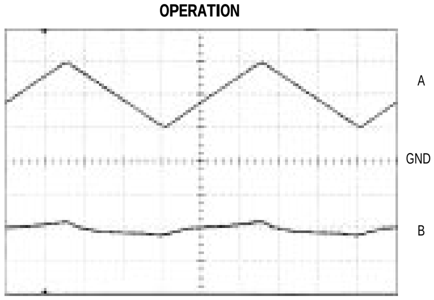
5µs/div
A: SWITCH VOLTAGE (LX PIN), 2V/div
B: INDUCTOR CURRENT, 0.5A/div
C: OUTPUT VOLTAGE RIPPLE, 50mV/div
MAX877L, V_{IN} = 1.5V, I_{OUT} = 100mA

SWITCHING WAVEFORMS— DISCONTINUOUS CONDUCTION



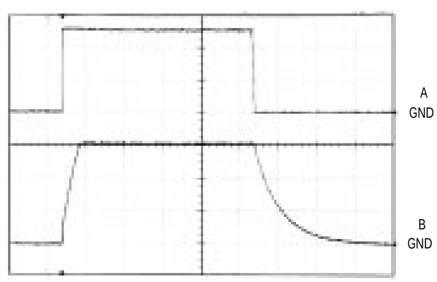
2µs/div
A: SWITCH VOLTAGE (LX PIN), 2V/div
B: INDUCTOR CURRENT, 0.5A/div
C: OUTPUT VOLTAGE RIPPLE, 50mV/div
MAX877L, V_{IN} = 3V, I_{OUT} = 70mA

STEP-UP/STEP-DOWN OPERATION



20ms/div
A: INPUT VOLTAGE, 2V/div, 2V to 4.5V
B: OUTPUT VOLTAGE, 50mV/div
MAX878L, I_{OUT} = 100mA, V_{OUT} = 3.3V

MAX878L START-UP TIME



10ms/div
A: $\overline{SHDN}$, 2V/div
B: V_{OUT}, 1V/div
V_{OUT} = 3V

5V/3.3V/3V/Adjustable-Output, Step-Up/Step-Down DC-DC Converters

Pin Description

PIN	NAME	FUNCTION
1	ILIM	Sets switch current-limit input. Connect to IN for 1A current limit. A resistor from ILIM to IN sets lower peak inductor currents.
2	IN	Input supply.
3	AGND	Analog ground. Not internally connected to PGND.
4	PGND	Power ground must be low impedance; solder directly to ground plane or star ground. Connect to AGND, close to the device.
5	LX	1A NPN power switch collector and active-rectifier PNP emitter.
6	OUT	Voltage output. Connect filter capacitor close to pin.
7	$\overline{SHDN}$	Shutdown input disables power supply when low. Also disconnects load from input. Threshold is set at $V_{IN}/2$. Connect to IN for normal operation.
8	N.C. (MAX877L)	No connect, not internally connected.
	SEL (MAX878L)	Selects the main output voltage: 3.3V when connected to AGND, 3.0V when left open.
	FB (MAX879L)	Feedback input for adjustable-output operation. Connect to an external voltage divider between V_{OUT} and AGND.

Detailed Description

Operating Principle

The MAX877L/MAX878L/MAX879L combine a switch-mode regulator with an NPN bipolar power switch and current limit, a precision voltage reference, and a synchronous rectifier—all in a single monolithic device. In shutdown mode, the internal rectifier is completely turned off and disconnects the load from the source. Only two external components are required in addition to the input bypass capacitor—a 22µH inductor, and a 100µF filter capacitor.

A minimum-off-time, current-limited, pulse-frequency-modulation (PFM) control scheme combines the high output power and efficiency of pulse-width modulation (PWM) with the low quiescent currents of traditional PFM pulse skippers.

External conditions (inductor value, load, and input voltage) determine the way the converter operates, as follows:

At light loads, the current through the inductor starts at zero, rises to a peak value, and drops down to zero in each cycle (discontinuous-conduction mode). In this case, the switching frequency is governed by a pair of one-shots, which set a maximum on-time inversely proportional to V_{IN} [$t_{ON} = 8.8/(V_{IN} - 0.25)$] and a minimum off-time (1.3µs for MAX877L/MAX879L, or 2.3µs for MAX878L). With a 22µH inductor, LX's peak current is about 400mA and is independent of input voltage. Efficiency at light loads is improved because of lower peak currents.

At very light loads, more energy is stored in the coil than is required by the load in each cycle. The converter regulates by skipping entire cycles. Efficiency is typically 65% to 75% in the pulse-skipping mode. Pulse-skipping waveforms can be irregular, and the output waveform contains a low-frequency component. Larger, low equivalent-series-resistance (ESR) filter capacitors can help reduce the ripple voltage if needed.

At heavy loads above approximately 100mA, the converter enters continuous-conduction mode, where current always flows in the inductor. The switch ON state is controlled on a cycle-by-cycle basis, either by the t_{ON}(max) time or the preset current limit in the switch. This prevents exceeding the switch current rating or saturating the inductor. At very heavy loads, the inductor current self-oscillates between this peak current limit and some lower value governed by the minimum off-time, the inductance value, and the input/output differential.

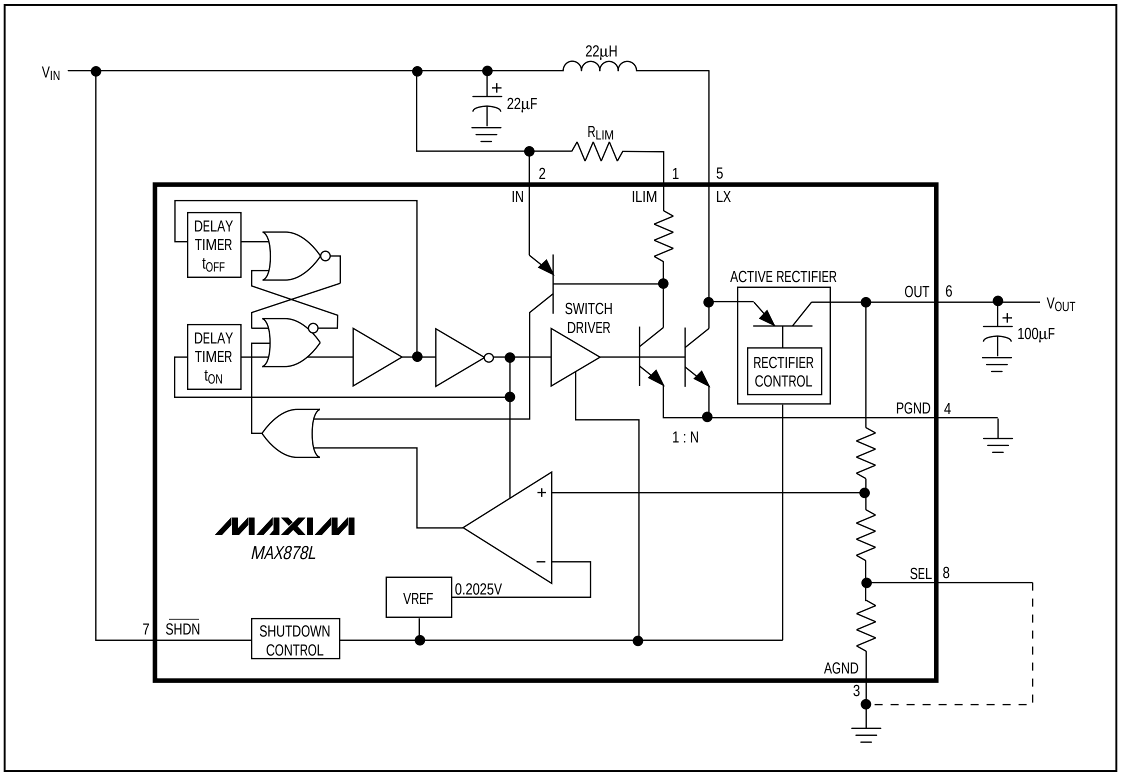


Figure 1. MAX878L Block Diagram

With ILIM shorted to IN, the peak switch current of the internal NPN power switch is set to 1A. It can be set to a lower value by connecting a resistor between ILIM and IN (see *Current Limit* section). This enables the use of physically smaller inductors with lower saturation-current ratings. At 1A, the switch voltage drop (V_{SW}) is about 500mV. V_{SW} decreases to about 250mV at 0.1A.

Conventional PWM converters generate constant-frequency switching noise, while this architecture produces variable-frequency switching noise. The output ripple is the product of the peak inductor current and the output capacitor's ESR. Unlike conventional pulse-skippers, the MAX877L/MAX878L/MAX879L peak currents are scaled down at light loads, resulting in lower output ripple.

Step-Down Mode and Power Dissipation

In battery-powered applications, for example, where the input voltage exceeds the output voltage, the MAX877L/MAX878L/MAX879L behave as “switched” linear regulators. If the output voltage starts to drop, the switch turns on and energy is stored in the coil, as in normal step-up mode. After the switch turns off, the voltage at LX flies high. The active rectifier turns on when LX rises above V_{IN}. As in a linear regulator, the voltage difference between V_{IN} and V_{OUT} appears across the rectifier (actually a PNP transistor) until the current goes to zero and the rectifier turns off. At high V_{IN} to V_{OUT} differentials, the maximum load current is limited by the maximum allowable power dissipation in the package (see *Typical Operating Characteristics*).

Active Rectifier

The internal active rectifier of the MAX877L/MAX878L/MAX879L replaces the external Schottky catch diode in normal boost operation. The rectifier consists of a PNP pass transistor and a unique control circuit which, in shutdown mode, entirely disconnects the load from the source. This is a distinct advantage over standard boost topologies, since it prevents battery drain in shutdown. The MAX877L/MAX878L/MAX879L can withstand a momentary short at the output in normal operation.

The active rectifier also acts as a zero-dropout regulator if the input exceeds the regulated output.The device still switches to deliver power to the output, and the difference between the input and output voltage appears across the rectifier. Efficiency is similar to that of a linear regulator if the MAX877L/MAX878L/MAX879L are used as step-down converters. The maximum output current (I_{OUT} (MAX)) with larger input/output differentials is determined by package power dissipation. I_{OUT} (MAX) can be approximated by:

$$I_{OUT}\text{ (MAX)} \approx \left(\frac{P_{DISS}}{(V_{IN} - V_{OUT})}\right) \times 0.9$$

Shutdown

Shutdown ($\overline{SHDN}$) is a high-impedance, active-low input. Connect it to IN for normal operation. Keeping $\overline{SHDN}$ at ground holds the converters in shutdown mode. Since the active rectifier is turned off in this mode, the path from input to load is cut, and the output effectively drops to 0V. The supply current in shutdown mode ranges from 4µA at V_{IN} = 1V to 50µA at V_{IN} = 4.5V. The shutdown-circuit threshold is set nominally to V_{IN}/2 + 250mV. When $\overline{SHDN}$ is below this threshold, the device is shut down; it is enabled with $\overline{SHDN}$ above the threshold. When driven from external logic, $\overline{SHDN}$ can be driven to a higher voltage than V_{IN}, (4.5V max).

Current Limit

Connecting ILIM to IN sets an LX current limit of 1A. For smaller output power levels that do not require the maximum peak current, reduce the peak inductor current by connecting a resistor between ILIM and IN. This optimizes overall efficiency and allows very small, low-cost coils with lower current ratings. See Figure 2 to select the resistor (see also *Inductor Selection* section).

Output Voltage Selection

The MAX877L's output voltage is fixed at 5V. The MAX878L's output voltage can be set to 3V by leaving the SEL pin open, or to 3.3V by connecting SEL to AGND.

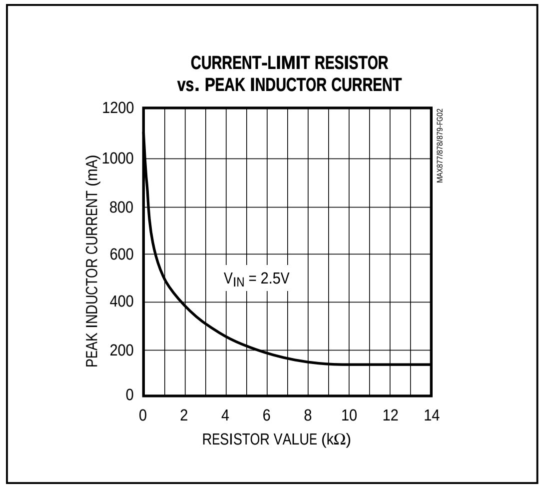


Figure 2. Current-Limit Resistor vs. Peak Inductor Current

The MAX879L's output voltage is set by two resistors, R1 and R2 (Figure 3), which form a voltage divider between the output and the FB pin. The output voltage can be set from 2.5V to 6.0V by the equation:

$$V_{OUT} = V_{REF}\ \frac{(R1 + R2)}{R2}$$

where V_{REF} = 0.2025V.

To simplify the resistor selection:

$$R1 = R2\ \left(\frac{V_{OUT}}{V_{REF}} - 1\right)$$

Since the input current at FB has a maximum of 40nA, large values (10kΩ to 50kΩ for R2) can be used without significant accuracy loss. For 1% error, the current through R2 should be at least 100 times FB's bias current.

When large values are used for the feedback resistors (R1 > 50kΩ), stray output impedance at FB can add a "lag" to the feedback response, destabilizing the regulator and creating a larger ripple at the output. Lead lengths and circuit board traces at the FB node should be kept short. Reduce ripple by adding a "lead" compensation capacitor (C3, 100pF to 50nF) in parallel with R1.

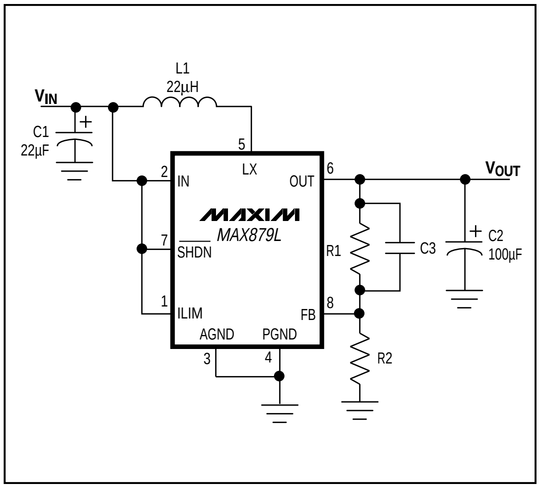


Figure 3. MAX879L Adjustable Voltage

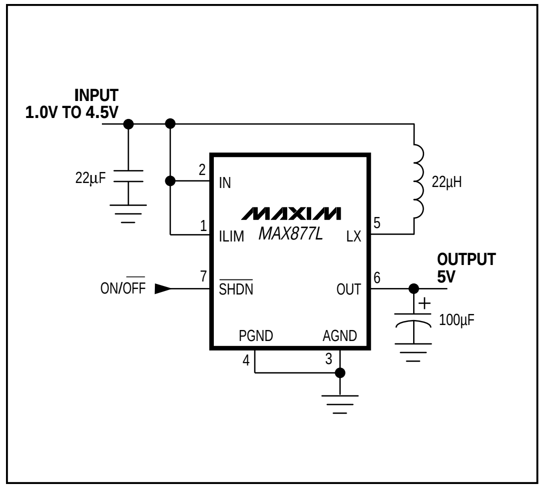


Figure 4. MAX877L Standard Application Circuit

Applications Information

Figure 4 shows a MAX877L step-up application circuit. This circuit starts up and operates with inputs ranging from 1.0V to 4.5V. Start-up time is a function of the load, typically less than 5ms. Output current capability is a function of the input voltage (see *Typical Operating Characteristics*).

The converters will regulate down to the output voltage and seamlessly switch into boost mode as the input drops below the output voltage. This is especially useful in battery-powered applications, where the battery voltage may initially exceed the output voltage. To generate 3.3V from three alkaline cells in series, the input ranges from 4.5V to 1.8V. When the battery pack is fresh, the MAX878L will step down with the active rectifier acting as the switch. As the batteries approach 3.3V, or the desired output voltage, the converter's control circuitry will ensure a smooth transition into step-up mode. The converter operates until the batteries are less than 3V; efficiency is typically 80% with fresh batteries, and is close to 85% at V_{IN} = 4V.

Inductor Selection

The 22µH inductor shown in the *Typical Operating Circuit* is sufficient for most MAX877L/MAX878L/MAX879L designs. Other inductor values ranging from 10µH to 47µH are also suitable. The inductor should have a saturation rating equal to or greater than the peak switch-current limit, which is 1A without an external current limit (ILIM connected to IN). It is acceptable to operate the inductor at 120% of its saturation rating; however, this may slightly reduce efficiency. For highest efficiency, use an inductor with a low **DC resistance**, preferably under 0.2Ω. Table 1 lists suggested inductor suppliers.

Capacitor Selection

The 100µF, 10V surface-mount tantalum (SMT) output capacitor shown in the *Typical Operating Circuit* will provide a 25mV output ripple or less, stepping up from 3V to 5V at 200mA. Smaller capacitors, down to 10µF, are acceptable for light loads or in applications that can tolerate higher output ripple. The input capacitor may be omitted if the supply has low output impedance and the input lead length is less than 2 inches (5cm) or the loads are small.

The primary factor in selecting both the output and input filter capacitor is low ESR. The ESR of both bypass and filter capacitors affects efficiency. Optimize performance by increasing filter capacitors or using specialized low-ESR capacitors. The smallest low-ESR SMT tantalum capacitors currently available are Sprague 595D or 695D series. Sanyo OS-CON organic-semiconductor through-hole capacitors also exhibit very low ESR, are rated for the wide temperature range, and are especially suitable for operation at cold temperatures (below 0°C).

Table 1 lists suggested capacitor suppliers.

5V/3.3V/3V/Adjustable-Output, Step-Up/Step-Down DC-DC Converters

Layout

The MAX877L/MAX878L/MAX879L's high peak currents and high-frequency operation make PC layout important for minimum ground bounce and noise. Locate input bypass and output filter capacitors close to the device pins. All connections to the FB pin (MAX879L) should also be kept as short as possible. A ground plane is recommended. Solder AGND (pin 3) and PGND (pin 4) directly to the ground plane. Refer to the MAX877L/MAX878L/MAX879L evaluation kit (EV kit) manual for a suggested surface-mount layout.

Table 1. Component Suppliers

PRODUCTION METHOD	INDUCTORS	CAPACITORS
Surface Mount	Sumida CD54-220 (22µH) Murata-Erie LQHYN1501K04M00-D5 (15µH) Coilcraft DO3316-223 (22µH) Coiltronics CTX20-1 (22µH)	Sprague 595D Sprague 695D Matsuo 267 series AVX TPS series
Miniature Through-Hole	Sumida RCH654-220 (22µH)	Sanyo OS-CON (low-ESR organic semiconductor)
Low-Cost Through-Hole	Coilcraft PCH-27-223 (22µH)	Nichicon PL series (low-ESR electrolytic) United Chemi-Con LXF series

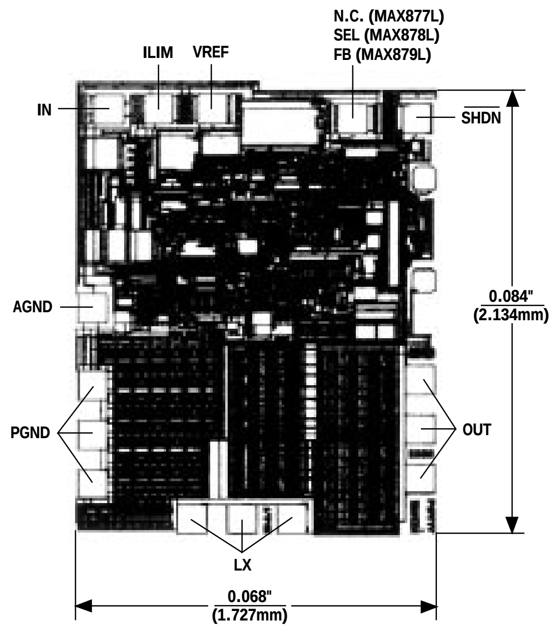
AVX	USA:	(803) 946-0690 (800) 282-4975	FAX (803) 626-3123
Coilcraft	USA:	(847) 639-6400	FAX (847) 639-1469
Coiltronics	USA:	(561) 241-7876	FAX (561) 241-9339
Matsuo	USA:	(714) 969-2491	FAX (714) 960-6492
Murata-Erie	USA:	(814) 237-1431 (800) 831-9172	FAX (814) 238-0490
Nichicon	USA:	(847) 843-7500	FAX (847) 843-2798
	Japan:	(81) 7-5231-8461	FAX (81) 7-5256-4158
Sanyo	USA:	(619) 661-6835	FAX (619) 661-1055
	Japan:	(81) 7-2070-6306	FAX (81) 7-2070-1174
Sprague	USA:	(603) 224-1961	FAX (603) 224-1430
Sumida	USA:	(847) 956-0666	FAX (847) 956-0702
	Japan:	(81) 3-3607-5111	FAX (81) 3-3607-5144
United Chemi-Con	USA:	(714) 255-9500	FAX (714) 255-9400

Ordering Information (continued)

PART	TEMP. RANGE	PIN-PACKAGE
MAX878LCPA	0°C to +70°C	8 Plastic DIP
MAX878LCSA	0°C to +70°C	8 SO
MAX878LC/D	0°C to +70°C	Dice*
MAX878LEPA	-40°C to +85°C	8 Plastic DIP
MAX878LESA	-40°C to +85°C	8 SO
MAX878LMJA	-55°C to +125°C	8 CERDIP
MAX879LCPA	0°C to +70°C	8 Plastic DIP
MAX879LCSA	0°C to +70°C	8 SO
MAX879LC/D	0°C to +70°C	Dice*
MAX879LEPA	-40°C to +85°C	8 Plastic DIP
MAX879LESA	-40°C to +85°C	8 SO
MAX879LMJA	-55°C to +125°C	8 CERDIP

** Contact factory for dice specifications.*

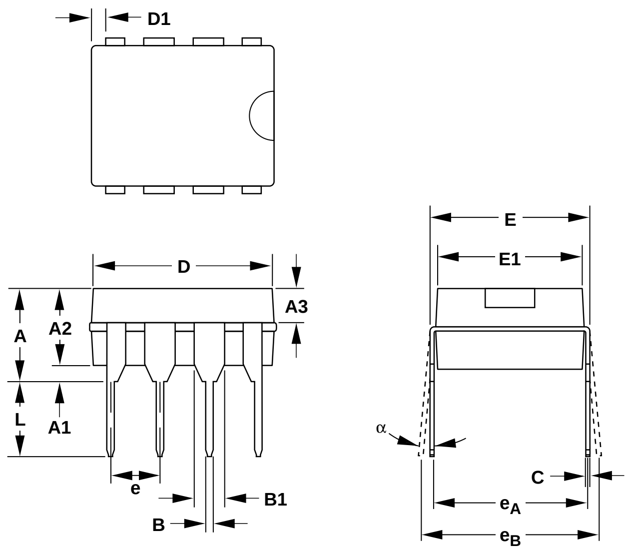
Chip Topography



TRANSISTOR COUNT: 170

SUBSTRATE CONNECTED TO AGND

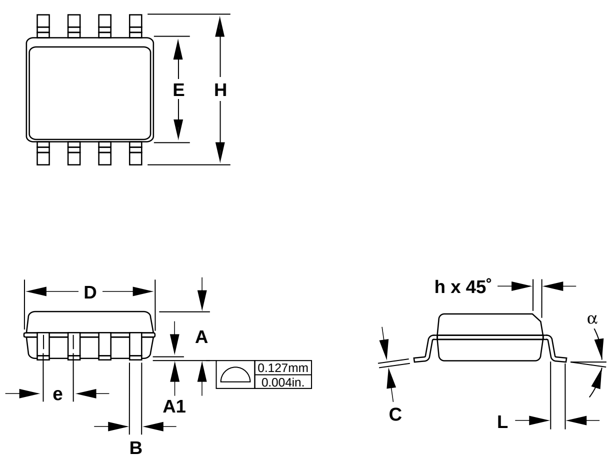
Package Information



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	–	0.200	–	5.08
A1	0.015	–	0.38	–
A2	0.125	0.175	3.18	4.45
A3	0.055	0.080	1.40	2.03
B	0.016	0.022	0.41	0.56
B1	0.050	0.065	1.27	1.65
C	0.008	0.012	0.20	0.30
D	0.348	0.390	8.84	9.91
D1	0.005	0.035	0.13	0.89
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
e	0.100 BSC		2.54 BSC	
e_A	0.300 BSC		7.62 BSC	
e_B	–	0.400	–	10.16
L	0.115	0.150	2.92	3.81
α	0˚	15˚	0˚	15˚

21-324A

8-PIN PLASTIC DUAL-IN-LINE PACKAGE



DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
D	0.189	0.197	4.80	5.00
E	0.150	0.157	3.80	4.00
e	0.050 BSC		1.27 BSC	
H	0.228	0.244	5.80	6.20
h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27
α	0˚	8˚	0˚	8˚

21-325A

8-PIN PLASTIC SMALL-OUTLINE PACKAGE