$V_{CC1} = 1, 15$
$V_{CC2} = 16$
$V_{EE} = 8$

$P_D = 80$ mW typ/gate (No Load)
$t_{pd} = 2.4$ ns typ (All Outputs Loaded)

Dual 3-input 3-output OR Gate

The MC10110 is designed to drive up to three transmission lines simultaneously. The multiple outputs of this device also allow the wire "OR"-ing of several levels of gating for minimization of gate and package count.

The ability to control three parallel lines from a single point makes the MC10110 particularly useful in clock distribution applications where minimum clock skew is desired. Three V_{CC} pins are provided and each one should be used.