



$$V_{CC1} = 1$$

$$V_{CC2} = 16$$

$$V_{EF} = 8$$

$$t_{pd} = 2.4 \text{ ns typ (Single Ended Input)}$$

$$t_{pd} = 2.0 \text{ ns typ (Differential Input)}$$

$$P_D = 145 \text{ mW typ/pkg}$$

Triple Line Receiver

The MC10114 is a triple line receiver designed for use in sensing differential signals over long lines. An active current source and translated emitter follower inputs provide the line receiver with a common mode noise rejection limit of one volt in either the positive or the negative position. This allows a large amount of common mode noise immunity for extra long lines. Another feature of the MC10114 is that the OR outputs (pins 3, 7, 15) go to a logic low level whenever the inputs are left floating. The outputs are each capable of driving 50 ohm transmission lines.

This device is useful in high speed central processors, digital communication systems, testing and instrumentation systems. The MC10114 can also be used for MOS to MECL interfacing and it is ideal as a sense amplifier for MOS RAM's.

A V_{BB} reference is provided which is useful in making the MC10114 a Schmitt trigger, allowing single-ended driving of the inputs, or other applications where a stable reference voltage is necessary.