$$\begin{array}{ll} 2 & (4 + 5 + 6) \cdot (7 + 9 + 10) \cdot (10 + 11 + 12) \cdot (13 + 14 + 15) \\ 3 & \overline{(4 + 5 + 6) \cdot (7 + 9 + 10) \cdot (10 + 11 + 12) \cdot (13 + 14 + 15)} \end{array}$$

$V_{CC1} = $ Pin 1
$V_{CC2} = $ Pin 16
$V_{EE} = $ Pin 8

$P_D = $ 100 mW typ/pkg (No Load)
$t_{pd} = $ 2.3 ns typ

4-Wide
OR-AND/OR-AND-INVERT
Gate

The MC10121 is a basic logic building block providing the simultaneous OR-AND/OR-AND-INVERT function, useful in data control and digital multiplexing applications.