



Gnd = Pin 16  
 $V_{CC}$  (+5.0 Vdc) = Pin 9  
 $V_{EE}$  (-5.2 Vdc) = Pin 8

$P_D$  = 380 mW typ/pkg (No Load)  
 $t_{pd}$  = 3.5 ns typ (+1.5 Vdc in to 50%o out)

## Quad MTTT to MECL Translator

The MC10124 is a quad translator for interfacing data and control signals between a saturated logic section and the MECL section of digital systems. The MC10124 has MTTT compatible inputs, and MECL complementary open-emitter outputs that allow use as an inverting/non-inverting translator or as a differential line driver. When the common strobe input is at the low logic level "0", it forces all true outputs to a MECL low logic state and all inverting outputs to a MECL high logic state.

Power supply requirements are ground, +5.0 Volts, and -5.2 Volts. Propagation delay of the MC10124 is typically 3.5 ns. The dc levels are standard or Schottky TTL in, MECL 10,000 out.

An advantage of this device is that MTTT level information can be transmitted differentially, via balanced twisted pair lines, to the MECL equipment, where the signal can be received by the MC10115 or MC10116 differential line receivers. The MC10124 is useful in computers, instrumentation, peripheral controllers, test equipment, and digital communications systems.