



COUNTER TRUTH TABLES

BI-QUINARY(Clock connected to C2
and Q3 connected to C1)

COUNT	Q1	Q2	Q3	Q0
0	L	L	L	L
1	H	L	L	L
2	L	H	L	L
3	H	L	L	L
4	L	L	H	L
5	L	L	L	H
6	H	L	L	H
7	L	H	L	H
8	H	H	L	H
9	L	L	H	H

BCD(Clock connected to C1
and Q0 connected to C2)

COUNT	Q0	Q1	Q2	Q3
0	L	L	L	L
1	H	L	L	L
2	L	H	L	L
3	H	L	L	L
4	L	L	H	L
5	H	L	H	L
6	L	H	H	L
7	H	L	H	L
8	L	L	L	H
9	H	L	L	H

V_{CC1} = Pin 1
 V_{CC2} = Pin 16
 V_{EE} = Pin 8

P_D = 370 mW typ/pkg (No Load)
 f_{tog} = 150 MHz typ

Bi-Quinary Counter

The MC10138 is a four bit counter capable of divide by two, five, or ten functions. It is composed of four set-reset master-slave flip-flops. Clock inputs trigger on the positive going edge of the clock pulse.

Set or reset input override the clock, allowing asynchronous "set" or "clear". Individual set and common reset inputs are provided, as well as complementary outputs for the first and fourth bits. True outputs are available at all bits.