12-Bit Parity
Generator-Checker

The MC10160 consists of nine EXCLUSIVE-OR gates in a single package, internally connected to provide odd parity checking or generation. Output goes high when an odd number of inputs are high. Unconnected inputs are pulled to low logic levels allowing parity detection and generation for less than 12 bits.

INPUT	OUTPUT
Sum of High Level Inputs	Pin 2
Even	Low
Odd	High

V_{CC1} — Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 320 mW typ/pkg (No Load)
t_{pd} = 5.0 ns typ

MC10160

MISCELLANEOUS