



V_{CC1} = Pin 1
 V_{CC2} = Pin 16
 V_{EE} = Pin 8

P_D = 315 mW typ/pkg (No Load)
 t_{pd} = 4.0 ns typ

Binary to 1-8 Decoder (Low)

The MC10161 is designed to decode a three bit input word to a one of eight line output. The selected output will be low while all other outputs will be high. The enable inputs, when either or both are high, force all outputs high. This device has high Z input pulldown resistors and open emitter outputs.

TRUTH TABLE

ENABLE INPUTS		INPUTS			OUTPUTS							
$\bar{E}1$	$\bar{E}0$	C	B	A	Q0	Q1	Q2	Q3	Q4	Q5	Q6	Q7
L	L	L	L	L	L	H	H	H	H	H	H	H
L	L	L	L	H	H	L	H	H	H	H	H	H
L	L	L	H	L	H	H	L	H	H	H	H	H
L	L	L	H	H	H	H	H	L	H	H	H	H
L	L	H	L	L	H	H	H	H	L	H	H	H
L	L	H	L	H	H	H	H	H	H	L	H	H
L	L	H	H	L	H	H	H	H	H	H	L	H
L	L	H	H	H	H	H	H	H	H	H	H	L
H	L	L	L	L	H	H	H	H	H	H	H	H
H	L	L	L	H	H	L	H	H	H	H	H	H
H	L	L	H	L	H	H	L	H	H	H	H	H
H	L	L	H	H	H	H	H	L	H	H	H	H
H	H	L	L	L	H	H	H	H	H	H	H	H
H	H	L	L	H	H	L	H	H	H	H	H	H
H	H	L	H	L	H	H	H	L	H	H	H	H
H	H	L	H	H	H	H	H	H	L	H	H	H
H	H	H	L	L	H	H	H	H	H	H	L	H
H	H	H	L	H	H	L	H	H	H	H	L	H
H	H	H	H	L	H	H	H	H	H	H	L	H
H	H	H	H	H	H	H	H	H	H	H	L	H

ϕ = Don't Care