



TRUTH TABLE

ENABLE INPUTS			INPUTS		OUTPUTS							
E	E0	E1	A	B	Q:0	Q:1	Q:2	Q:3	Q00	Q01	Q02	Q03
L	L	L	L	L	L	H	H	H	H	H	H	H
L	L	L	L	H	L	H	H	H	H	H	H	H
L	L	L	H	L	L	H	H	H	H	H	L	H
L	L	L	H	H	L	L	H	H	H	H	H	L
L	L	H	L	L	L	L	H	H	H	H	H	H
L	L	H	L	H	L	L	L	H	H	H	H	H
L	L	H	H	L	L	L	L	H	H	H	H	H
L	L	H	H	H	L	L	L	L	H	H	H	H
L	H	L	L	L	L	L	L	L	L	L	L	L
L	H	L	L	H	L	L	L	L	L	L	L	L
L	H	L	H	L	L	L	L	L	L	L	L	L
L	H	L	H	H	L	L	L	L	L	L	L	L
L	H	H	L	L	L	L	L	L	L	L	L	L
L	H	H	L	H	L	L	L	L	L	L	L	L
L	H	H	H	L	L	L	L	L	L	L	L	L
L	H	H	H	H	L	L	L	L	L	L	L	L
H	L	L	L	L	L	L	L	L	L	L	L	L
H	L	L	L	H	L	L	L	L	L	L	L	L
H	L	L	H	L	L	L	L	L	L	L	L	L
H	L	L	H	H	L	L	L	L	L	L	L	L
H	H	L	L	L	L	L	L	L	L	L	L	L
H	H	L	L	H	L	L	L	L	L	L	L	L
H	H	L	H	L	L	L	L	L	L	L	L	L
H	H	L	H	H	L	L	L	L	L	L	L	L
H	H	H	L	L	L	L	L	L	L	L	L	L
H	H	H	L	H	L	L	L	L	L	L	L	L
H	H	H	H	L	L	L	L	L	L	L	L	L
H	H	H	H	H	L	L	L	L	L	L	L	L

Q: Don't Care

VCC1 = Pin 1

VCC2 = Pin 16

VEE = Pin 8

PD = 325 mW typ/pkg (No Load)

t_{pd} = 4.0 ns typ

Dual Binary to 1-4 Decoder (Low)

The MC10171 is a binary coded 2 line to dual 4 line decoder with selected outputs low. With either E0 or E1 high, the corresponding selected 4 outputs are high. The common enable E, when high, forces all outputs high.

All propagation delay times are equal due to the internal emitter dotting techniques used. High impedance 50 k ohm resistors on all inputs eliminate the need to tie unused inputs to VEE.