TRUTH TABLE

ENABLE INPUTS			INPUTS		OUTPUTS							
E	E0	E1	A	B	Q10	Q11	Q12	Q13	Q00	Q01	Q02	Q03
L	L	L	•	•	H	H	H	H	H	H	H	H
L	L	L	L	L	L	H	H	H	L	H	H	H
L	L	L	H	L	H	L	H	H	H	L	H	H
L	L	L	L	H	H	H	L	H	H	H	L	H
L	L	L	H	H	H	H	H	L	H	H	H	L
L	H	ø	L	L	L	H	H	H	H	H	H	H
L	ø	H	L	L	H	H	H	H	L	H	H	H

ø Don't Care

V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 325 mW typ/pkg (No Load)
t_{pd} = 4.0 ns typ

Dual Binary to 1-4 Decoder (Low)

The MC10171 is a binary coded 2 line to dual 4 line decoder with selected outputs low. With either $\overline{E0}$ or $\overline{E1}$ high, the corresponding selected 4 outputs are high. The common enable $\overline{E}$, when high, forces all outputs high.

All propagation delay times are equal due to the internal emitter dotting techniques used. High impedance 50 k ohm resistors on all inputs eliminate the need to tie unused inputs to V_{EE}.

MC10171

DECODERS