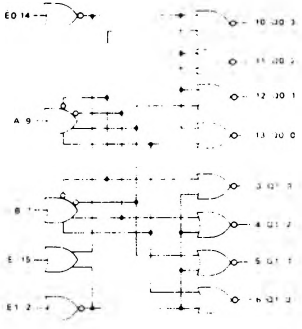

TRUTH TABLE

Ē	Ē1	Ē0	A	B	Q0.0	Q0.1	Q0.2	Q0.3	Q1.0	Q1.1	Q1.2	Q1.3
L	L	L	L	L	L	H	H	H	L	H	H	H
L	L	L	H	L	H	L	H	H	H	L	H	H
L	L	H	L	L	H	H	L	H	H	H	L	H
L	L	H	H	L	H	H	H	L	H	H	H	L
L	H	L	L	L	L	H	H	H	L	H	H	H
L	H	L	H	L	H	L	H	H	H	L	H	H
L	H	H	L	L	H	H	L	H	H	H	L	H
L	H	H	H	L	H	H	H	L	H	H	H	L
H	X	X	X	L	H	H	H	H	H	H	H	H

X = Don't Care

V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 325 mW typ/pkg (No Load)
t_{pd} = 4.0 ns typ

Dual Binary to 1-4 Decoder (High)

The MC10172 is a binary-coded 2 line to dual 4 line decoder with selected outputs high. With either $\bar{E}0$ or $\bar{E}1$ low, the corresponding selected 4 outputs are low. The common enable $\bar{E}$, when high, forces all outputs low.

All propagation delay times are equal. High impedance 50 k ohm resistors on all inputs eliminate the need to tie unused inputs to V_{EE}.