



TRUTH TABLE

SELECT	CLOCK	Q _{n+1}
H	L	D ₀₀
L	L	D ₀₁
H	H	D ₀₀

□ Don't Care

V_{CC} = Pin 16

V_{EE} = Pin 8

P_D = 275 mW typ/pkg (No Load)

t_{pd} = 2.5 ns typ

Quad 2-input Multiplexer/Latch

The MC10173 is a quad two channel multiplexer with latch. It incorporates common clock and common data select inputs. The select input determines which data input is enabled. A high (H) level enables data inputs D₀₀, D₁₀, D₂₀, and D₃₀ and a low (L) level enables data inputs D₀₁, D₁₁, D₂₁, D₃₁. Any change on the data input will be reflected at the outputs while the clock is low. The outputs are latched on the positive transition of the clock. While the clock is in the high state, a change in the information present at the data inputs will not affect the output information.

MC10173

LATCHES