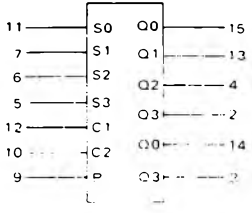
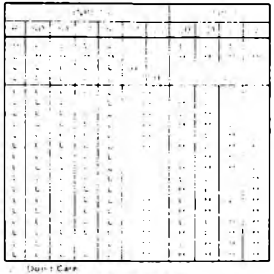


TRUTH TABLE



V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

P_D = 370 mW typ/pkg (No Load
f_{tog} = 150 MHz (typ)

Binary Counter

The MC10178 is a four-bit counter capable of divide-be-two, divide-by-four, divide-by-eight or a divide-by-sixteen function.
Clock inputs trigger on the positive going edge of the clock pulse. Set and Reset inputs override the clock, allowing asynchronouse "set" or "clear". Individual Set and common Reset inputs are provided, as well as complementary outputs for the first and fourth bits. True outputs are available at all bits.