



TRUTH TABLE

Inputs		Output
A	B	O
L	L	H
L	H	L
H	L	L
H	H	H

V_{CC1} = Pin 1
 V_{CC2} = Pin 16
 V_{EE} = Pin 8

P_D = 200 mW typ/pkg (No Load)
 t_{pd} = 2.8 ns typ

Hex Inverter/Buffer

The MC10195 is a Hex Buffer Inverter which is built using six EXCLUSIVE NOR gates. There is a common input to these gates which when placed low or left open allows them to act as inverters. With the common input connected to a high logic level the MC10195 is a hex buffer, useful for high fanout clock driving and reducing stub lengths on long bus lines.

MC10195

DRIVERS-RECEIVERS