

### Two-Modulus Prescaler

The MC12013/12513 is a two-modulus prescaler which will divide by 10 and 11. A MECL-to-MTTL translator is provided to interface directly with the MC12014 Counter Control Logic. In addition, the MC12013/MC12513 provides a buffered clock input and MECL bias voltage source. Details of operation are on the MC12012 data sheet.

- 600 MHz (typ) Toggle Frequency
- $\div 10/11$
- +5.0 or -5.2 V Operation\*
- $P_D = 310$  mW/typ

\*When using +5.0 V supply, apply +5.0 V to pin 1 (V<sub>CC</sub>), pin 6 (MTTL V<sub>CC</sub>), pin 16 (V<sub>CC</sub>), and ground pin 8 (V<sub>EE</sub>). When using -5.2 V supply, ground pin 1 (V<sub>CC</sub>), pin 6 (MTTL V<sub>CC</sub>), and pin 16 (V<sub>CC</sub>) and apply -5.2 V to pin 8 (V<sub>EE</sub>). If the translator is not required, pin 6 may be left open to conserve dc power drain.

