$$X = \overline{A + B + C + D}$$
$$Y = A + B + C + D$$

V_{CC1} = Pin 1
V_{CC2} = Pin 16
V_{EE} = Pin 8

t_{pd} = 0.9 ns typ (510 ohm load)
= 1.1 ns typ (50 ohm load)
P_D = 120 mW typ/pkg (No load)
Full Load Current, I_L = −25 mAdc max

Dual 4-input Gate

MC1660 provides simultanous OR-NOR or AND-NAND output functions with the capability of driving 50-ohm lines. These devices contain an internal bias reference voltage insuring that the threshold point is always in the center of the transition region over the temperature range (−30° to +85°C). The input pulldown resistors eliminate the need to tie unused inputs to V_{EE}.