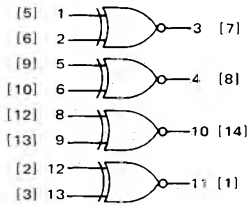


QUAD EXCLUSIVE
"NOR" GATE
(Open Collector)

MC8200/MC7200 series

MC8242F, L*
MC7242F, L, P*

ADVANCE INFORMATION/NEW PRODUCT



$$3 = \bar{1} \cdot \bar{2} + 1 \cdot 2$$

V_{CC} = Pin 14 [4]
GND = Pin 7 [11]

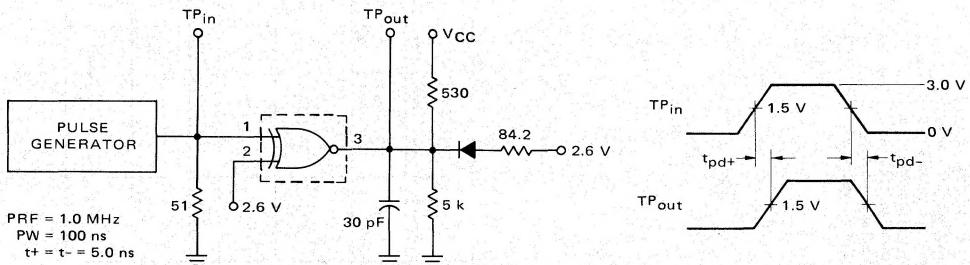
Numbers at ends of terminals represent pin numbers for devices in the dual in-line package.

Numbers in brackets represent pin numbers for devices in the flat package.

This device contains four independent Exclusive NOR gates with open collector outputs which may be used to implement digital comparison functions. A four-bit comparison may be made by connecting the outputs together.

Input Loading Factor = 2
Output Loading Factor = 10
Total Power Dissipation = 170 mW typ/pkg

SWITCHING TIME TEST CIRCUIT AND WAVEFORMS



5 kΩ and 30 pF include jig and scope.

*F suffix = TO-86 ceramic flat package (Case 607).
L suffix = TO-116 ceramic dual in-line package (Case 632).
P suffix = TO-116 plastic dual in-line package (Case 605).

