

**MCC1539**  
**MCC1439**

**Advance Information**

**MONOLITHIC OPERATIONAL AMPLIFIER CHIP**

... designed for use as a summing amplifier, integrator, or amplifier with operating characteristics as a function of the external feedback components. For detailed information see Motorola Application Note AN-439.

The MCC1539 and MCC1439 employ phosphosilicate passivation that protects the entire die surface area, including metalization interconnects. All dice have a minimum gold-backed thickness of 4000 Angstroms. The interconnecting metalization and bonding pads are of evaporated aluminum.

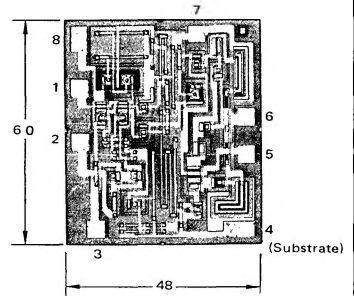
- Low Input Offset Voltage – 3.0 mV max
- Low Input Offset Current – 60 nA max
- Large Power-Bandwidth – 20 V<sub>p-p</sub> Output Swing at 20 kHz min
- Output Short-Circuit Protection
- Input Over-Voltage Protection
- Class AB Output for Excellent Linearity
- Slew Rate – 34 V/μs typ

**MAXIMUM RATINGS (T<sub>A</sub> = +25°C unless otherwise noted)**

Rating	Symbol	Value	Unit
Power Supply Voltage	V <sup>+</sup>	+18	Vdc
	V <sup>-</sup>	-18	Vdc
Differential Input Signal	V <sub>in</sub>	±[V <sup>+</sup> +  V <sup>-</sup>  ]	Vdc
Common Mode Input Swing	CMV <sub>in</sub>	+V <sup>+</sup> , - V <sup>-</sup>	Vdc
Load Current	I <sub>L</sub>	15	mA
Output Short Circuit Duration	t <sub>S</sub>	Continuous	
Operating Temperature Range	T <sub>A</sub>	-55 to +125	°C
Junction Temperature Range	T <sub>J</sub>	-65 to +150	°C

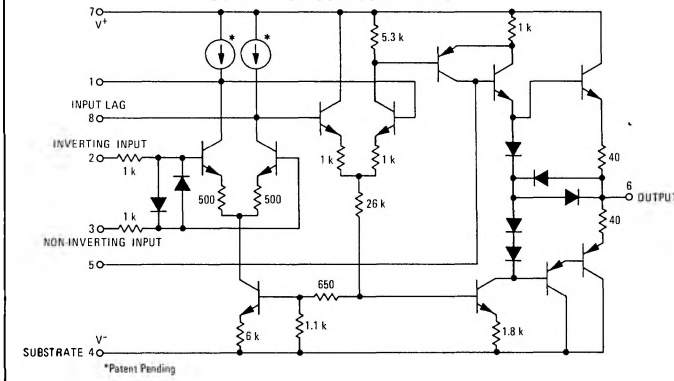
**OPERATIONAL AMPLIFIER CHIP  
INTEGRATED CIRCUIT**

MONOLITHIC SILICON

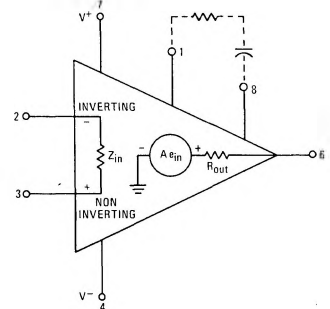


All dimensions are nominal and in mils (10<sup>-3</sup> inches).  
Die Dimensions  
Thickness = 8.0  
Bonding Pads = 4.0 x 4.0

**FIGURE 1 – CIRCUIT SCHEMATIC**



**FIGURE 2 – EQUIVALENT CIRCUIT**



# MCC1539, MCC1439 (continued)

**ELECTRICAL CHARACTERISTICS** ( $V^+ = +15$  Vdc,  $V^- = -15$  Vdc,  $T_A = +25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	MCC1539			MCC1439			Unit
		Min	Typ	Max	Min	Typ	Max	
Input Bias Current	$I_b$	-	0.20	0.50	-	0.20	1.0	$\mu\text{A}$
Input Offset Current	$ I_{io} $	-	20	60	-	20	100	nA
Input Offset Voltage	$ V_{io} $	-	1.0	3.0	-	2.0	7.5	mV
Average Temperature Coefficient of Input Offset Voltage ( $R_G = 50 \text{ k}\Omega$ )	$ TCV_{io} $	-	3.0	-	-	3.0	-	$\mu\text{V}/^\circ\text{C}$
Input Impedance	$Z_{in}$	-	300	-	-	300	-	k $\Omega$
Input Common-Mode Voltage Swing	$CMV_{in}$	-	+12	-	-	+12	-	V <sub>pk</sub>
Common Mode Rejection Ratio ( $f = 1.0 \text{ kHz}$ )	$CM_{rej}$	-	110	-	-	110	-	dB
Open-Loop Voltage Gain ( $V_O = \pm 10$ V, $R_L = 10 \text{ k}\Omega$ , $R_G = \infty$ ) ( $T_A = +25^\circ\text{C}$ to $T_{high}$ ) ( $T_A = T_{low}$ )	$AV_{OL}$	50,000 25,000	120,000 100,000	- -	15,000 15,000	100,000 100,000	- -	-
Power Bandwidth ( $A_v = 1$ , THD $\leq 5\%$ , ( $V_O = 20$ Vp-p, $R_L = 1.0 \text{ k}\Omega$ )	PBW	-	50	-	-	50	-	kHz
Step Response Gain = 1000, no overshoot,	$t_f$	-	130	-	-	130	-	ns
	$t_{pd}$	-	190	-	-	190	-	ns
Gain = 1000, 15% overshoot,	$dV_{out}/dt$	-	6.0	-	-	6.0	-	V/ $\mu\text{s}$
	$t_f$	-	80	-	-	80	-	ns
Gain = 100, no overshoot,	$t_{pd}$	-	100	-	-	100	-	ns
	$dV_{out}/dt$	-	14	-	-	14	-	V/ $\mu\text{s}$
Gain = 10, 15% overshoot,	$t_f$	-	60	-	-	60	-	ns
	$t_{pd}$	-	100	-	-	100	-	ns
Gain = 1, 15% overshoot,	$dV_{out}/dt$	-	34	-	-	34	-	V/ $\mu\text{s}$
	$t_f$	-	120	-	-	120	-	ns
Gain = 1, 15% overshoot,	$t_{pd}$	-	80	-	-	80	-	ns
	$dV_{out}/dt$	-	6.25	-	-	6.25	-	V/ $\mu\text{s}$
Output Impedance ( $f = 20 \text{ Hz}$ )	$t_f$	-	160	-	-	160	-	ns
	$t_{pd}$	-	80	-	-	80	-	ns
Output Voltage Swing ( $R_L = 2.0 \text{ k}\Omega$ , $f = 1.0 \text{ kHz}$ ) ( $R_L = 1.0 \text{ k}\Omega$ , $f = 1.0 \text{ kHz}$ )	$dV_{out}/dt$	-	4.2	-	-	4.2	-	V/ $\mu\text{s}$
	$Z_{out}$	-	4.0	-	-	4.0	-	k $\Omega$
Output Voltage Swing ( $R_L = 2.0 \text{ k}\Omega$ , $f = 1.0 \text{ kHz}$ ) ( $R_L = 1.0 \text{ k}\Omega$ , $f = 1.0 \text{ kHz}$ )	$V_{out}$	-	-	-	$\pm 10$	$\pm 13$	-	V <sub>pk</sub>
Positive Supply Sensitivity ( $V^-$ constant)	$S^+$	-	50	150	-	50	200	$\mu\text{V}/\text{V}$
Negative Supply Sensitivity ( $V^+$ constant)	$S^-$	-	50	150	-	50	200	$\mu\text{V}/\text{V}$
Power Supply Current ( $V_O = 0$ )	$I_{D+}$ $I_{D-}$	-	3.0 3.0	5.0 5.0	-	3.0 3.0	6.7 6.7	mAdc
DC Quiescent Power Dissipation ( $V_O = 0$ )	$P_D$	-	90	150	-	90	200	mW

See current MCC1539/1439 data sheet for additional information.

## PACKAGING AND HANDLING

The MCC1539/MCC1439 operational amplifier is now available as a single monolithic die or encapsulated in the TO-99 and TO-116 hermetic and plastic packages. The phosphorsilicate passivation protects the metalization and active area of the die but care must be exercised when removing the dice from the shipping carrier to avoid scratching the bonding pads. A vacuum pickup is useful for handling of dice. Tweezers are not recommended for this purpose.

The non-spill type shipping carrier consists of a compartmentalized tray and fitted cover. Die are placed in the carrier with geometry side up.