TRUTH TABLE

MODE	INPUT			OUTPUT
	CE	WE	D_{in}	D_{out}
Write "0"	L	L	L	L
Write "1"	L	L	H	L
Read	L	H	φ	Q
Disabled	H	φ	φ	L

φ : Don't Care.

P_D = 420 mW typ/pkg
Access Time = 10 ns max (MCM10142)
= 15 ns max (MCM10140—10148)

64-Bit Random Access Memory

The MC10140, MCM10142 ans MCM10148 are 64-Bit Random Access Memories (RAMs). They offer very high speed, full binary decoding, two chip enable inputs for easy memory expansion, and separate data input and data output pins.

Organization of these memories is 64 one-bit words and they are package in standard 16-pin hermetic dual in-line packages.

MCM10142 and MCM10148 logic levels are fully compatible with the MECL 10,000 logic family and are specified for driving a 50 ohm load. The MCM10140 logic levels are compatible with the MECL 10,000 logic family except they are specified for driving a 90 ohm load.

MCM10140/10142/10148   **MEMORIES**