UNA0236

Silicon PNP epitaxial planar transistor (2 elements) Silicon NPN epitaxial planar transistor (2 elements)

For motor drives

For small motor drive circuits in general

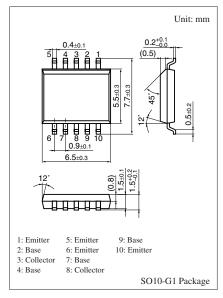
■ Features

- Small and lightweight
- Low power consumption
- Low-voltage drive
- With 4 elements incorporated

■ Absolute Maximum Ratings $T_a = 25$ °C

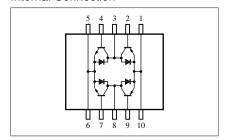
Parameter		Symbol	Rating	Unit
PNP	Collector-base voltage (Emitter open)	V _{CBO}	-12	V
	Collector-emitter voltage (Base open)	V _{CEO}	-10	V
	Emitter-base voltage (Collector open)	V _{EBO}	-7	V
	Collector current	I_C	-1	A
	Peak collector current	I_{CP}	-2.5	A
NPN	Collector-base voltage (Emitter open)	V _{CBO}	12	V
	Collector-emitter voltage (Base open)	V _{CEO}	10	V
	Emitter-base voltage (Collector open)	V _{EBO}	7	V
	Collector current	I_C	1	A
	Peak collector current	I_{CP}	2.5	A
Overall	Total power dissipation *	P_{T}	0.5	W
	Junction temperature	T_{j}	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Note) *: When the dissipation on one device is $T_C = 25^{\circ}C$



Marking Symbol: UN236

Internal Connection



\blacksquare Electrical Characteristics $T_a = 25 ^{\circ} C \pm 3 ^{\circ} C$

• PNP

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = -10 \mu A, I_E = 0$	-12			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = -1 \text{ mA}, I_B = 0$	-10			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = -10 \ \mu A, I_C = 0$	-7			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = -10 \text{ V}, I_E = 0$			-1	μΑ
Forward current transfer ratio *1	h_{FE}	$V_{CE} = -1 \text{ V}, I_C = -0.5 \text{ A}$	200		800	_
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_C = -1 A, I_B = -30 \text{ mA}$		- 0.2	- 0.3	V
Transition frequency	f_T	$V_{CB} = -6 \text{ V}, I_E = 50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance	Cob	$V_{CB} = -10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		65		pF
(Common base, input open circuited)						
Forward voltage *2	V _F	$I_F = -1 A$			-1.5	V

Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

- 2. *1: Pulse measurement
 - *2: Application to the internal diode

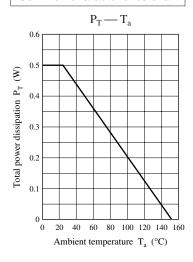
• NPN

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Collector-base voltage (Emitter open)	V _{CBO}	$I_C = 10 \mu\text{A}, I_E = 0$	12			V
Collector-emitter voltage (Base open)	V _{CEO}	$I_C = 1 \text{ mA}, I_B = 0$	10			V
Emitter-base voltage (Collector open)	V_{EBO}	$I_E = 10 \ \mu A, I_C = 0$	7			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 10 \text{ V}, I_{E} = 0$			1	μΑ
Forward current transfer ratio *1	h _{FE}	$V_{CE} = 1 \text{ V}, I_{C} = 0.5 \text{ A}$	200		800	_
Collector-emitter saturation voltage *1	V _{CE(sat)}	$I_C = 1 \text{ A}, I_B = 30 \text{ mA}$		0.2	0.3	V
Transition frequency	f_T	$V_{CB} = 6 \text{ V}, I_E = -50 \text{ mA}, f = 200 \text{ MHz}$		150		MHz
Collector output capacitance (Common base, input open circuited)	C _{ob}	$V_{CB} = 10 \text{ V}, I_E = 0, f = 1 \text{ MHz}$		50		pF
Forward voltage *2	$V_{\rm F}$	I _F = 1 A			1.5	V

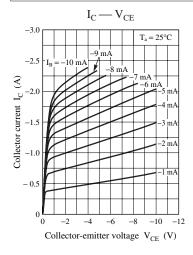
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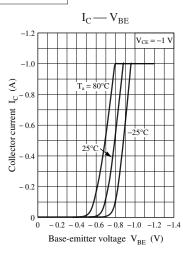
- 2. *1: Pulse measurement
 - *2: Application to the internal diode

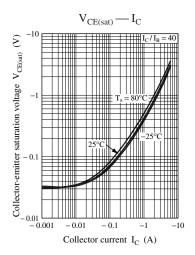
Common characteristics chart

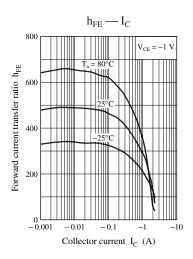


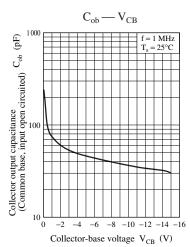
Characteristics charts of PNP transistor block





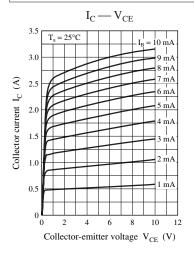


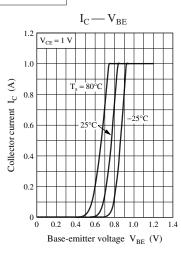


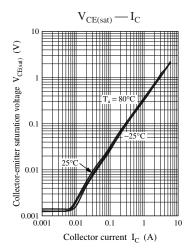


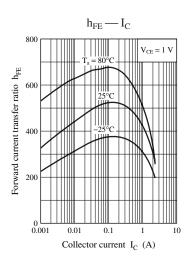
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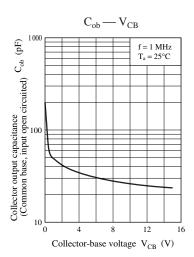
Characteristics charts of NPN transistor block











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