

DATA SHEET

BIPOLAR ANALOG INTEGRATED CIRCUT

POSITIVE VOLTAGE STABILIZED POWER SUPPLY

DESCRIPTION

The μ PC305 is a high-performance stabilized power supply that can supply a constant voltage in a wide temperature range even if the input voltage or load voltage fluctuates, by integrating a high-gain error amplifier and a temperature-compensating constant-voltage diode on a single chip.

FEATURES

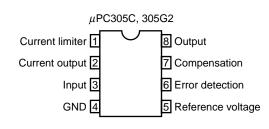
• Wide output voltage variable range Vo: 4.5 to 30 V, VDIF: 3 to 30 V

0.02%

- Excellent load stability
- Good ripple rejection ratio
 0.003%/V

PIN CONFIGURATION (Top View)

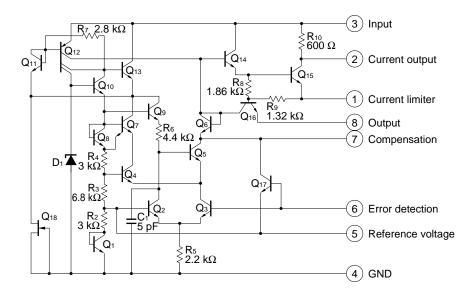
μ**PC305**



ORDERING INFORMATION

Part Number	Package
μPC305C	8-pin plastic DIP (7.62 mm (300))
μPC305G2	8-pin plastic SOP (5.72 mm (225))

EQUIVALENT CIRCUIT



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ABSOLUTE MAXIMUM RATINGS (Unless otherwise specified, $T_A = 25^{\circ}C$)

Parameter	Symbol	μPC305C	μPC305C μPC305G2	
Input Voltage	Vin	-0.3 to +40		
Input – Output Voltage Difference	VDIF	40		
Maximum Output Current	lo	50		
Total Loss	Рт	350 ^{Note 1} 440 ^{Note 2}		mW
Operating Temperature	TA	0 to +70		
Storage Temperature	Tstg	-55 to +125		

Notes 1. Where $T_A > +55^{\circ}C$, perform derating at T_J MAX. 125°C, -5 mW/°C.

2. Where $T_A > +25^{\circ}C$, perform derating at T_J MAX. 125°C, -4.4 mW/°C.

Caution If any of the parameters exceeds the absolute maximum ratings, even momentarily, the quality of the product may be impaired. The absolute maximum ratings are values that may physically damage the product(s). Be sure to use the product(s) within the ratings.

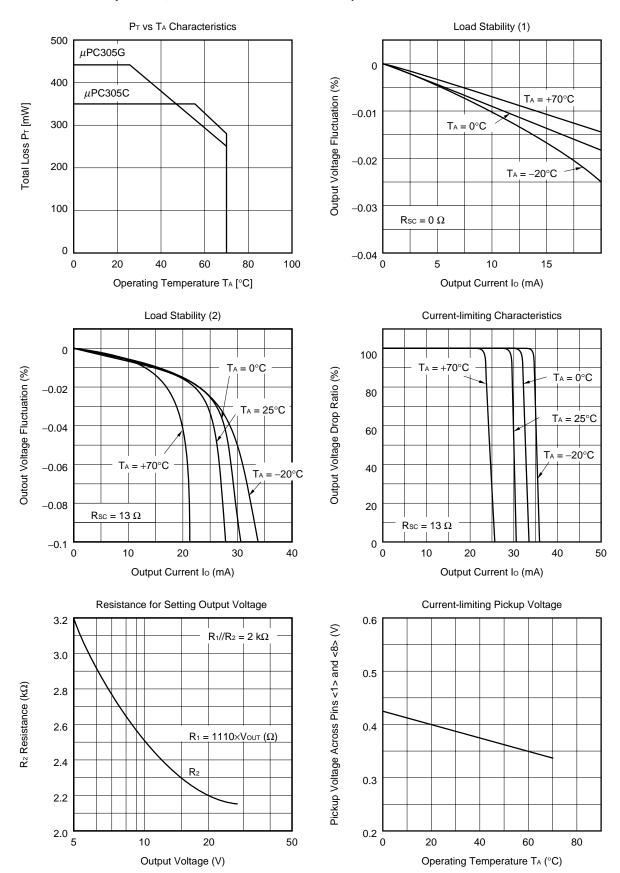
Parameter	Symbol	Condition	MIN.	TYP.	MAX.	Unit
Input Voltage Range	Vin		8.0		40	V
Output Voltage Range	Vout		4.5		30	V
Input – Output Voltage Difference	VDIF		3.0		30	V
Load Stability	REG∟	$0 \le Io \le 12 \text{ mA}, \text{ Rsc} = 18 \Omega$		0.02	0.05	%
Input Stability	REGıℕ	$V_{IN} - V_{OUT} \le 5 V$		0.025	0.06	%/V
		VIN – VOUT > 5 V		0.015	0.03	%/V
Ripple Rejection Ratio	REJ	C _{REF} = 10 μF, f = 120 Hz		0.003		%/V
Temperature Stability		$0^\circ C \leq T_A \leq 70^\circ C$		0.3	1.0	%
Reference Voltage	Vref		1.65	1.8	1.90	V
Output Noise Voltage	VN	10 Hz \leq f \leq 10 kHz, Cref = 0 μ F		0.005		%
		$C_{REF} = 0.1 \ \mu F$		0.002		%
Long-time Stability				0.1		%
Supply Current under No Load	Icc	V _{IN} = 40 V		1.0	2.0	mA

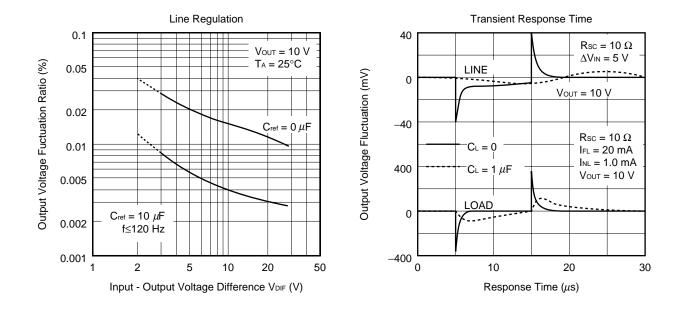
ELECTRICAL SPECIFICATIONS (Unless otherwise specified, T_A = 25°C)

Remark Rsc : Current-limiting resistor

CREF : Bypass capacitor of reference voltage pin

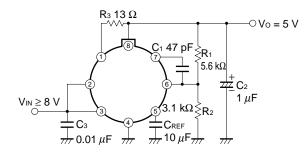
STANDARD CHARACTERISTIC CURVES (Unless otherwise specified, T_A = 25°C. Reference values)



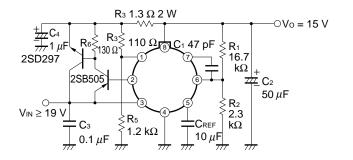


APPLICATION CIRCUIT EXAMPLES

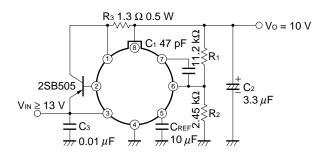
5 V-15 mA Regulator



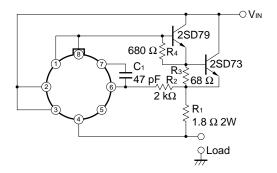
15 V-1A Regulator (Fold-back Characteristics)



10 V-200 mA Regulator (Drooping Characteristics)



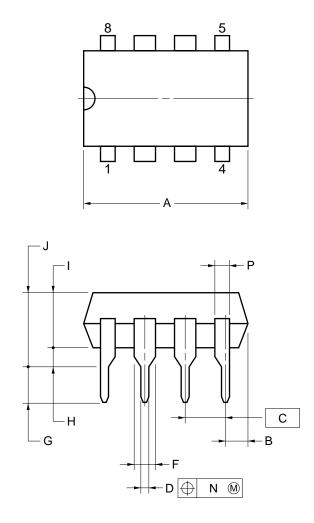
1A Constant-current Regulator

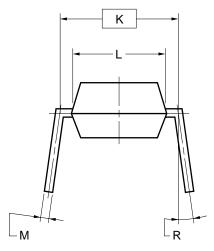


Caution Note the power consumption of the μ PC305 when the output pin is short-circuited and that of the external transistor.

PACKAGE DRAWINGS

8-PIN PLASTIC DIP (7.62mm(300))





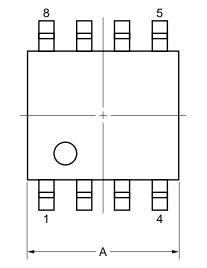
NOTES

1. Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

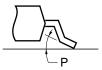
2. Item "K" to center of leads when formed parallel.

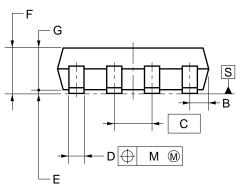
ITEM	MILLIMETERS		
А	10.16 MAX.		
В	1.27 MAX.		
С	2.54 (T.P.)		
D	0.50±0.10		
F	1.4 MIN.		
G	3.2±0.3		
Н	0.51 MIN.		
I	4.31 MAX.		
J	5.08 MAX.		
К	7.62 (T.P.)		
L	6.4		
М	$0.25\substack{+0.10 \\ -0.05}$		
Ν	0.25		
Р	0.9 MIN.		
R	0~15°		
1	P8C-100-300B,C-2		

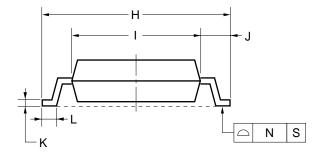
8-PIN PLASTIC SOP (5.72 mm (225))



detail of lead end







NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
А	$5.2 \begin{array}{c} +0.17 \\ -0.20 \end{array}$
В	0.78 MAX.
С	1.27 (T.P.)
D	$0.42\substack{+0.08\\-0.07}$
E	0.1±0.1
F	1.59±0.21
G	1.49
Н	6.5±0.3
I	4.4±0.15
J	1.1±0.2
К	$0.17\substack{+0.08\\-0.07}$
L	0.6±0.2
М	0.12
Ν	0.10
Р	3° ^{+7°} 3°
	S8GM-50-225B-6

RECOMMENDED SOLDERING CONDITIONS

Solder this product under the following recommended conditions.

For details of the recommended soldering conditions, refer to information document **Semiconductor Device Mounting Technology Manual (C10535E)**.

For soldering methods and conditions other than those recommended, consult NEC.

Surface Mount Type

μPC305G2: 8-pin plastic SOP (5.72 mm (225))

Soldering Method	Soldering Conditions	Recommended Conditions Symbol
Infrared reflow	Package peak temperature: 230°C, Time: 30 sec max. (210°C min.), Number of times: once	IR30-00-1
VPS	Package peak temperature: 215°C, Time: 40 sec max. (200°C min.), Number of times: once	VP15-00-1
Wave soldering	Solder bath temperature: 260°C max., Time: 10 sec max., Number of times: once, Preheating temperature: 120°C max. (Package surface temperature)	WS60-00-1

Caution Do not use two or more soldering methods in combination (except partial heating).

Through Hole type

μPC305C: 8-pin plastic DIP (7.62 mm (300))

Soldering Method	Soldering Conditions	Recommended Conditions Symbol
Wave soldering	Solder bath temperature: 260°C max., Time: 10 sec max.	

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 customer designated "quality assurance program" for a specific application. The recommended applications of
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