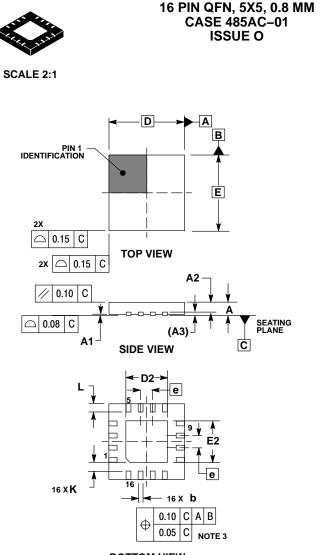
MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

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BOTTOM VIEW

DATE 12 SEP 2003

- NOTES:
 DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL.
 COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

| | MILLIMETERS | | |
|-----|-------------|------|--|
| DIM | MIN | MAX | |
| Α | 0.80 | 1.00 | |
| A1 | 0.00 | 0.05 | |
| A2 | 0.65 | 0.75 | |
| A3 | 0.20 REF | | |
| b | 0.25 | 0.35 | |
| D | 5.00 BSC | | |
| D2 | 2.55 | 2.85 | |
| E | 5.00 BSC | | |
| E2 | 2.55 | 2.85 | |
| е | 0.80 BSC | | |
| Κ | 0.20 | | |
| L | 0.35 | 0.75 | |

GENERIC **MARKING DIAGRAM***

| | 16 |
|-------|-------------------------------------|
| 1 | 0 |
| | XXXXXX |
| | XXXXXX |
| | ALYW |
| | |
| | |
| xxxxx | <pre>x = Specific Device Code</pre> |
| A | = Assembly Location |
| L | = Wafer Lot |
| Y | = Year |
| W | = Work Week |
| | |

*This information is generic. Please refer to device data sheet for actual part marking.

| DOCUMENT NUMBER: | 98AON13146D | Electronic versions are uncontrolle | |
|------------------|----------------------------------|--|-------------|
| STATUS: | ON SEMICONDUCTOR STANDARD | accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red. | |
| NEW STANDARD: | | | |
| DESCRIPTION: | 16 PIN QFN, 5X5 MM, 0.8 MM PITCH | | PAGE 1 OF 2 |



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PAGE 2 OF 2

| ISSUE | REVISION | DATE |
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