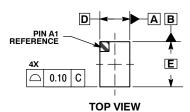
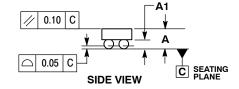
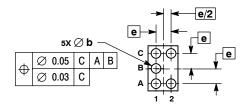


5 PIN FLIP-CHIP, 0.9x1.4 CASE 499BG ISSUE O

DATE 21 JUL 2009

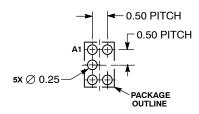






BOTTOM VIEW

SOLDERING FOOTPRINT*



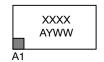
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES:

- DIMENSIONING AND TOLERANCING PER
- 1. DIMENSIONING AND TOLERANGING FER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.44	0.50	
A1	0.15	0.19	
b	0.21	0.25	
D	0.90 BSC		
Е	1.40 BSC		
e	0.50	BSC	

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code = Assembly Location

= Year ww = Work Week

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

DOCUMENT NUMBER:	98AON42382E	
STATUS:	ON SEMICONDUCTOR STANDARD	
NEW STANDARD:		
DESCRIPTION:	5 PIN FLIP-CHIP. 1.4X0.9. 0.5 PITCH	

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DOCUMENT	NUMBER:
98A0N42382	F

PAGE 2 OF 2

ISSUE	REVISION	DATE		
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