

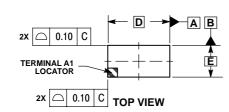


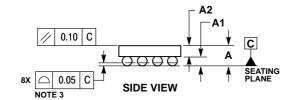
SCALE 4:1

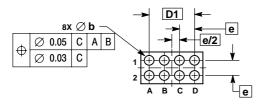
8 PIN FLIP-CHIP, 2.05x1.05, 0.5P CASE 766AE-01

ISSUE C

DATE 06 NOV 2006





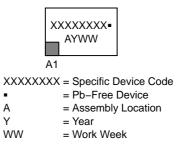


**BOTTOM VIEW** 

NOTES: 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS. 3. COPLANARITY APPLIES TO SPHERICAL CROWNS OF SOLDER BALLS.

CROWING OF SOLDER B								
		MILLIMETERS						
	DIM	MIN	MAX					
[	Α		0.655					
[	A1	0.210	0.270					
	A2	0.335	0.385					
	b	0.290	0.340					
	D	2.050 BSC						
[	D1	1.500 BSC						
[	Е	1.050 BSC						
	е	0.500 BSC						

## GENERIC **MARKING DIAGRAM\***



\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present.

DOCUMENT NUMBER:	98AON22071D	SEMICONDUCTOR STANDARD SEMICONDUCTOR STANDARD CONTROLLED COPY" in red.	
STATUS:	ON SEMICONDUCTOR STANDARD		
NEW STANDARD:			
DESCRIPTION:	8 PIN FLIP-CHIP, 2.05X1.05, 0.5P		





ISSUE	REVISION	DATE			
0	RELEASED FOR PRODUCTION. REQ. BY B. BEYRON.	13 FEB 2006			
А	CORRECTED MARKING INFORMATION. REQ. BY B. BEYRON.	06 JUL 2006			
В	CORRECTED TERMINAL A1 LOCATOR & BUMP MATRIX CODES. REQ. BY B. BEYRON.	13 OCT 2006			
С	MOVED TOP VIEW TERMINAL A1 INDICATOR DOWN TO BOTTOM LEFT COR- NER TO BE CONSISTENT WITH OTHER VIEWS. REQ. BY B. BEYRON.	06 NOV 2006			

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