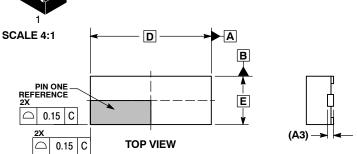


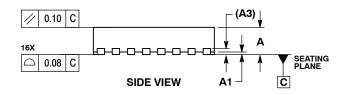
DATE 22 AUG 2013

DIMENSIONING AND TOLERANCING PER

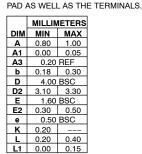
ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER. DIMENSION b APPLIES TO TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM

FROM TERMINAL.
COPLANARITY APPLIES TO THE EXPOSED









NOTES:

3.

## **GENERIC MARKING DIAGRAM\***



XXX = Specific Device Code = Assembly Location Α

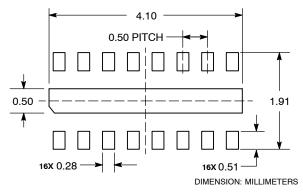
= Year W = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

DETAIL A	■ D2 — ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■ ■	8 	2X 0.25 TES 1	5 x 0.40 mm ST PAD SIZE
16X K □ ''	→ 163	🛧	0.10	C A B
	BOTTOM VIEW	TE 3	0.05	С

## **RECOMMENDED SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	DFN16, 4.0X1.6 MM, 0.5 LEAD PITCH		

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ISSUE	REVISION	DATE		
0	RELEASED FOR PRODUCTION. REQ. BY A. TAM.	13 SEP 2004		
Α	CHANGED MARKING DIAGRAM. REQ. BY A. TAM.	07 JAN 2005		
В	ADDED SOLDERING FOOTPRINT. REQ. BY A. TAM.	08 JUN 2005		
С	ADDED DETAIL A AND DIMENSION L1. REQ. BY K. LIM.	14 JAN 2010		
D	ROTATED GENERIC MARKING DIAGRAM 90 DEGREES COUNTER-CLOCKWISE. REQ. BY M. BEGONIA.	22 AUG 2013		

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