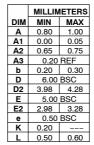
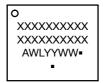
DFN20, 6x5, 0.5P CASE 505AB **ISSUE C**

DATE 23 MAY 2012

- NOTES:
 1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
- DIMENSIONS IN MILLIMETERS.
 DIMENSION b APPLIES TO PLATED TERMINALS AND IS 3. MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL
- AS THE TERMINALS.
 FOR DEVICE OPN CONTAINING W OPTION, DETAIL B AL-
- TERNATE CONSTRUCTION IS NOT APPLICABLE.



GENERIC MARKING DIAGRAM*

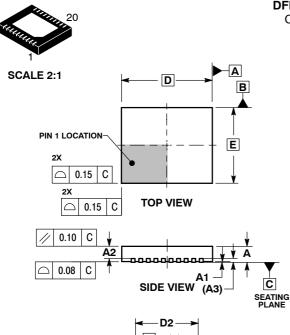


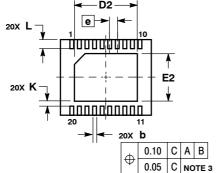
XXXXX = Specific Device Code = Assembly Location

WL = Wafer Lot = Year VV = Work Week WW = Pb-Free Package

(Note: Microdot may be in either location)

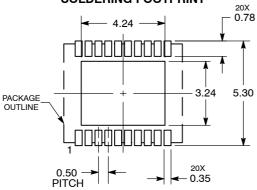
*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.





BOTTOM VIEW

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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DESCRIPTION:	20 PIN DFN, 6X5 MM, 0.5 MM PITCH		PAGE 1 OF 2



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98AON13117	'D

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED TO PRODUCTION. REQ. BY P. CELAYA.	21 JUL 2003
Α	CORRECTED MARKING DIAGRAM. REQ. BY J. LETTERMAN.	09 JUL 2004
В	CORRECTED DIMENSION B TO 0.20 MIN AND 0.30 MAX. REQ. BY S. WHITE.	05 DEC 2008
С	ADDED NOTE 5. REQ. BY B. MARQUIS.	23 MAY 2012

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