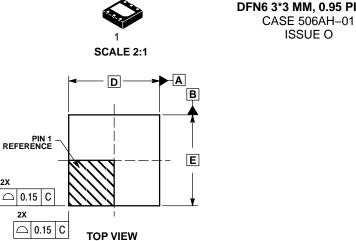
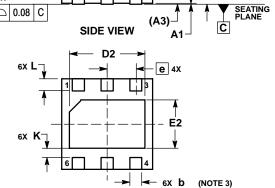
0.10 C



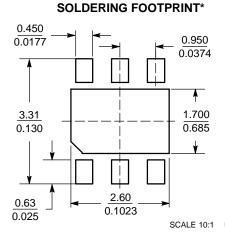


**BOTTOM VIEW** 

Ф

0.05 C

# 0.10 C A B



## \*For additional information on our Pb-Free strategy and soldering details, please download the ON Semice Mounting Techniques Reference Manual

conductor Soldering and II, SOLDERRM/D.				
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# DFN6 3\*3 MM, 0.95 PITCH

**DATE 17 NOV 2004** 

#### NOTES:

- NOTES:

  1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.

  2. CONTROLLING DIMENSION: MILLIMETERS.

  3. DIMESNION & APPLIES TO PLATED TERMINAL
- AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL. COPLANARITY APPLIES TO THE EXPOSED
- PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	NOM	MAX
Α	0.80	0.90	1.00
A1	0.00	0.03	0.05
A3	0	.20 REF	:
b	0.35	0.40	0.45
D	3.00 BSC		
D2	2.40	2.50	2.60
Е	3.00 BSC		
E2	1.50	1.60	1.70
е	0.95 BSC		
K	0.21		
L	0.30	0.40	0.50

# **GENERIC MARKING DIAGRAM\***

1 xxxxx xxxxx AYWW	1 xxxxx AYWW
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### Standard

Pb-Free

= Specific Device Code = Assembly Location Α Υ = Year

WW = Work Week = Pb-Free Package

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STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.	
NEW STANDARD:			
DESCRIPTION:	DFN6 3*3 MM, 0.95 PITCH, SINGLE FLAG		PAGE 1 OF 2

<sup>\*</sup>This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.



<b>DOCUMENT</b>	NUMBER
98AON19891	ID

PAGE 2 OF 2

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