

3.30 2.05 **□** 0.63 PACKAGE OUTLINE 1.64 3.30 0.65 **PITCH** 12X 0.40

RECOMMENDED

SOLDERING FOOTPRINT*

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DIMENSIONS: MILLIMETERS

0.65 PITCH

DATE 12 OCT 2016

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETERS.
- DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND
- 0.30mm FROM THE TERMINAL TIP.
 COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.80	1.00	
A1	0.00	0.05	
A3	0.20 REF		
b	0.25	0.35	
b1	0.20	0.30	
D	3.00 BSC		
D2	1.65	1.85	
Е	3.00 BSC		
E2	1.40	1.60	
е	0.65 BSC		
e1	0.65 REF		
L	0.30	0.50	
L1	0.00	0.15	

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code

Α = Assembly Location

L = Wafer Lot Υ = Year W = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

DESCRIPTION:	DFN8, 3X3, 0.65P		PAGE 1 OF 2
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PAGE 2 OF 2

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А	REMOVED ALTERNATE TERMINAL CONSTRUCTIONS FROM DETAIL A AND B. REQ. BY I. HYLAND.	12 OCT 2016		

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