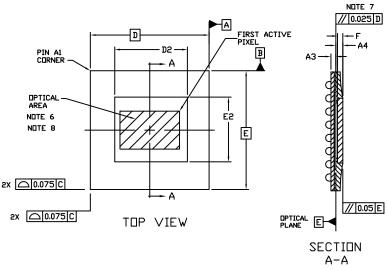
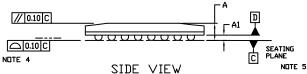
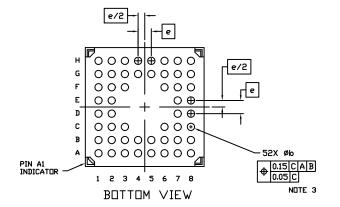
IBGA52 9x9 CASE 503AA ISSUE A

**DATE 25 JUN 2018** 



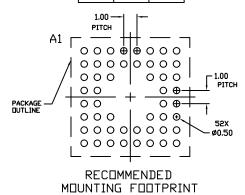




## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- 3. DIMENSION 6 IS MEASURED AT THE MAXIMUM SOLDER BALL DIAMETER PARALLEL TO DATUM C.
- COPLANARITY APPLIES TO THE SPHERICAL CROWNS OF THE SOLDER BALLS.
- 5. DATUM C, THE SEATING PLANE, IS DEFINED BY THE SPHERICAL CROWNS OF THE SOLDER BALLS.
- 6. MAXIMUM ROTATION OF THE OPTICAL AREA RELATIVE TO D AND E WILL BE 0.5°. OPTICAL AREA IS DEFINED BY THE ACTIVE PIXEL ARRAY. REFER TO THE DEVICE DATASHEET FOR TOTAL ARRAY AND FIRST PIXEL DEFINITIONS.
- 7. PARALLELISM APPLIES ONLY TO THE OPTICAL AREA.
- OPTICAL CENTER OFFSET WITH RESPECT TO THE PACKAGE CENTER IS X=0.0 MICRONS, Y=0.0 MICRONS ±75 MICRONS.

	MILLIMETERS		
DIM	MIN.	MAX.	
Α		1.44	
A1	0.34	0.44	
A3	0.325	0.425	
A4	0.475	0.575	
b	0.50	0.60	
D	9.00 BSC		
D2	5.40	5.60	
Ε	9.00 BSC		
E2	4.80	5.00	
e	1.00 BSC		
F	0.38	0.42	



DOCUMENT NUMBER:	98AON93388F	Electronic versions are uncontrolled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED of the controlled except when stamped "CONTROLLED of the controlled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED of the controlled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED of the controlled except when stamped "CONTROLLED of the controlled except when accessed directly from Printed versions are uncontrolled except when stamped "CONTROLLED of the controlled except when stamped "CONTROLLED of the controlled except when stamped "CONTROLLED of the controlled except when	
DESCRIPTION:	IBGA52 9x9		PAGE 1 OF 1

ON Semiconductor and are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. ON Semiconductor does not convey any license under its patent rights nor the rights of others.