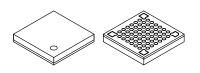
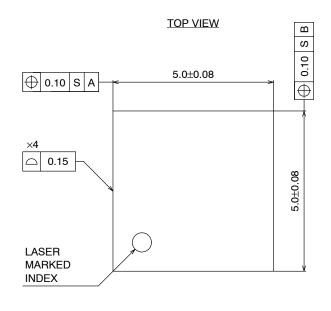
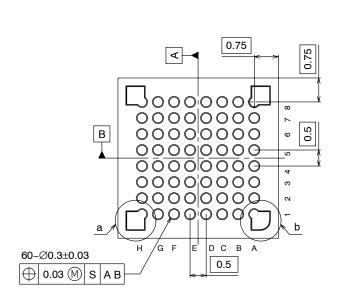
MECHANICAL CASE OUTLINE



LFLGA64 5x5 / FLGA64 CASE 569AD ISSUE A

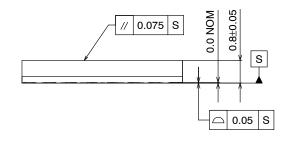
DATE 17 JAN 2014

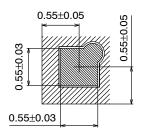




BOTTOM VIEW

SIDE VIEW





DETAIL_a

GENERIC MARKING DIAGRAM*



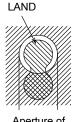
XXXXX = Specific Device Code Y = Year

M = Month

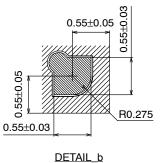
DDD = Additional Traceability Data

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.





Aperture of Solder Resist



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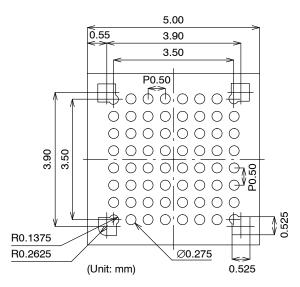
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SOLDERING FOOTPRINT*



NOTE: The measurements are not to guarantee but for reference only.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION FROM SANYO ENACT# S-380 TO ON SEMICONDUCTOR. REQ. BY D. TRUHITTE.	29 FEB 2012
Α	ADDED MARKING AND SOLDER FOOTPRINT INFORMATION. REQ. BY D. TRUHITTE.	17 JAN 2014

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