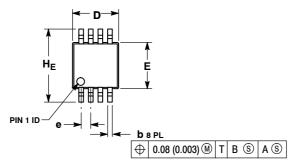
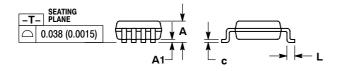
DATE 02 JUL 2013

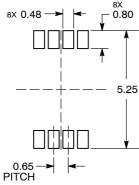


SCALE 2:1





RECOMMENDED **SOLDERING FOOTPRINT***



DIMENSION: MILLIMETERS

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

NOTES

Micro8[™] CASE 846A-02 **ISSUE J**

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- 2. CONTROLLING DIMENSION: MILLIMETER. 3. DIMENSION A DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE
- 0.15 (0.000) FEN SIDE. DIMENSION B DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION. INTERLEAD FLASH OR PROTRUSION SHALL NOT EXCEED 0.25 (0.010) PER SIDE. 4.

5. 846A-01 OBSOLETE, NEW STANDARD 846A-02.

| | MILLIMETERS | | | INCHES | | |
|-----|-------------|------|------|-----------|-------|-------|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX |
| Α | | | 1.10 | | | 0.043 |
| A1 | 0.05 | 0.08 | 0.15 | 0.002 | 0.003 | 0.006 |
| b | 0.25 | 0.33 | 0.40 | 0.010 | 0.013 | 0.016 |
| c | 0.13 | 0.18 | 0.23 | 0.005 | 0.007 | 0.009 |
| D | 2.90 | 3.00 | 3.10 | 0.114 | 0.118 | 0.122 |
| E | 2.90 | 3.00 | 3.10 | 0.114 | 0.118 | 0.122 |
| е | 0.65 BSC | | | 0.026 BSC | | |
| L | 0.40 | 0.55 | 0.70 | 0.016 | 0.021 | 0.028 |
| HE | 4.75 | 4.90 | 5.05 | 0.187 | 0.193 | 0.199 |





XXXX = Specific Device Code

- = Assembly Location
- = Year

А

Υ

W

- = Work Week
- = Pb-Free Package
- (Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " •", may or may not be present.

| STYLE 1: | STYLE 2: | STYLE 3: |
|---------------|----------------------------|-----------------|
| PIN 1. SOURCE | PIN 1. SOURCE 1 | PIN 1. N-SOURCE |
| 2. SOURCE | 2. GATE 1 | 2. N-GATE |
| 3. SOURCE | SOURCE 2 | 3. P-SOURCE |
| 4. GATE | 4. GATE 2 | 4. P-GATE |
| 5. DRAIN | 5. DRAIN 2 | 5. P-DRAIN |
| 6. DRAIN | 6. DRAIN 2 | 6. P-DRAIN |
| 7. DRAIN | 7. DRAIN 1 | 7. N-DRAIN |
| 8. DRAIN | 8. DRAIN 1 | 8. N-DRAIN |

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| STATUS: | ON SEMICONDUCTOR STANDARD | | |
| NEW STANDARD: | | "CONTROLLED COPY" in red. | |
| DESCRIPTION: | MICR08 | | PAGE 1 OF 2 |



DOCUMENT NUMBER: 98ASB14087C

PAGE 2 OF 2

| ISSUE | REVISION | DATE | | | |
|-------|--|-------------|--|--|--|
| G | ADDED NOMINAL VALUES AND UPDATED GENERIC MARKING DIAGRAM. REQ. BY HONG XIAO | 18 JUL 2005 | | | |
| Н | CORRECTED GENERIC MARKING INFORMATION. REQ. BY T. GURNETT. | 12 NOV 2007 | | | |
| J | CORRECTED SOLDERING FOOTPRINT. REQ. BY J. LIU. | 02 JUL 2013 | | | |
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