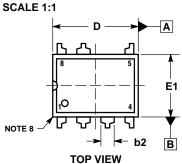
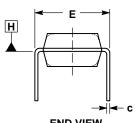


## PDIP-7 (PDIP-8 LESS PIN 6)

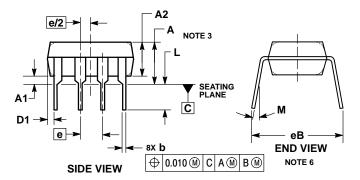
CASE 626A ISSUE C

**DATE 22 APR 2015** 





**END VIEW** WITH LEADS CONSTRAINED NOTE 5



## NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
  2. CONTROLLING DIMENSION INCLES
- CONTROLLING DIMENSION: INCHES.
  DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACK-
- AGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.
   DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, MOLD FLASH OR PROTRUSIONS ARE NOT TO EXCEED 0.10 INCH.
- DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR TO DATUM C.
  6. DIMENSION 6B IS MEASURED AT THE LEAD TIPS WITH THE
- LEADS UNCONSTRAINED.
  DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE
- LEADS, WHERE THE LEADS EXIT THE BODY. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α		0.210		5.33
A1	0.015		0.38	
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.35	0.56
b2	0.060	TYP	1.52 TYP	
С	0.008	0.014	0.20	0.36
D	0.355	0.400	9.02	10.16
D1	0.005		0.13	
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
е	0.100 BSC		2.54 BSC	
eВ		0.430		10.92
L	0.115	0.150	2.92	3.81
М		10°		10°

## **GENERIC MARKING DIAGRAM\***



XXXX = Specific Device Code = Assembly Location Α

WL = Wafer Lot YY = Year  $\mathsf{W}\mathsf{W}$ = Work Week G = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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DESCRIPTION:	PDIP-7 (PDIP-8 LESS PIN 6)		PAGE 1 OF 2



DOCUMENT	NUMBER:
98AON11774	ID.

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION.	15 JUL 2002
Α	CHANGED DIMENSION N MIN VALUES TO 0.51 MM & 0.020 INCHES. REQ. BY J. FLYNN.	08 JUN 2012
В	REDRAWN TO UNIFORM FABRICATION SITE SPECIFICATIONS. REQ. BY J. LETTERMAN.	29 MAY 2013
С	CORRECTED DIMENSION REFERENCE IN NOTE 6 FROM E3 TO eB. REQ. BY JJ. YEOH.	22 APR 2015

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