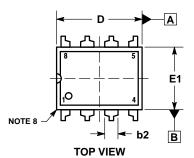
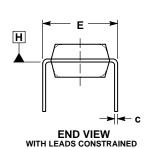


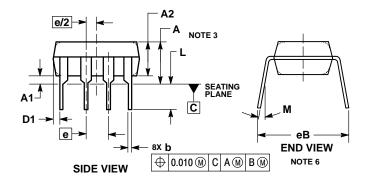
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NOTE 5



STYLE 1: PIN 1. AC IN 2. DC + IN 3. DC - IN

- 4. AC IN 5. GROUND
- 6. OUTPUT 7. AUXILIARY 8. V_{CC}

NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: INCHES.
- CONTROLLING DIMENSION: INCHES.

 DIMENSIONS A, A1 AND L ARE MEASURED WITH THE PACKAGE SEATED IN JEDEC SEATING PLANE GAUGE GS-3.

 DIMENSIONS D, D1 AND E1 DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS. MOLD FLASH OR PROTRUSIONS ARE

 NOT TO EXCEED A 10 INCH.
- NOT TO EXCEED 0.10 INCH.

 DIMENSION E IS MEASURED AT A POINT 0.015 BELOW DATUM
 PLANE H WITH THE LEADS CONSTRAINED PERPENDICULAR
- TO DATUM C.
 6. DIMENSION 6B IS MEASURED AT THE LEAD TIPS WITH THE
- LEADS UNCONSTRAINED.
 7. DATUM PLANE H IS COINCIDENT WITH THE BOTTOM OF THE
- LEADS, WHERE THE LEADS EXIT THE BODY.

 8. PACKAGE CONTOUR IS OPTIONAL (ROUNDED OR SQUARE CORNERS).

	INCHES MILLIMETER		ETERS	
DIM	MIN	MAX	MIN	MAX
Α		0.210		5.33
A1	0.015		0.38	
A2	0.115	0.195	2.92	4.95
b	0.014	0.022	0.35	0.56
b2	0.060	TYP	1.52 TYP	
С	0.008	0.014	0.20	0.36
D	0.355	0.400	9.02	10.16
D1	0.005		0.13	
E	0.300	0.325	7.62	8.26
E1	0.240	0.280	6.10	7.11
е	0.100 BSC		2.54 BSC	
eВ		0.430		10.92
L	0.115	0.150	2.92	3.81
M		10°		10°

GENERIC MARKING DIAGRAM*



XXXX = Specific Device Code = Assembly Location Α

WL = Wafer Lot ΥY = Year WW = Work Week = Pb-Free Package G

*This information is generic. Please refer to device data sheet for actual part marking. Pb–Free indicator, "G" or microdot " ■", may or may not be present.

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ISSUE	REVISION	DATE
L	REVISION TO CHANGE LEGAL OWNERSHIP OF DOCUMENT FROM MOTOROLA TO ON SEMICONDUCTOR. REQ. BY F. PADILLA.	29 SEP 2000
М	REDRAWN TO JEDEC STANDARDS AND TO MATCH UNISEM'S POD. REQ. BY Z. CANEDA.	08 APR 2010
N	REDRAWN TO UNIFORM FABRICATION SITE SPECIFICATIONS. REQ. BY J. LETTERMAN.	14 JUN 2013
Р	CORRECTED DIMENSION REFERENCE IN NOTE 6 FROM E3 TO eB. REQ. BY JJ. YEOH.	22 APR 2015

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