

PLLP-12, 9x9 mm

**DATE 18 JUL 2005** 

## NOTES:

- DIMENSIONS AND TOLERANCING PER
- ASME Y14.5M, 1994.

  DIMENSIONS IN MILLIMETERS.

  COPLANARITY APPLIES TO THE LEAD, DIMENSION B, AND EXPOSED PAD.

	MILLIMETERS		
DIM	MIN	MAX	
Α	1.750	1.950	
A1	0.000 0.050		
A3	0.254 REF		
b	0.400	0.600	
D	9.000 BSC		
E	9.000 BSC		
е	1.270 BSC		
D2	5.400	5.600	
E2	7.400	7.600	
K	0.850 REF		
L	0.850	0.950	

## **GENERIC MARKING DIAGRAM\***



xxxxxxx = Specific Device Code

= Assembly Location Α

WL = Wafer Lot ΥY = Year = Work Week WW G or ■ = Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

- STYLE 1: PIN 1. OVLO 2. UVLO 3. ENABLE/TIMER

  - 4. GND 5. CCHARGE
  - 6. CURRENT LIMIT 7. POWER GOOD

  - 8. N/C 9. N/C 10. SOURCE 11. SOURCE
  - 12. SOURCE

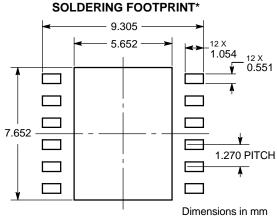
	CASE 488AB-0 ISSUE C
SCALE 1:1	0.10 C  0.08 C SEATING PLANE
PIN ONE LOCATION  4 X  0.15 C	(A3) 
TOP VIEW	SIDE VIEW
10 X e 12 X	

0.10 M C A B

С

**BOTTOM VIEW** 

0.05 M



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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NEW STANDARD:			
DESCRIPTION:	PLLP-12, 9*9 MM, SMART HOTPLUG		PAGE 1 OF 2



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ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY P. CELAYA.	01 MAR 2004
Α	CHANGED TITLE OF CASE TO 12 PIN PLLP. REQ. BY S. CHANG.	03 FEB 2005
В	CHANGED DIMS A3, K AND L. ADDED STYLE 1. REQ. BY S. CHANG.	28 APR 2005
С	ADDED SOLDERING FOOTPRINT. REQ. BY S. CHANG.	18 JUL 2005

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