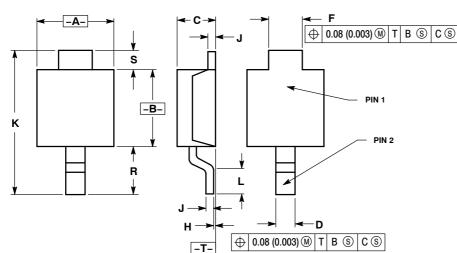


## **POWERMITE** CASE 457-04 ISSUE F

**DATE 14 MAY 2013** 



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: MILLIMETER.

  - DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.15 (0.006) PER SIDE.

	MILLIN	IETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	1.75	2.05	0.069	0.081
В	1.75	2.18	0.069	0.086
С	0.85	1.15	0.033	0.045
D	0.40	0.69	0.016	0.027
F	0.70	1.00	0.028	0.039
Н	-0.05	+0.10	-0.002	+0.004
J	0.10	0.25	0.004	0.010
K	3.60	3.90	0.142	0.154
L	0.50	0.80	0.020	0.031
R	1.20	1.50	0.047	0.059
S	0.50 REF		0.019 REF	

### **GENERIC MARKING DIAGRAMS\***

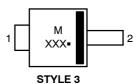
STYLE 1: PIN 1. CATHODE 2. ANODE

STYLE 2: PIN 1. ANODE OR CATHODE 2. CATHODE OR ANODE (BI-DIRECTIONAL) STYLE 3: PIN 1. ANODE 2. CATHODE

### Μ M 2 2 XXX. XXX.

# STYLE 1





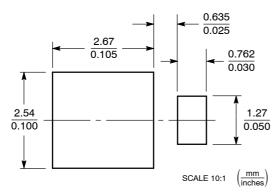
XXX = Specific Device Code

= Date Code

= Pb-Free Package

\*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DESCRIPTION:	POWERMITE		PAGE 1 OF 2
NEW STANDARD:		"CONTROLLED COPY" in red.	
STATUS:	ON SEMICONDUCTOR STANDARD	accessed directly from the Document versions are uncontrolled except	' '
DOCUMENT NUMBER:	98ASB14853C	Electronic versions are uncontrolle	



DOCUMENT	NUMBER:
98ASB14853	С

PAGE 2 OF 2

ISSUE	REVISION	DATE
Е	ADDED SOLDERING FOOTPRINT AND MARKING DIAGRAMS, STYLES 1, 2 & 3. REQ. BY D. TRUHITTE.	26 NOV 2008
F	MODIFED NOTE 3 FROM 'DIMENSION A' TO 'DIMENSIONS A AND B'. REQ. BY D. TRUHITTE.	14 MAY 2013

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