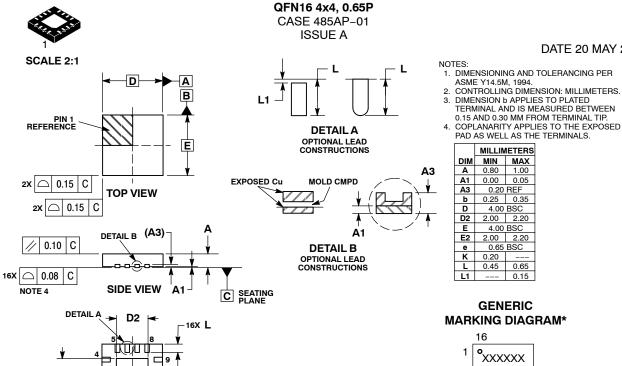
DATE 20 MAY 2009



MOUNTING FOOTPRINT*

е

BOTTOM VIEW

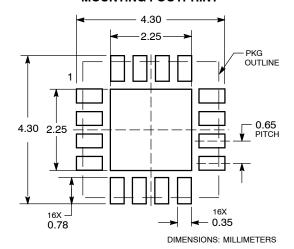
16X b

0.05

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0.10 C A B

C NOTE 3



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC



XXXX = Specific Device Code

= Assembly Location

= Wafer Lot L = Year = Work Week W = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G", may or not be present.

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