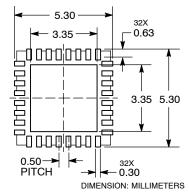


RECOMMENDED SOLDERING FOOTPRINT*

<u> 1 do o o¦o o o lo f</u>



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

DATE 23 OCT 2013

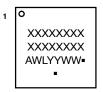
NOTES:

- NOTES:

 1. DIMENSIONS AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS.
 3. DIMENSION & APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.15 AND 0.30MM FROM THE TERMINAL TIP.
- COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

	MILLIMETERS		
DIM	MIN	MAX	
Α	0.80	1.00	
A1		0.05	
A3	0.20 REF		
b	0.18	0.30	
D	5.00 BSC		
D2	2.95	3.25	
E	5.00 BSC		
E2	2.95	3.25	
е	0.50 BSC		
K	0.20		
L	0.30	0.50	
L1		0.15	

GENERIC MARKING DIAGRAM*



XXXXX = Specific Device Code = Assembly Location

= Wafer Lot WL ΥY = Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

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PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. P. CELAYA	18 APR 2005
Α	ADDED ALTERNATE TERMINAL CONSTRUCTIONS. REQ. BY B. MARQUIS.	23 OCT 2013

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