

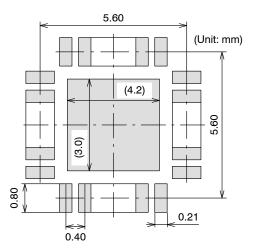
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ON Semiconductor

QFN44 6x6, 0.4P / VQFN44L CASE 485CL ISSUE A

DATE 05 DEC 2013

SOLDERING FOOTPRINT*



NOTES:

1. The measurements are not to guarantee but for reference only.

2. Module level verification after set designing must be implemented. Validating solderbility and Reliability verification for joint areas such as Exposed Die-pad and must be carried out.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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