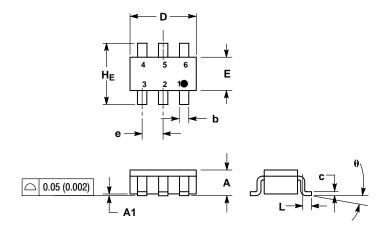
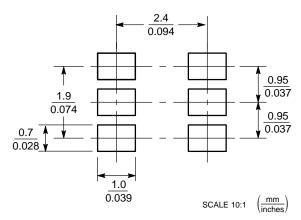


SC-74R CASE 318AA-01 **ISSUE B**

DATE 27 MAY 2005



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

> STYLE 20: PIN 1. COLLECTOR 1 STYLE 21: PIN 1. COLLECTOR 1 2. EMITTER 2 3. BASE 2 4. COLLECTOR 2 5. EMITTER 1 2. BASE 2 3. EMITTER 2 4. COLLECTOR 2 5. BASE 1 6. EMITTER 1 6. BASE 1

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI
- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.90	1.00	1.10	0.035	0.039	0.043
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.25	0.37	0.50	0.010	0.015	0.020
С	0.10	0.18	0.26	0.004	0.007	0.010
D	2.90	3.00	3.10	0.114	0.118	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
е	0.85	0.95	1.05	0.034	0.037	0.041
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.75	3.00	0.099	0.108	0.118
θ	0°	-	10°	0°	_	10°

GENERIC MARKING DIAGRAM*



XXX= Specific Device Code Μ = Date Code = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

DOCUMENT NUMBER:	98AON13505D
STATUS:	ON SEMICONDUCTOR STANDARD
NEW STANDARD:	
DESCRIPTION:	SC-74R

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DOCUMENT	NUMBER:
98AON13505	D

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY D. TRUHITTE.	30 SEP 2003
А	ADDED STYLE 21. REQ. BY D. BARLOW	03 NOV 2003
В	ADDED NOMINAL VALUES AND SOLDERING FOOTPRINT. REQ BY HONG XIAO	27 MAY 2005

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