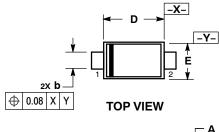
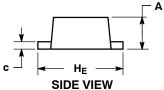


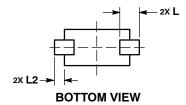
SOD-923 CASE 514AA-01 ISSUE E

DATE 09 MAR 2011

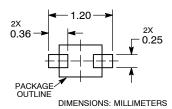
STYLE 1 STYLE 2 SCALE 8:1







SOLDERING FOOTPRINT*



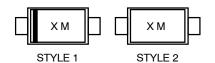
See Application Note AND8455/D for more mounting details

NOTES:

- DIMENSIONING AND TOLERANCING PER ASME
 V14 5M 1994
- Y14.5M, 1994. 2. CONTROLLING DIMENSION: MILLIMETERS.
- MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL
 DIMENSIONS D AND E DO NOT INCLUDE MOLD
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.

	MIL	LIMETE	ERS	INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.34	0.39	0.43	0.013	0.015	0.017
b	0.15	0.20	0.25	0.006	0.008	0.010
O	0.07	0.12	0.17	0.003	0.005	0.007
D	0.75	0.80	0.85	0.030	0.031	0.033
Е	0.55	0.60	0.65	0.022	0.024	0.026
HE	0.95	1.00	1.05	0.037	0.039	0.041
٦	0.19 REF			0.007 REF		
12	0.05	0.10	0.15	0.002	0.004	0.006

GENERIC MARKING DIAGRAM*



X = Specific Device Code M = Date Code

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1: STYLE 2: PIN 1. CATHODE (POLARITY BAND) NO POLARITY 2. ANODE

DOCUMENT NUMBER:	98AON21772D	
STATUS:	ON SEMICONDUCTOR STANDARD	ac ve
NEW STANDARD:		"(
DESCRIPTION:	SOD-923, 1.0X0.6X0.39, MAX HEIGHT (0.43

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PAGE 1 OF 2

^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.



DOCUMENT	NUMBER:
98AON21772	D

PAGE 2 OF 2

ISSUE	REVISION	DATE
0	RELEASED FOR PRODUCTION. REQ. BY D. TRUHITTE.	14 NOV 2005
Α	CHANGED DIMENSIONS C AND L. REQ. BY D. TRUHITTE.	18 NOV 2005
В	CHANGED DIMENSION TABLE VALUES. REQ. BY D. TRUHITTE.	03 APR 2006
С	CHANGED DIMENSION A. REQ. BY D. TRUHITTE.	30 NOV 2006
D	CREATED CATHODE AND NON-CATHODE BAND VERSIONS. REQ. BY J. DAUGHERTY.	07 MAR 2007
Е	ADDED BOTTOM VIEW AND DIMENSION L2. MODIFIED SOLDER FOOTRPINT. REQ. BY D. TRUHITTE.	09 MAR 2011

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